

MCT5201M, MCT5210M, MCT5211M

Low Input Current Phototransistor Optocouplers

Features

- High $CTR_{CE(SAT)}$ comparable to Darlington
- CTR guaranteed 0°C to 70°C
- High common mode transient rejection 5kV/μs
- Data rates up to 150kbits/s (NRZ)
- Underwriters Laboratory (UL) recognized, file #E90700, volume 2
- IEC60747-5-2 approved (ordering option V)

Applications

- CMOS to CMOS/LSTTL logic isolation
- LSTTL to CMOS/LSTTL logic isolation
- RS-232 line receiver
- Telephone ring detector
- AC line voltage sensing
- Switching power supply

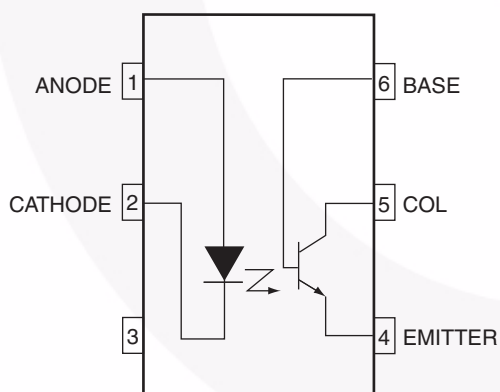
Description

The MCT52XXM series consists of a high-efficiency AlGaAs, infrared emitting diode, coupled with an NPN phototransistor in a six pin dual-in-line package.

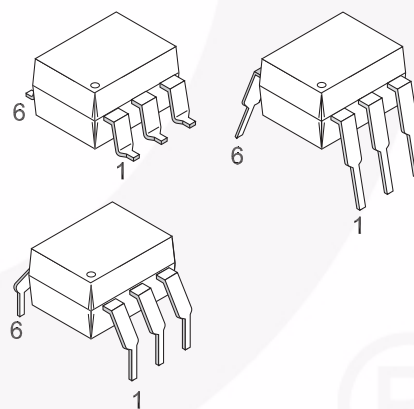
The MCT52XXM is well suited for CMOS to LSTTL/TTL interfaces, offering 250% $CTR_{CE(SAT)}$ with 1mA of LED input current. When an LED input current of 1.6mA is supplied data rates to 20K bits/s are possible.

The MCT52XXM can easily interface LSTTL to LSTTL/TTL, and with use of an external base to emitter resistor data rates of 100K bits/s can be achieved.

Schematic



Package Outlines



Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameters	Value	Units
TOTAL DEVICE			
T _{STG}	Storage Temperature	-55 to +150	°C
T _{OPR}	Operating Temperature	-40 to +100	°C
T _{SOL}	Lead Solder Temperature	260 for 10 sec	°C
P _D	Total Device Power Dissipation @ 25°C (LED plus detector) Derate Linearly From 25°C	260	mW
		3.5	mW/°C
EMITTER			
I _F	Continuous Forward Current	50	mA
V _R	Reverse Input Voltage	6	V
I _{F(pk)}	Forward Current - Peak (1 μs pulse, 300 pps)	3.0	A
P _D	LED Power Dissipation	75	mW
	Derate Linearly From 25°C	1.0	mW/°C
DETECTOR			
I _C	Continuous Collector Current	150	mA
P _D	Detector Power Dissipation Derate Linearly from 25°C	150	mW
		2.0	mW/°C

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise specified)**Individual Component Characteristics**

Symbol	Parameters	Test Conditions	Device	Min.	Typ.*	Max.	Units
EMITTER							
V_F	Input Forward Voltage	$I_F = 5\text{mA}$	All		1.25	1.5	V
$\frac{\Delta V_F}{\Delta T_A}$	Forward Voltage Temp. Coefficient	$I_F = 2\text{mA}$	All		-1.75		mV/°C
V_R	Reverse Voltage	$I_R = 10\mu\text{A}$	All	6			V
C_J	Junction Capacitance	$V_F = 0\text{V}$, $f = 1.0\text{MHz}$	All		18		pF
DETECTOR							
BV_{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 1.0\text{mA}$, $I_F = 0$	All	30	100		V
BV_{CBO}	Collector-Base Breakdown Voltage	$I_C = 10\mu\text{A}$, $I_F = 0$	All	30	120		V
BV_{EBO}	Emitter-Base Breakdown Voltage	$I_E = 10\mu\text{A}$, $I_F = 0$	All	5	10		V
I_{CER}	Collector-Emitter Dark Current	$V_{CE} = 10\text{V}$, $I_F = 0$, $R_{BE} = 1\text{M}\Omega$	All		1	100	nA
C_{CE}	Capacitance, Collector to Emitter	$V_{CE} = 0$, $f = 1\text{MHz}$	All		10		pF
C_{CB}	Capacitance, Collector to Base	$V_{CB} = 0$, $f = 1\text{MHz}$	All		80		pF
C_{EB}	Capacitance, Emitter to Base	$V_{EB} = 0$, $f = 1\text{MHz}$	All		15		pF

Isolation Characteristics

Symbol	Characteristic	Test Conditions	Device	Min.	Typ.*	Max.	Units
V_{ISO}	Input-Output Isolation Voltage ⁽¹⁰⁾	$f = 60\text{Hz}$, $t = 1\text{ sec.}$	All	7500			Vac(peak)
R_{ISO}	Isolation Resistance ⁽¹⁰⁾	$V_{I-O} = 500\text{ VDC}$, $T_A = 25^\circ\text{C}$	All	10^{11}			Ω
C_{ISO}	Isolation Capacitance ⁽⁹⁾	$V_{I-O} = 0$, $f = 1\text{ MHz}$	All		0.4	0.6	pF
CM_H	Common Mode Transient	$V_{CM} = 50\text{ V}_{P-P}$, $R_L = 750\Omega$, $I_F = 0$	MCT5210M/11M		5000		V/ μs
	Rejection – Output HIGH	$V_{CM} = 50\text{ V}_{P-P}$, $R_L = 1\text{K}\Omega$, $I_F = 0$	MCT5201M				
CM_L	Common Mode Transient	$V_{CM} = 50\text{ V}_{P-P}$, $R_L = 750\Omega$, $I_F = 1.6\text{mA}$	MCT5210M/11M		5000		V/ μs
	Rejection – Output LOW	$V_{CM} = 50\text{ V}_{P-P}$, $R_L = 1\text{K}\Omega$, $I_F = 5\text{mA}$	MCT5201M				

*All typical $T_A = 25^\circ\text{C}$

Electrical Characteristics (Continued) ($T_A = 25^\circ\text{C}$ unless otherwise specified)**Transfer Characteristics**

Symbol	Characteristics	Test Conditions		Device	Min.	Typ.*	Max.	Units
DC CHARACTERISTICS								
CTR _{CE(SAT)}	Saturated Current Transfer Ratio ⁽¹⁾ (Collector to Emitter)	I _F = 5mA, V _{CE} = 0.4V		MCT5201M	120			%
		I _F = 3.0mA, V _{CE} = 0.4V		MCT5210M	60			
		I _F = 1.6mA, V _{CE} = 0.4V		MCT5211M	100			
		I _F = 1.0mA, V _{CE} = 0.4V			75			
CTR _(CE)	Current Transfer Ratio (Collector to Emitter) ⁽¹⁾	I _F = 3.0mA, V _{CE} = 5.0V		MCT5210M	70			%
		I _F = 1.6mA, V _{CE} = 5.0V		MCT5211M	150			
		I _F = 1.0mA, V _{CE} = 5.0V			110			
CTR _(CB)	Current Transfer Ratio Collector to Base ⁽²⁾	I _F = 5mA, V _{CB} = 4.3V		MCT5201M	0.28			%
		I _F = 3.0mA, V _{CE} = 4.3V		MCT5210M	0.2			
		I _F = 1.6mA, V _{CE} = 4.3V		MCT5211M	0.3			
		I _F = 1.0mA, V _{CE} = 4.3V			0.25			
V _{CE(SAT)}	Saturation Voltage	I _F = 5mA, I _{CE} = 6mA		MCT5201M			0.4	V
		I _F = 3.0mA, I _{CE} = 1.8mA		MCT5210M			0.4	
		I _F = 1.6mA, I _{CE} = 1.6mA		MCT5211M			0.4	
AC CHARACTERISTICS								
T _{PHL}	Propagation Delay HIGH-to-LOW ⁽³⁾	R _L = 330 Ω, R _{BE} = ∞	I _F = 3.0mA, V _{CC} = 5.0V	MCT5210M		10		μs
		R _L = 3.3 kΩ, R _{BE} = 39kΩ				7		
		R _L = 750 Ω, R _{BE} = ∞	I _F = 1.6mA, V _{CC} = 5.0V	MCT5211M		14		
		R _L = 4.7 kΩ, R _{BE} = 91kΩ				15		
		R _L = 1.5 kΩ, R _{BE} = ∞				17		
		R _L = 10 kΩ, R _{BE} = 160kΩ				24		
		V _{CE} = 0.4V, V _{CC} = 5V, R _L = fig. 13, R _{BE} = 330kΩ	I _F = 5mA	MCT5201M		3	30	
T _{PLH}	Propagation Delay LOW-to-HIGH ⁽⁴⁾	R _L = 330 Ω, R _{BE} = ∞	I _F = 3.0mA, V _{CC} = 5.0V	MCT5210M		0.4		μs
		R _L = 3.3 kΩ, R _{BE} = 39kΩ				8		
		R _L = 750 Ω, R _{BE} = ∞	I _F = 1.6mA, V _{CC} = 5.0V	MCT5211M		2.5		
		R _L = 4.7 kΩ, R _{BE} = 91kΩ				11		
		R _L = 1.5 kΩ, R _{BE} = ∞				7		
		R _L = 10 kΩ, R _{BE} = 160kΩ				16		
		V _{CE} = 0.4V, V _{CC} = 5V, R _L = fig. 13, R _{BE} = 330kΩ	I _F = 5mA	MCT5201M		12	13	
t _d	Delay Time ⁽⁵⁾	V _{CE} = 0.4V, R _{BE} = 330kΩ, R _L = 1 kΩ, V _{CC} = 5V	I _F = 5mA	MCT5201M		1.1	15	μs
t _r	Rise Time ⁽⁶⁾	V _{CE} = 0.4V, R _{BE} = 330kΩ, R _L = 1 kΩ, V _{CC} = 5V	I _F = 5mA	MCT5201M		2.5	20	μs
t _s	Storage Time ⁽⁷⁾	V _{CE} = 0.4V, R _{BE} = 330 kΩ, R _L = 1 kΩ, V _{CC} = 5V	I _F = 5mA	MCT5201M		10	13	μs
t _f	Fall Time ⁽⁸⁾	V _{CE} = 0.4V, R _{BE} = 330 kΩ, R _L = 1 kΩ, V _{CC} = 5V	I _F = 5mA	MCT5201M		16	30	μs

*All typicals at $T_A = 25^\circ\text{C}$

Notes:

1. DC Current Transfer Ratio (CTR_{CE}) is defined as the transistor collector current (I_{CE}) divided by the input LED current (I_F) x 100%, at a specified voltage between the collector and emitter (V_{CE}).
2. The collector base Current Transfer Ratio (CTR_{CB}) is defined as the transistor collector base photocurrent (I_{CB}) divided by the input LED current (I_F) time 100%.
3. Referring to Figure 14 the T_{PHL} propagation delay is measured from the 50% point of the rising edge of the data input pulse to the 1.3V point on the falling edge of the output pulse.
4. Referring to Figure 14 the T_{PLH} propagation delay is measured from the 50% point of the falling edge of data input pulse to the 1.3V point on the rising edge of the output pulse.
5. Delay time (t_d) is measured from 50% of rising edge of LED current to 90% of V_o falling edge.
6. Rise time (t_r) is measured from 90% to 10% of V_o falling edge.
7. Storage time (t_s) is measured from 50% of falling edge of LED current to 10% of V_o rising edge.
8. Fall time (t_f) is measured from 10% to 90% of V_o rising edge.
9. C_{ISO} is the capacitance between the input (pins 1, 2, 3 connected) and the output, (pin 4, 5, 6 connected).
10. Device considered a two terminal device: Pins 1, 2, and 3 shorted together, and pins 5, 6 and 7 are shorted together.

Safety and Insulation Ratings

As per IEC 60747-5-2, this optocoupler is suitable for “safe electrical insulation” only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Symbol	Parameter	Min.	Typ.	Max.	Unit
	Installation Classifications per DIN VDE 0110/1.89 Table 1				
	For Rated Main Voltage < 150Vrms		I-IV		
	For Rated Main voltage < 300Vrms		I-IV		
	Climatic Classification		55/100/21		
	Pollution Degree (DIN VDE 0110/1.89)		2		
CTI	Comparative Tracking Index	175			
V_{PR}	Input to Output Test Voltage, Method b, $V_{IORM} \times 1.875 = V_{PR}$, 100% Production Test with $t_m = 1$ sec, Partial Discharge < 5pC	1594			V_{peak}
	Input to Output Test Voltage, Method a, $V_{IORM} \times 1.5 = V_{PR}$, Type and Sample Test with $t_m = 60$ sec, Partial Discharge < 5pC	1275			V_{peak}
V_{IORM}	Max. Working Insulation Voltage	850			V_{peak}
V_{IOTM}	Highest Allowable Over Voltage	6000			V_{peak}
	External Creepage	7			mm
	External Clearance	7			mm
	Insulation Thickness	0.5			mm
RIO	Insulation Resistance at T_s , $V_{IO} = 500V$	10^9			Ω

Typical Performance Curves

Fig. 1 LED Forward Voltage vs. Forward Current

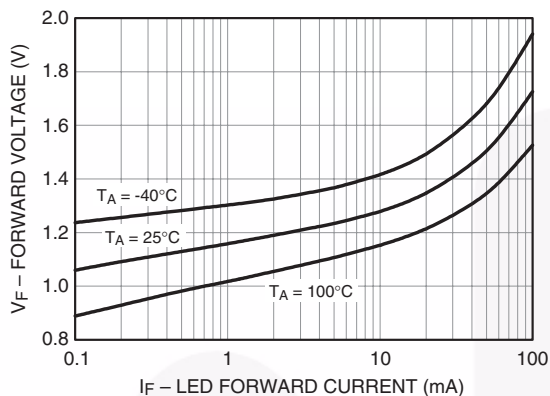


Fig. 2 Normalized Current Transfer Ratio vs. Forward Current

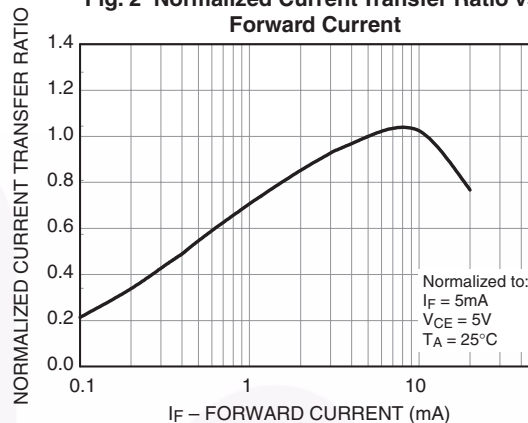


Fig. 3 Normalized CTR vs. Temperature

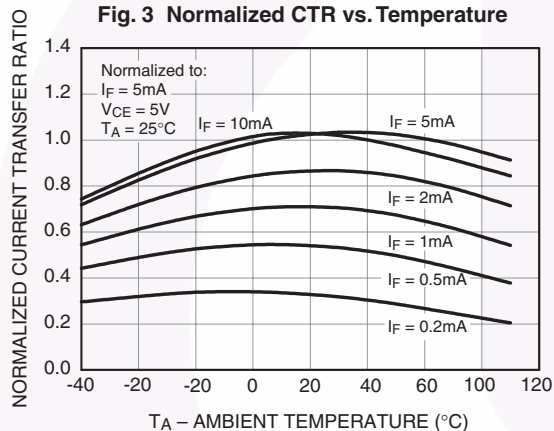


Fig. 4 Normalized Collector vs. Collector-Emitter Voltage

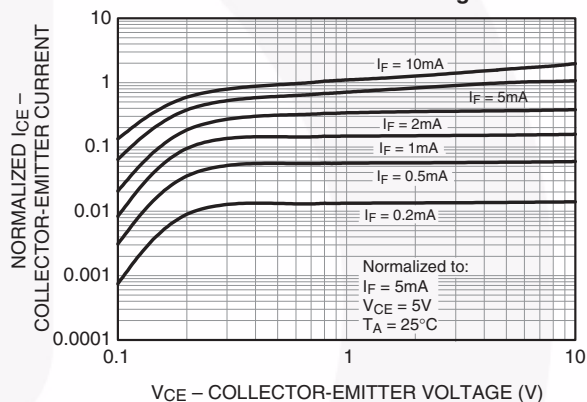


Fig. 5 Normalized Collector Base Photocurrent Ratio vs. Forward Current

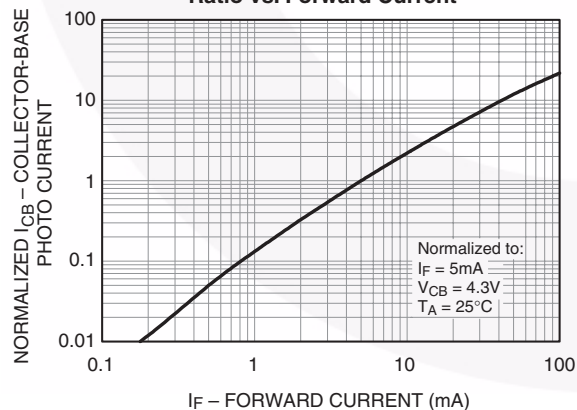
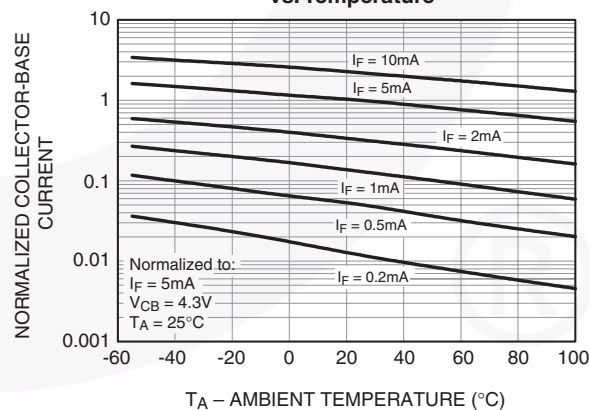


Fig. 6 Normalized Collector-Base Current vs. Temperature



Typical Performance Curves (Continued)

Fig. 7 Collector-Emitter Dark Current vs. Ambient Temperature

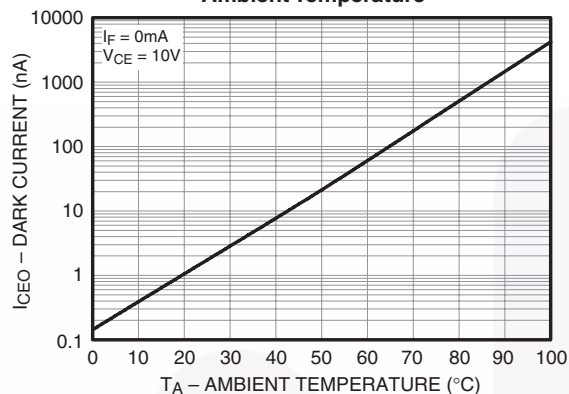


Fig. 8 Switching Time vs. Ambient Temperature

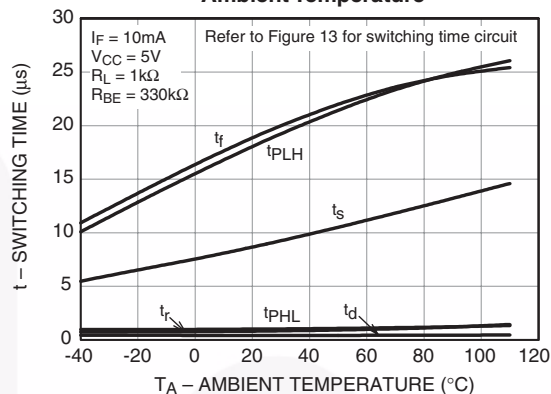


Fig. 9 Switching Time vs. Ambient Temperature

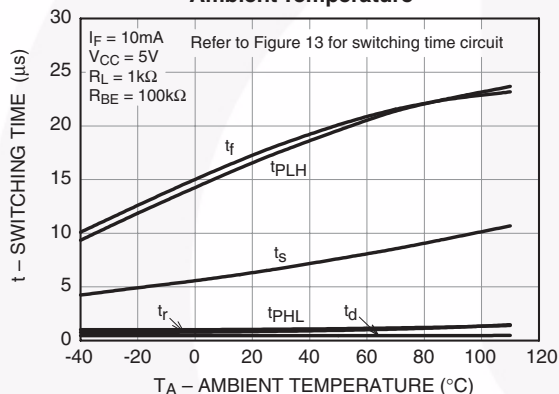


Fig. 10 Switching Time vs. Ambient Temperature

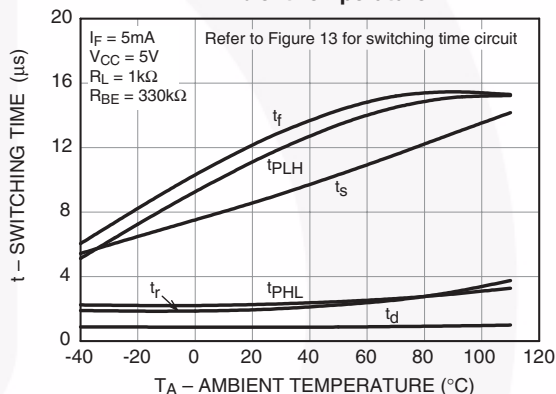


Fig. 11 Switching Time vs. Ambient Temperature

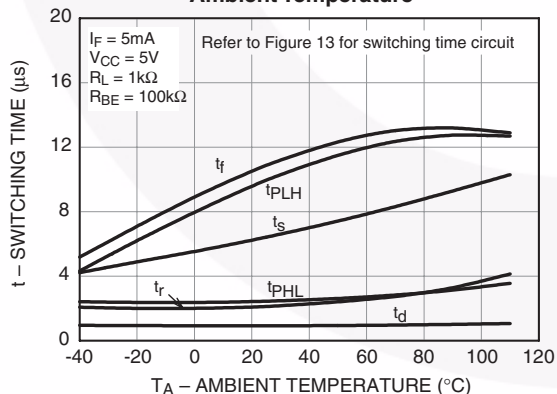
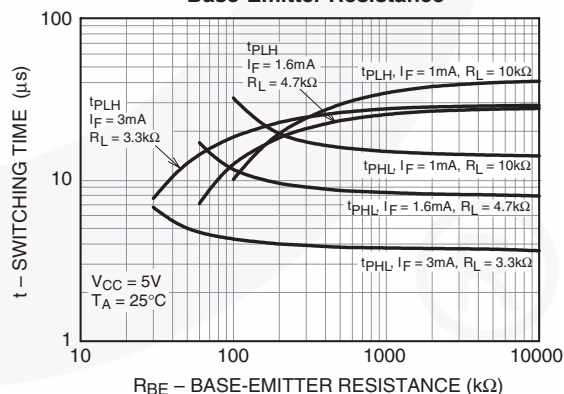


Fig. 12 Switching Time vs. Base-Emitter Resistance



Typical Electro-Optical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise specified)

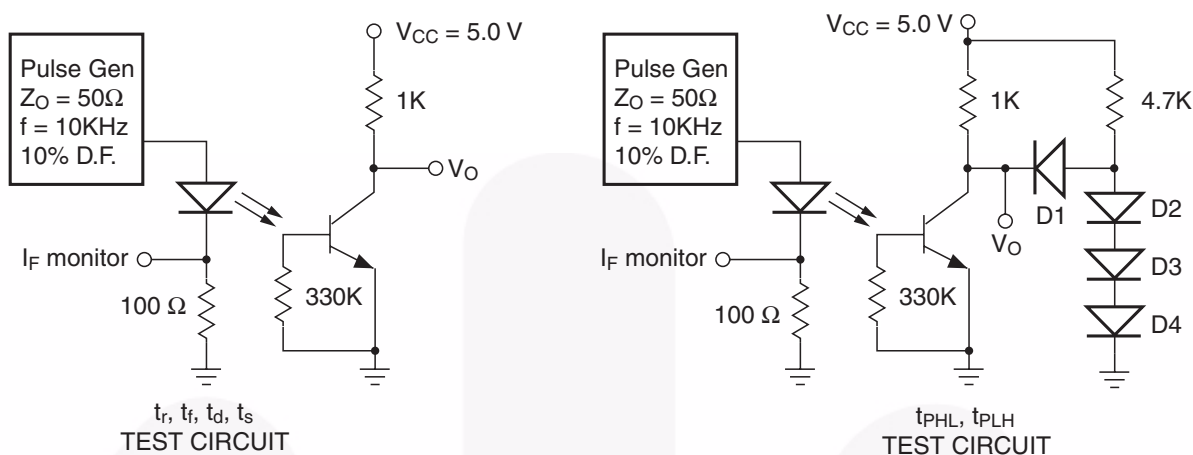


Figure 13.

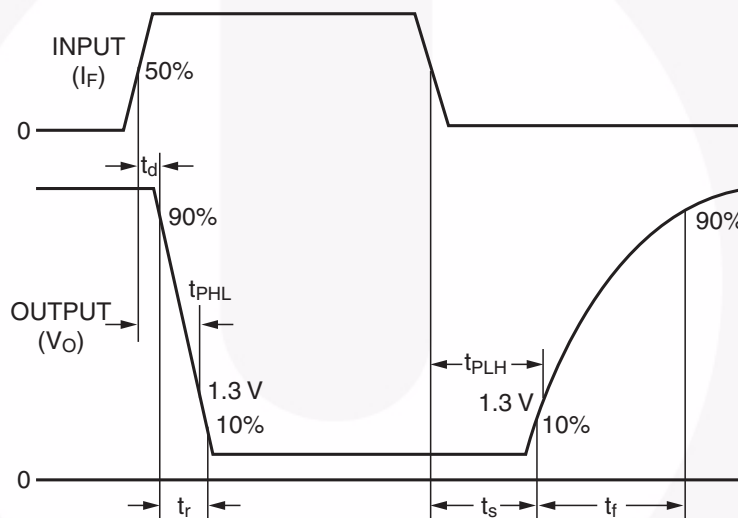
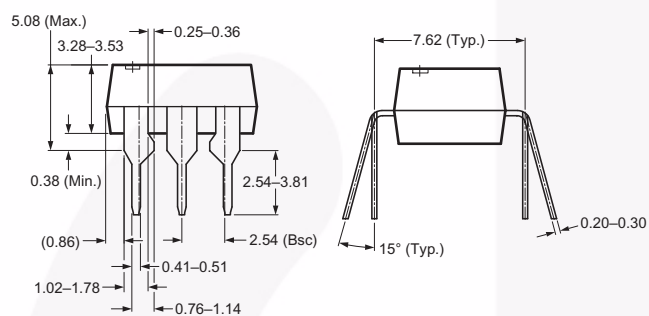
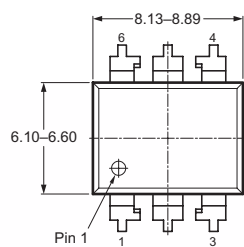


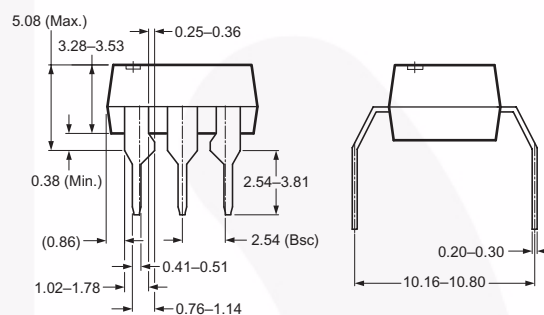
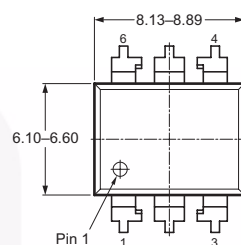
Figure 14. Switching Circuit Waveforms

Package Dimensions

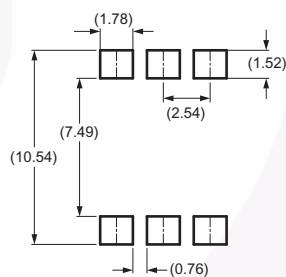
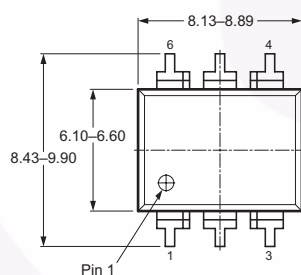
Through Hole



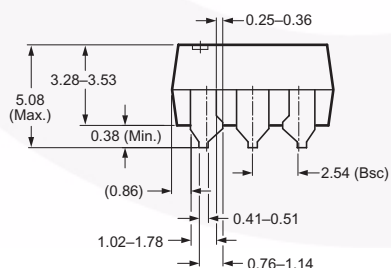
0.4" Lead Spacing



Surface Mount



Recommended Pad Layout

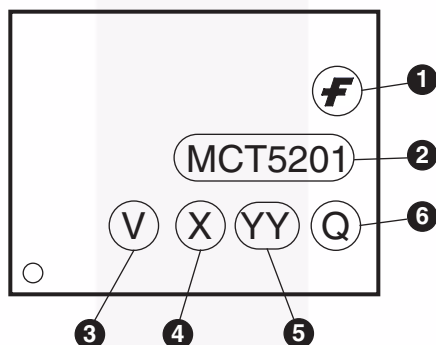


Note:
All dimensions in mm.

Ordering Information

Option	Order Entry Identifier (Example)	Description
No suffix	MCT5201M	Standard Through Hole Device (50 units per tube)
S	MCT5201SM	Surface Mount Lead Bend
SR2	MCT5201SR2M	Surface Mount; Tape and Reel (1,000 units per reel)
T	MCT5201TM	0.4" Lead Spacing
V	MCT5201VM	IEC60747-5-2
TV	MCT5201TVM	IEC60747-5-2, 0.4" Lead Spacing
SV	MCT5201SVM	IEC60747-5-2, Surface Mount
SR2V	MCT5201SR2VM	IEC60747-5-2, Surface Mount, Tape and Reel (1,000 units per reel)

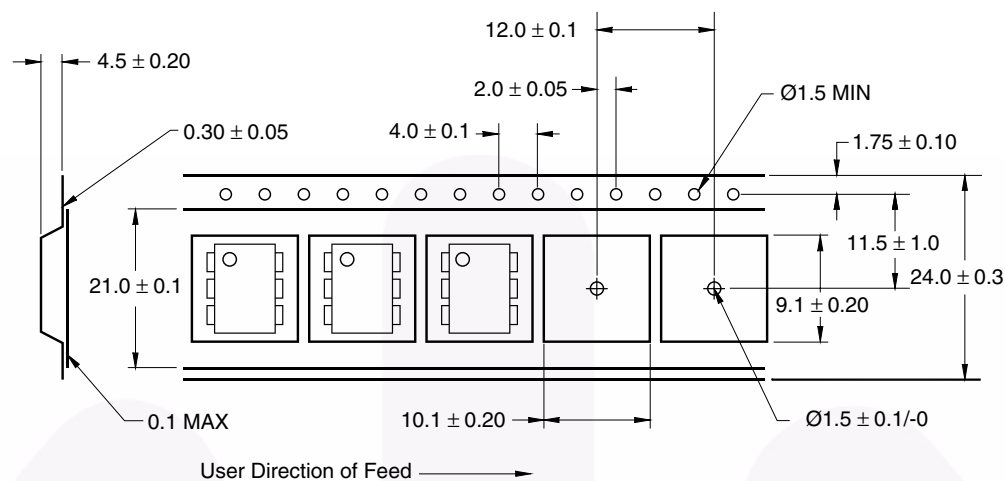
Marking Information



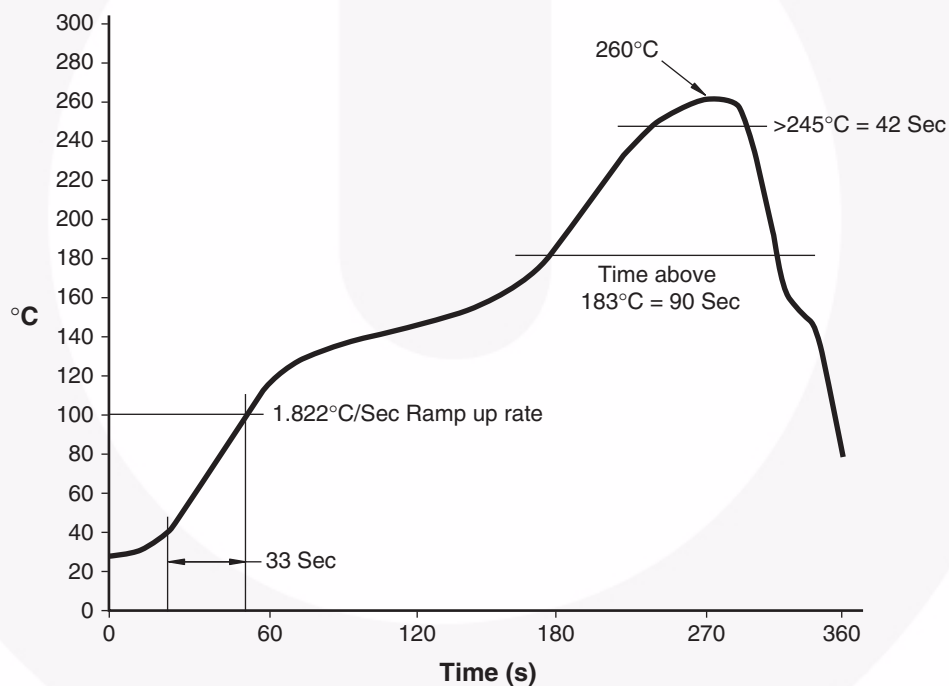
Definitions	
1	Fairchild logo
2	Device number
3	VDE mark (Note: Only appears on parts ordered with VDE option – See order entry table)
4	One digit year code, e.g., '7'
5	Two digit work week ranging from '01' to '53'
6	Assembly package code

*Note – Parts that do not have the 'V' option (see definition 3 above) that are marked with date code '325' or earlier are marked in portrait format.

Carrier Tape Specification



Reflow Profile




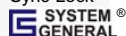


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FlashWriter®*
FPS™

F-PFS™
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2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
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