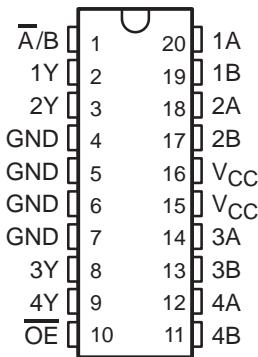


74ACT11257  
QUADRUPLE 2-LINE TO 1-LINE DATA SELECTOR/MUX  
WITH 3-STATE OUTPUTS

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- Inputs Are TTL-Voltage Compatible
- 3-State Outputs Interface Directly With System Bus
- Flow-Through Architecture Optimizes PCB Layout
- Center-Pin  $V_{CC}$  and GND Configurations Minimize High-Speed Switching Noise
- **EPIC™** (Enhanced-Performance Implanted CMOS) 1- $\mu$ m Process
- 500-mA Typical Latch-Up Immunity at 125°C
- Provides Bus Interface From Multiple Sources in High-Performance Systems
- Package Options Include Plastic Small-Outline (DW) and Shrink Small-Outline (DB) Packages, and Standard Plastic 300-mil DIPs (N)

**DB, DW, OR N PACKAGE  
(TOP VIEW)**



### description

The 74ACT11257 is designed to multiplex signals from 4-bit data sources to four output data lines in bus-organized systems. The 3-state outputs do not load the data lines when the output-enable ( $\overline{OE}$ ) input is at a high logic level.

The 74ACT11257 is characterized for operation from –40°C to 85°C.

FUNCTION TABLE

| $\overline{OE}$ | INPUTS        |      | OUTPUT<br>Y |
|-----------------|---------------|------|-------------|
|                 | SELECT<br>A/B | DATA |             |
|                 |               | A    | B           |
| H               | X             | X    | X           |
| L               | L             | L    | X           |
| L               | L             | H    | X           |
| L               | H             | X    | L           |
| L               | H             | X    | H           |



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**TEXAS  
INSTRUMENTS**

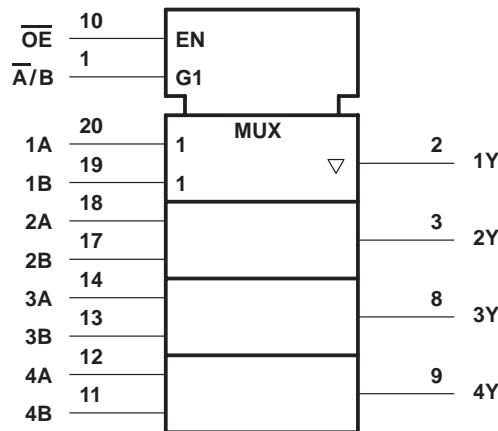
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74ACT11257

QUADRUPLE 2-LINE TO 1-LINE DATA SELECTOR/MULTIPLEXER  
WITH 3-STATE OUTPUTS

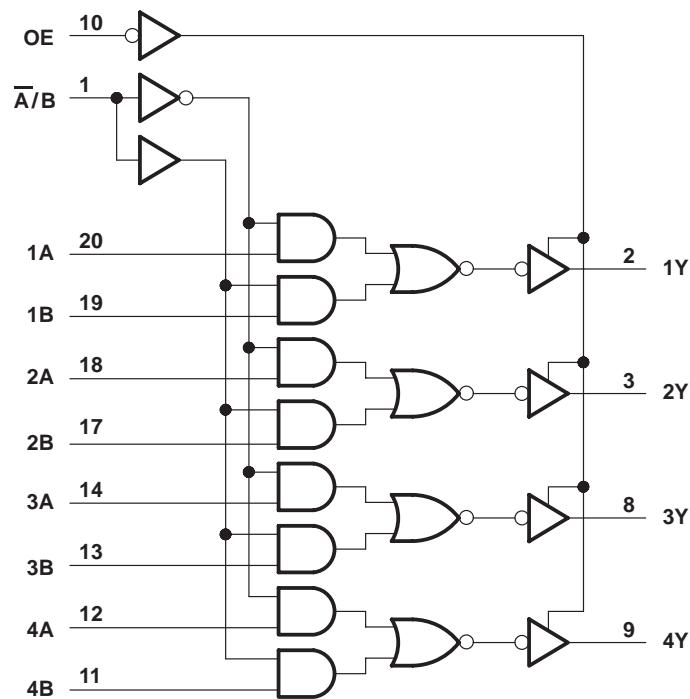
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logic symbol†



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

logic diagram (positive logic)



74ACT11257  
**QUADRUPLE 2-LINE TO 1-LINE DATA SELECTOR/MULTIPLEXER  
 WITH 3-STATE OUTPUTS**  
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**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†**

|  |                            |
|--|----------------------------|
| Supply voltage range, $V_{CC}$ .....   | –0.5 V to 7 V              |
| Input voltage range, $V_I$ (see Note 1) .....                                      | –0.5 V to $V_{CC}$ + 0.5 V |
| Output voltage range, $V_O$ (see Note 1) .....                                     | –0.5 V to $V_{CC}$ + 0.5 V |
| Input clamp current, $I_{IK}$ ( $V_I < 0$ or $V_I > V_{CC}$ ) .....                | ±20 mA                     |
| Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{CC}$ ) .....               | ±50 mA                     |
| Continuous output current, $I_O$ ( $V_O = 0$ to $V_{CC}$ ) .....                   | ±50 mA                     |
| Continuous current through $V_{CC}$ or GND .....                                   | ±100 mA                    |
| Maximum power dissipation at $T_A = 55^\circ\text{C}$ (in still air) (see Note 2): |                            |
| DB package .....   | 0.6 W                      |
| DW package .....   | 1.6 W                      |
| N package .....  | 1.3 W                      |
| Storage temperature range, $T_{stg}$ .....   | –65°C to 150°C             |

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.  
 2. The maximum package power dissipation is calculated using a junction temperature of 150°C and a board trace length of 750 mils, except for the N package, which has a trace length of zero.

**recommended operating conditions**

|                     |                                    | MIN | MAX      | UNIT |
|---------------------|------------------------------------|-----|----------|------|
| $V_{CC}$            | Supply voltage                     | 4.5 | 5.5      | V    |
| $V_{IH}$            | High-level input voltage           | 2   |          | V    |
| $V_{IL}$            | Low-level input voltage            |     | 0.8      | V    |
| $V_I$               | Input voltage                      | 0   | $V_{CC}$ | V    |
| $V_O$               | Output voltage                     | 0   | $V_{CC}$ | V    |
| $I_{OH}$            | High-level output current          |     | –24      | mA   |
| $I_{OL}$            | Low-level output current           |     | 24       | mA   |
| $\Delta t/\Delta v$ | Input transition rise or fall rate | 0   | 10       | ns/V |
| $T_A$               | Operating free-air temperature     | –40 | 85       | °C   |

74ACT11257

QUADRUPLE 2-LINE TO 1-LINE DATA SELECTOR/MUX  
WITH 3-STATE OUTPUTS

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETERS                    | TEST CONDITIONS   | V <sub>CC</sub> | T <sub>A</sub> = 25°C |      |      | MIN | MAX | UNIT |
|-------------------------------|---|-----------------|-----------------------|------|------|-----|-----|------|
|                               |   |                 | MIN                   | TYP  | MAX  |     |     |      |
| V <sub>OH</sub>               | I <sub>OH</sub> = -50 µA                                    | 4.5 V           | 4.4                   |      | 4.4  | V   |     |      |
|                               |   | 5.5 V           | 5.4                   |      | 5.4  |     |     |      |
|                               | I <sub>OH</sub> = -24 mA                                    | 4.5 V           | 3.94                  |      | 3.8  |     |     |      |
|                               |   | 5.5 V           | 4.94                  |      | 4.8  |     |     |      |
|                               | I <sub>OH</sub> = -75 mA <sup>†</sup>                       | 5.5 V           |                       |      | 3.85 |     |     |      |
|                               |   |                 |                       |      |      |     |     |      |
| V <sub>OL</sub>               | I <sub>OL</sub> = 50 µA                                     | 4.5 V           |                       | 0.1  | 0.1  | V   |     |      |
|                               |   | 5.5 V           |                       | 0.1  | 0.1  |     |     |      |
|                               | I <sub>OL</sub> = 24 mA                                     | 4.5 V           |                       | 0.36 | 0.44 |     |     |      |
|                               |   | 5.5 V           |                       | 0.36 | 0.44 |     |     |      |
|                               | I <sub>OL</sub> = 75 mA <sup>†</sup>                        | 5.5 V           |                       |      | 1.65 |     |     |      |
|                               |   |                 |                       |      |      |     |     |      |
| I <sub>OZ</sub>               | V <sub>O</sub> = V <sub>CC</sub> or GND                     | 5.5 V           |                       | ±0.5 | ±5   | µA  |     |      |
| I <sub>I</sub>                | V <sub>I</sub> = V <sub>CC</sub> or GND                     | 5.5 V           |                       | ±0.1 | ±1   | µA  |     |      |
| I <sub>CC</sub>               | V <sub>I</sub> = V <sub>CC</sub> or GND, I <sub>O</sub> = 0 | 5.5 V           |                       | 8    | 80   | µA  |     |      |
| ΔI <sub>CC</sub> <sup>‡</sup> | One input at 3.4 V, Other inputs at V <sub>CC</sub> or GND  | 5.5 V           |                       | 0.9  | 1    | mA  |     |      |
| C <sub>i</sub>                | V <sub>I</sub> = V <sub>CC</sub> or GND                     | 5 V             |                       | 3.5  |      | pF  |     |      |
| C <sub>o</sub>                | V <sub>O</sub> = V <sub>CC</sub> or GND                     | 5 V             |                       | 8    |      | pF  |     |      |

<sup>†</sup> Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.<sup>‡</sup> This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather than 0 V to V<sub>CC</sub>.

switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 1)

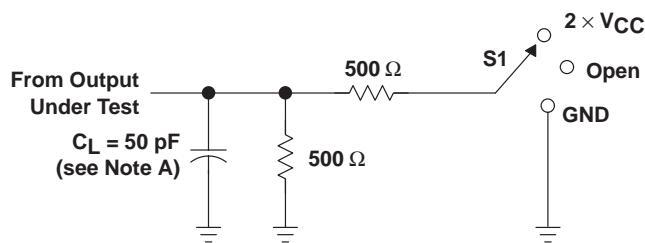
| PARAMETER        | FROM<br>(INPUT) | TO<br>(OUTPUT) | T <sub>A</sub> = 25°C |     |     | MIN | MAX | UNIT |
|------------------|-----------------|----------------|-----------------------|-----|-----|-----|-----|------|
|                  |                 |                | MIN                   | TYP | MAX |     |     |      |
| t <sub>PLH</sub> | A or B          | Y              | 1.5                   | 4.4 | 6.4 | 1.5 | 6.9 | ns   |
| t <sub>PHL</sub> |                 |                | 1.5                   | 5   | 8   | 1.5 | 8.7 |      |
| t <sub>PLH</sub> | A/B             | Any Y          | 1.5                   | 4.7 | 7.6 | 1.5 | 8.2 | ns   |
| t <sub>PHL</sub> |                 |                | 1.5                   | 5.7 | 8.5 | 1.5 | 9.4 |      |
| t <sub>PZH</sub> | OE              | Any Y          | 1.5                   | 4.2 | 6.9 | 1.5 | 7.3 | ns   |
| t <sub>PZL</sub> |                 |                | 1.5                   | 5.5 | 8.7 | 1.5 | 9.6 |      |
| t <sub>PHZ</sub> | OE              | Any Y          | 1.5                   | 5.7 | 7.6 | 1.5 | 8.4 | ns   |
| t <sub>PLZ</sub> |                 |                | 1.5                   | 6   | 7.9 | 1.5 | 8.5 |      |

operating characteristics, V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C

| PARAMETER                                     | TEST CONDITIONS                   | TYP      | UNIT |
|---|-----------------------------------|----------|------|
|   |                                   |          |      |
| C <sub>pd</sub> Power dissipation capacitance | C <sub>L</sub> = 50 pF, f = 1 MHz | 41<br>13 | pF   |

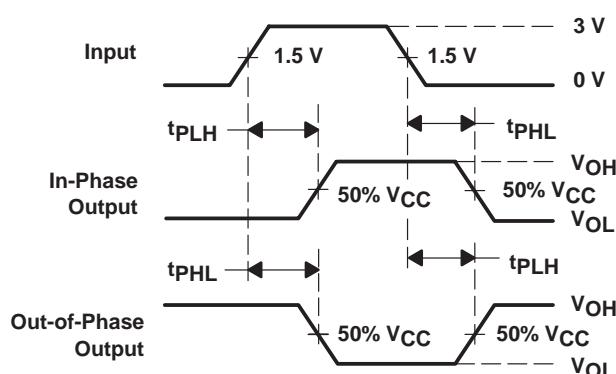
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QUADRUPLE 2-LINE TO 1-LINE DATA SELECTOR/MUX  
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**PARAMETER MEASUREMENT INFORMATION**

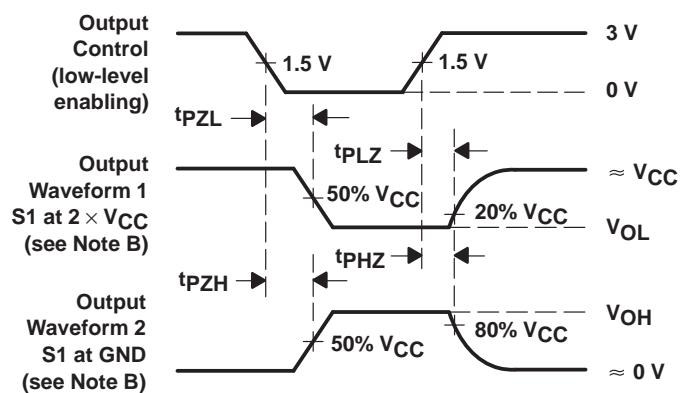


| TEST              | S1                |
|-------------------|-------------------|
| $t_{PLH}/t_{PHL}$ | Open              |
| $t_{PLZ}/t_{PZL}$ | $2 \times V_{CC}$ |
| $t_{PHZ}/t_{PZH}$ | GND               |

**LOAD CIRCUIT**



**VOLTAGE WAVEFORMS**



**VOLTAGE WAVEFORMS**

NOTES:

- A.  $C_L$  includes probe and jig capacitance.
- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: PRR  $\leq 1$  MHz,  $Z_O = 50 \Omega$ ,  $t_f = 3$  ns,  $t_f = 3$  ns.
- D. The outputs are measured one at a time with one input transition per measurement.

**Figure 1. Load Circuit and Voltage Waveforms**

**PACKAGING INFORMATION**

| Orderable Device | Status <sup>(1)</sup> | Package Type | Package Drawing | Pins | Package Qty | Eco Plan <sup>(2)</sup> | Lead/Ball Finish | MSL Peak Temp <sup>(3)</sup> |
|------------------|-----------------------|--------------|-----------------|------|-------------|-------------------------|------------------|------------------------------|
| 74ACT11257DBR    | ACTIVE                | SSOP         | DB              | 20   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ACT11257DBRE4  | ACTIVE                | SSOP         | DB              | 20   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ACT11257DW     | ACTIVE                | SOIC         | DW              | 20   | 25          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ACT11257DWE4   | ACTIVE                | SOIC         | DW              | 20   | 25          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ACT11257DWR    | ACTIVE                | SOIC         | DW              | 20   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ACT11257DWRE4  | ACTIVE                | SOIC         | DW              | 20   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ACT11257N      | ACTIVE                | PDIP         | N               | 20   | 20          | Pb-Free (RoHS)          | CU NIPDAU        | N / A for Pkg Type           |
| 74ACT11257NE4    | ACTIVE                | PDIP         | N               | 20   | 20          | Pb-Free (RoHS)          | CU NIPDAU        | N / A for Pkg Type           |
| 74ACT11257NSR    | ACTIVE                | SO           | NS              | 20   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ACT11257NSRE4  | ACTIVE                | SO           | NS              | 20   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ACT11257PW     | ACTIVE                | TSSOP        | PW              | 20   | 70          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ACT11257PWE4   | ACTIVE                | TSSOP        | PW              | 20   | 70          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ACT11257PWR    | ACTIVE                | TSSOP        | PW              | 20   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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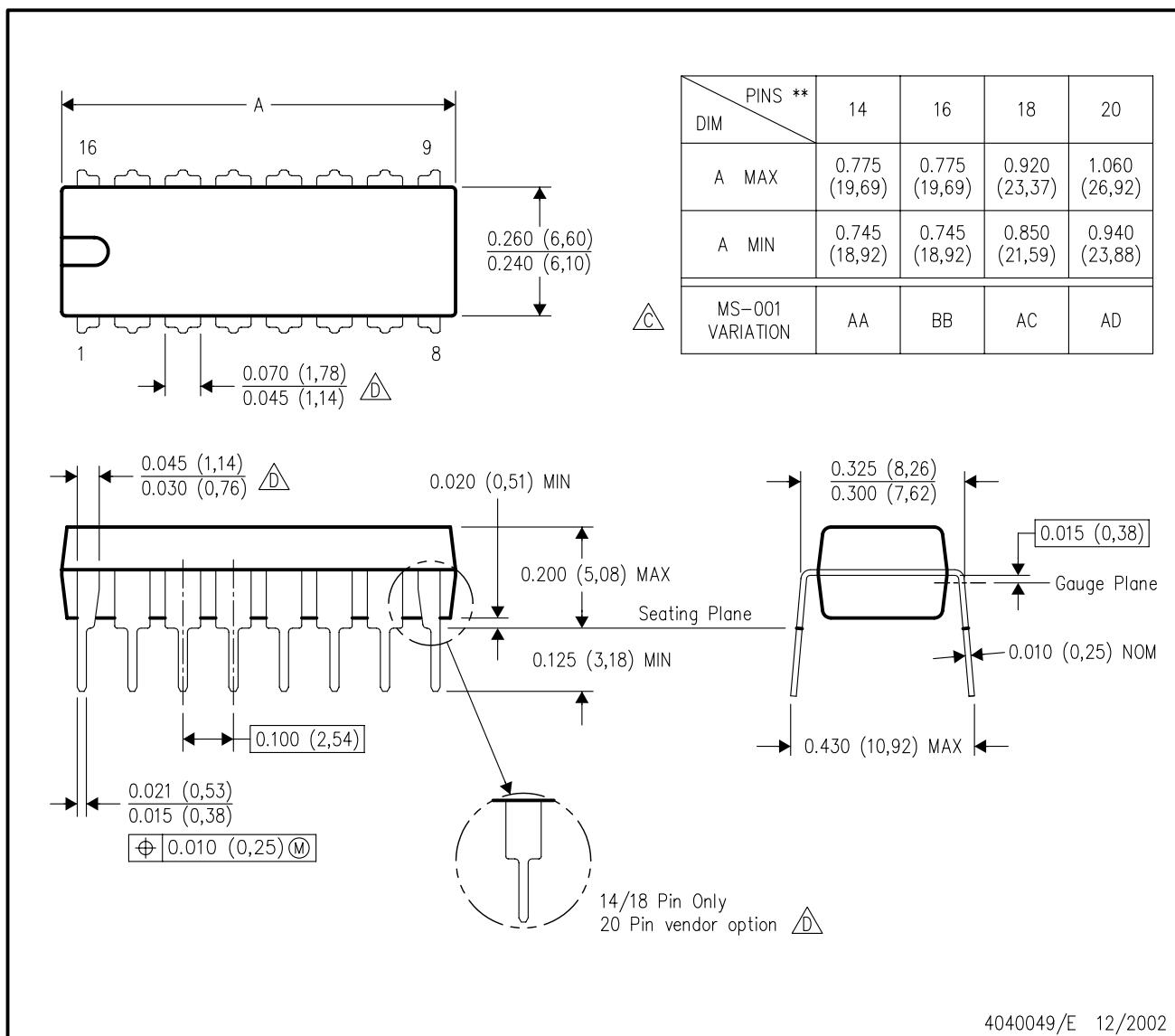
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## N (R-PDIP-T\*\*)

16 PINS SHOWN

## PLASTIC DUAL-IN-LINE PACKAGE



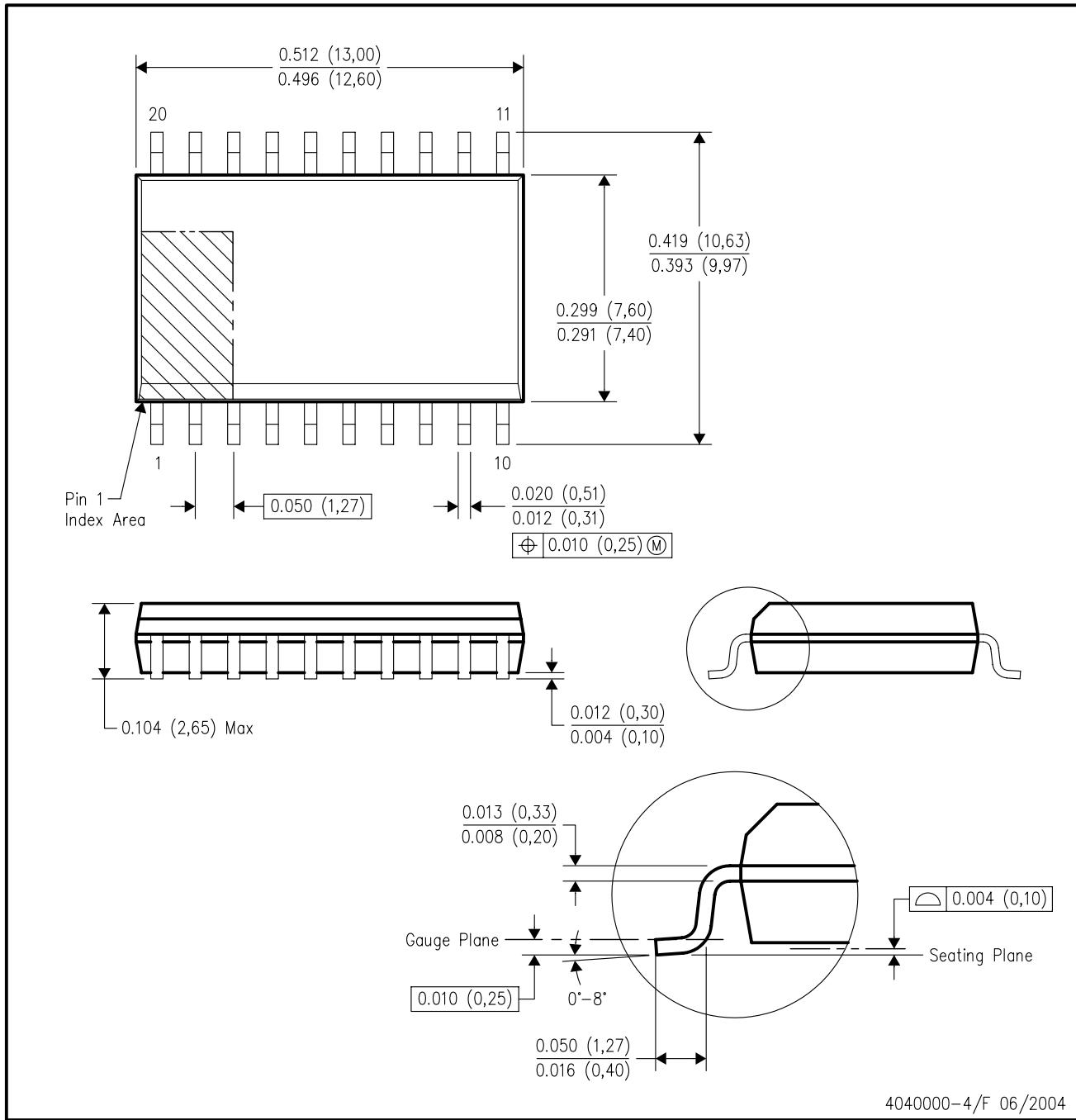
NOTES: A. All linear dimensions are in inches (millimeters).  
 B. This drawing is subject to change without notice.

△ Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).

△ The 20 pin end lead shoulder width is a vendor option, either half or full width.

## DW (R-PDSO-G20)

## PLASTIC SMALL-OUTLINE PACKAGE



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NOTES:

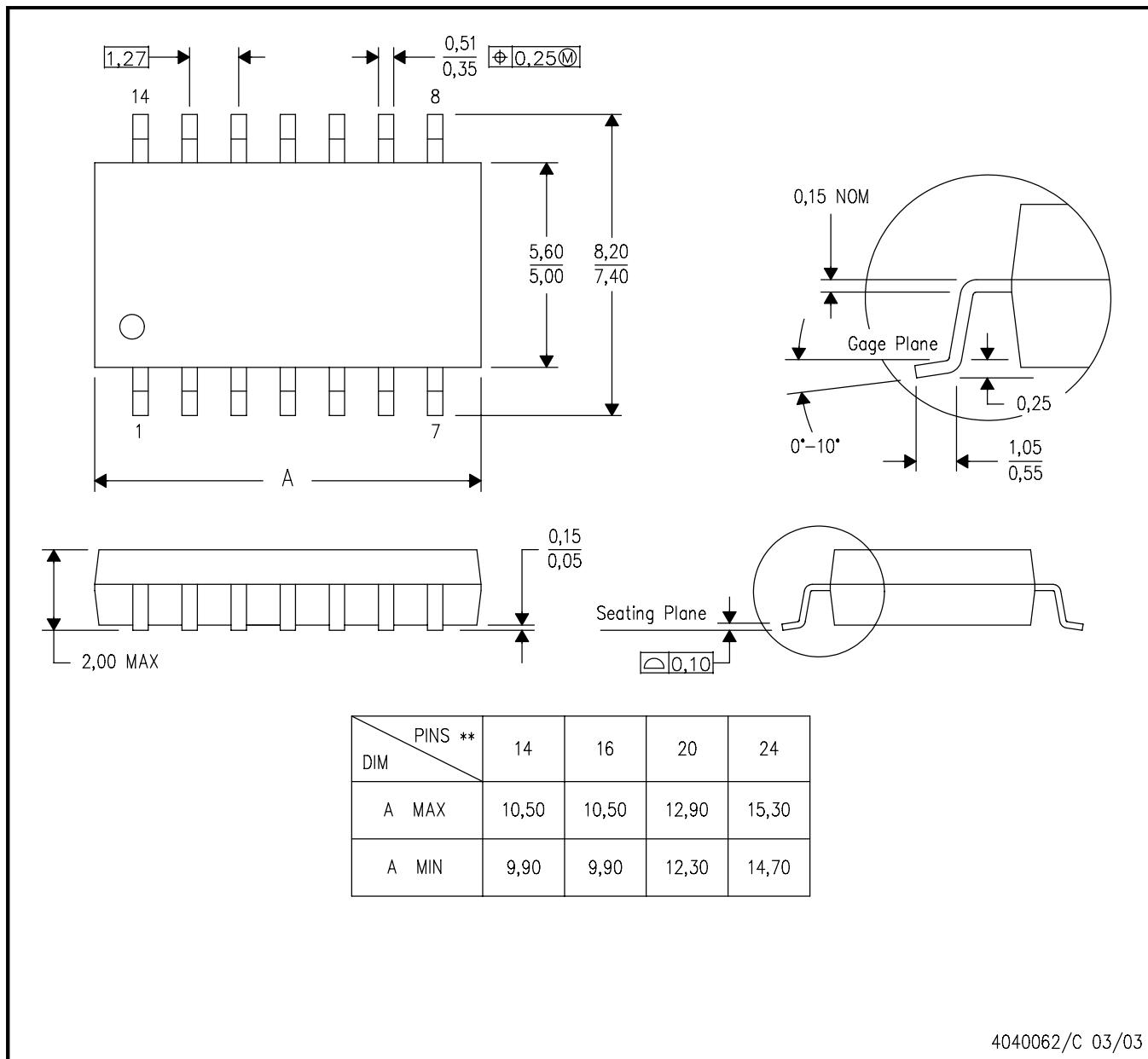
- All linear dimensions are in inches (millimeters).
- This drawing is subject to change without notice.
- Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
- Falls within JEDEC MS-013 variation AC.

## MECHANICAL DATA

**NS (R-PDSO-G\*\*)**

## PLASTIC SMALL-OUTLINE PACKAGE

**14-PINS SHOWN**

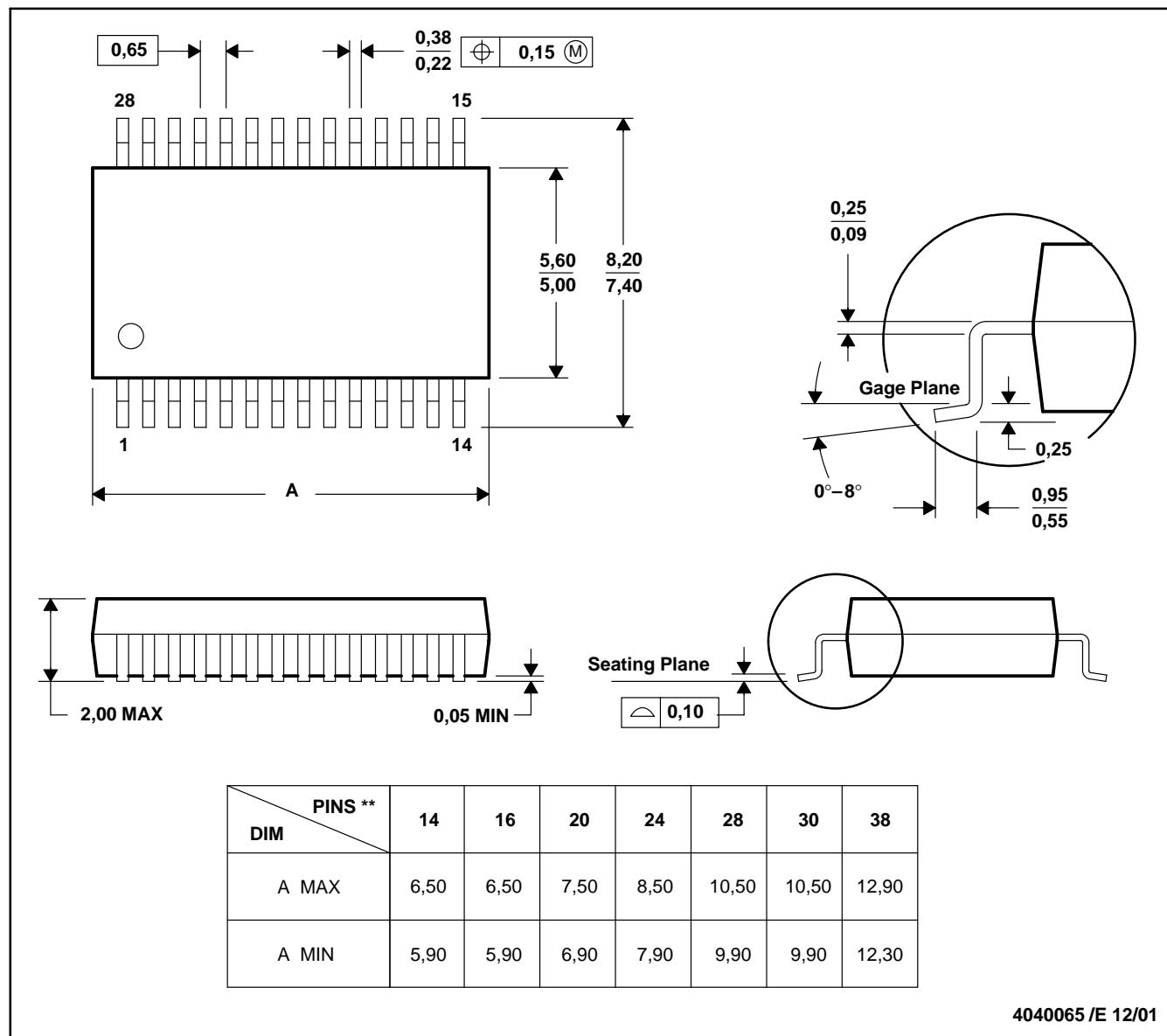


NOTES: A. All linear dimensions are in millimeters.  
B. This drawing is subject to change without notice.  
C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

## DB (R-PDSO-G\*\*)

## PLASTIC SMALL-OUTLINE

28 PINS SHOWN

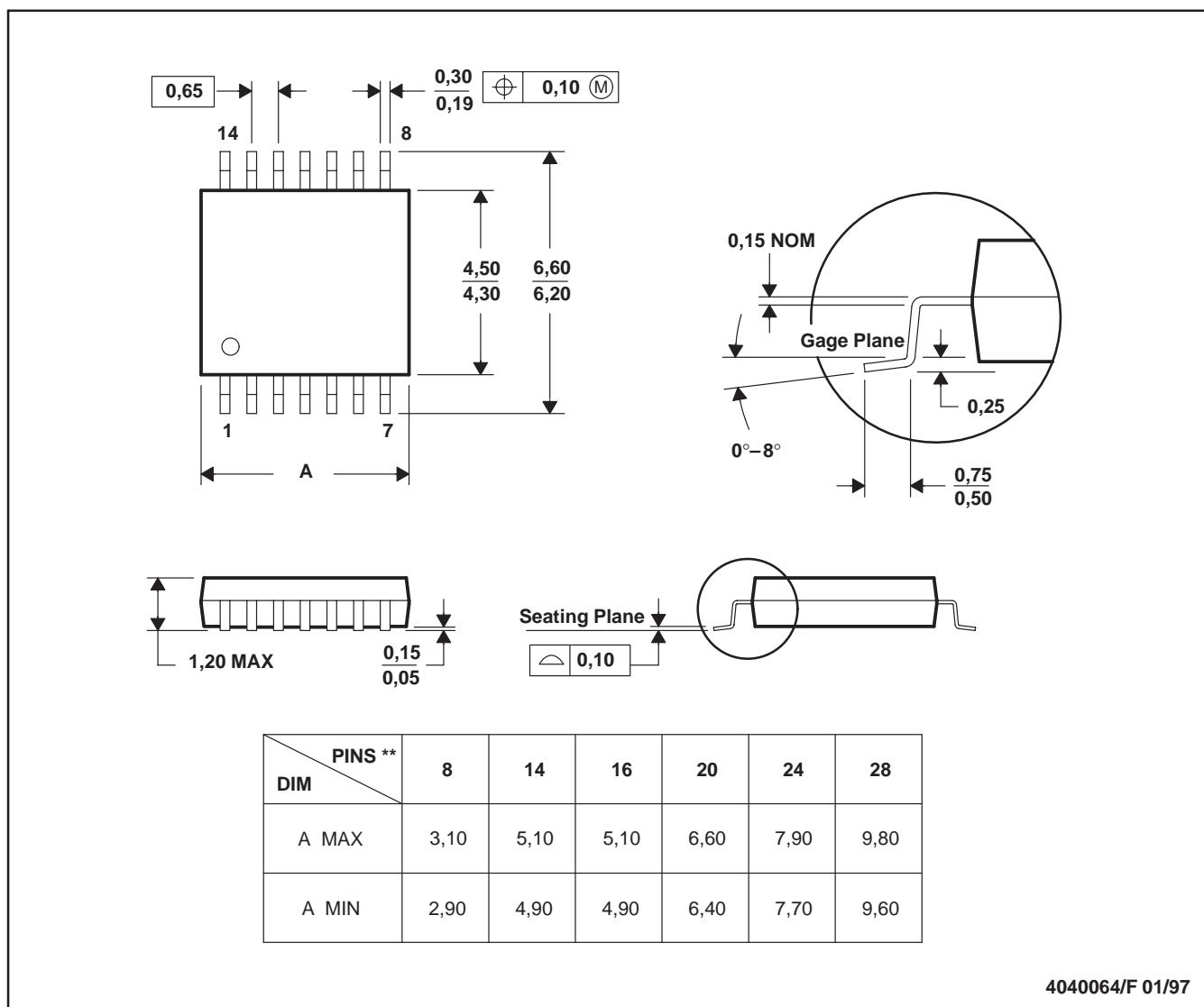


NOTES: A. All linear dimensions are in millimeters.  
 B. This drawing is subject to change without notice.  
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.  
 D. Falls within JEDEC MO-150

## PW (R-PDSO-G\*\*)

## PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



NOTES:

- All linear dimensions are in millimeters.
- This drawing is subject to change without notice.
- Body dimensions do not include mold flash or protrusion not to exceed 0,15.
- Falls within JEDEC MO-153

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