

DUAL LINE RECEIVER

DS7820/DS8820

DESCRIPTION

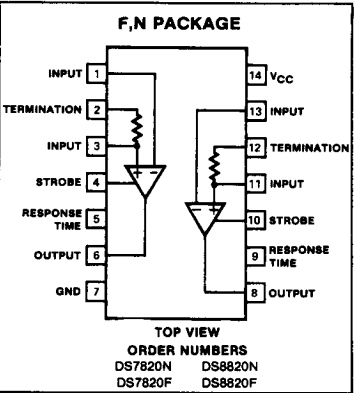
The DS7820, specified from -55°C to 125°C, and the DS8820, specified from 0°C to 70°C, are digital line receivers with two completely independent units fabricated on a single silicon chip. Intended for use with digital systems connected by twisted pair lines, they have a differential input designed to reject large common mode signals while responding to small differential signals. The output is directly compatible with RTL, DTL or TTL integrated circuits.

The response time can be controlled with an external capacitor to eliminate noise spikes, and the output state is determined for open inputs. Termination resistors for the twisted pair line are also included in the circuit. Both the DS7820 and the DS8820 are specified, worst case, over their full operating temperature range, for ±10-percent supply voltage variations and over the entire input voltage range.

FEATURES

- Operation from a single +5V logic supply
- Input voltage range of ±15V
- Independent channel strobing
- High input resistance
- Fanout of two with DTL or TTL
- Output can be wire OR'ed
- DS7820 MII std 883A,B,C available

PIN CONFIGURATION



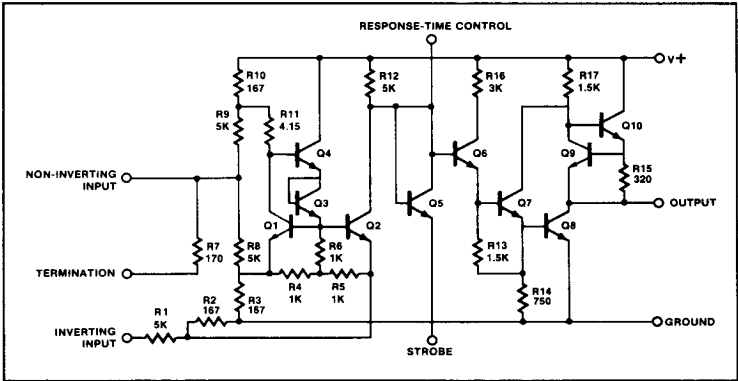
ABSOLUTE MAXIMUM RATINGS

PARAMETER	RATING	UNIT
Supply voltage	8.0	V
Input voltage	±20	V
Differential input voltage	±20	V
Strobe voltage	8.0	V
Output sink current	25	mA
Power dissipation	600	mW
Operating temperature range		
DS7820	-55 to +125	°C
DS8820	0 to 70	°C
Lead temperature (soldering, 10sec)	300	°C

NOTE

"Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range" they are not meant to imply devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

CIRCUIT SCHEMATIC



Signetics

DUAL LINE RECEIVER**DS7820/DS8820****DC ELECTRICAL CHARACTERISTICS**

Specifications apply for $4.5V \leq V_{CC} \leq 5.5$,
 $-15V \leq V_{CM} \leq 15V$ and $-55^\circ C \leq T_A \leq +125^\circ C$ for the DS7820
or $0^\circ C \leq T_A \leq +70^\circ C$ for the DS8820 unless otherwise specified.
Typical values given are for $V_{CC} = 5.0V$, $T_A = 25^\circ C$ and $V_{CM} = 0V$
unless stated differently.^{1,2,3}

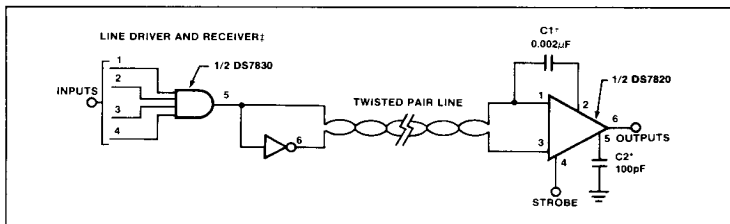
PARAMETER	TEST CONDITIONS	DS7820			DS8820			UNIT
		Min	Typ	Max	Min	Typ	Max	
V_{TH}	Input threshold	-0.5	0	0.5	-0.5	0	0.5	V
V_{TH}	Input threshold	-1.0	0	1.0	-1.0	0	1.0	V
V_{OH}	High output level	2.5		5.5	2.5		5.5	V
V_{OL}	Low output level	0		0.4	0		0.4	V
R_{IN-}	Inverting input resistance	3.6	5.0		3.6	5.0		k Ω
R_{IN+}	Noninverting input resistance	1.8	2.5		1.8	2.5		k Ω
R_T	Line termination resistance	120	170	250	120	170	250	Ω
I_{ST}	Strobe current		1.0	1.4		1.0	1.4	mA
I_{ST}	Strobe current			-5			-5	μA
I_{CC}	Supply current ³		3.2	6.0		3.2	6.0	mA
I_{CC}	Supply current ³		5.8	10.2		5.8	10.2	mA
I_{CC}	Supply current ³		8.3	15.0		8.3	15.0	mA
I_{IN+}	Noninverting input current		3.0	7.0		5.0	7.0	mA
I_{IN+}	Noninverting input current	-1.6	-1.0		-1.6	-1.0		mA
I_{IN+}	Noninverting input current	-9.8	-7.0		-9.8	-7.0		mA
I_{IN-}	Inverting input current		3.0	4.2		3.0	4.2	mA
I_{IN-}	Inverting input current		0	-0.5		0	-0.5	mA
I_{IN-}	Inverting input current	-4.2	-3.0		-4.2	-3.0		mA

NOTES

- All currents into device pins shown as positive, out of device pins as negative, all voltages referenced to ground unless otherwise noted. All values shown as max or min on absolute value basis.
- Only one output at a time should be shorted.
- The specifications and curves given are for one side only. Therefore, the total package dissipation and supply currents will be double the values given when both receivers are operated under identical conditions.

AC ELECTRICAL CHARACTERISTICS

PARAMETER	TEST CONDITIONS	DS7820			DS8820			UNIT
		Min	Typ	Max	Min	Typ	Max	
T_R	Response time		40			40		ns
T_R	Response time		150			150		ns

DS7820-DS8820 TYPICAL APPLICATION

[†]Exact value depends on line length

[‡] V_{CC} is 4.5V to 5.5V for both the DS7820 and DS7830

^{*}Optional to control response time