Silicon NPN Power Transistor

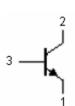




Application:

For medium power linear and switching applications

Fig. 1 Simplified Outline (TO-126) and Symbol



Pinning

Pin	Description	
1	Emitter	
2	Collector; connected to mounting base	
3	Base	

Absolute Maximum Ratings $(T_a = 25^{\circ}C)$

Symbol	Parameter	Conditions	Value	Unit
V_{CBO}	Collector - base voltage	Open emitter	45	V
V _{CEO}	Collector - emitter voltage	Open base	45	V
V _{EBO}	Emitter - base voltage	Open collector	5	V
I _C	Collector current (DC)	-	4	Α
I _{CM}	Collector current - peak	-	7	Α
Ι _Β	Base current		1	Α
P _C	Collector power dissipation	T _C = 25°C	36	W
T _j	Junction temperature	-	150	°C
T _{stg}	Storage temperature	-	-65 to 150	°C

Characteristics (T_j = 25°C Unless Otherwise Specified)

Symbol	Parameter	Conditions	Minimum	Typical	Maximum	Unit
V _{CEsat}	Collector - emitter saturation voltage	I _C = 2 A; I _B = 0.2 A	-	0.2	0.6	V
V _{BE}	Base - emitter on voltage	I _C = 2 A; V _{CE} = 1 V	-	-	1.2	V
V _{CEO (SUS)}	Collector - emitter sustaining voltage	I _C = 0.1 A; I _B = 0	45	-	-	V
I _{CES}	Collector cut-off current	V _{CB} = 45 V; I _E = 0	-	-	100	mA
I _{CES}	Collector cut-off current	$V_{CE} = 45 \text{ V}; V_{BE} = 0$			100	mA
I _{EBO}	Emitter cut-off current	V _{EB} = 5 V; I _C = 0	-	-	1	mA
h _{FE-1}	DC current gain	I _C = 10 mA; V _{CE} = 5 V	30	-	130	-

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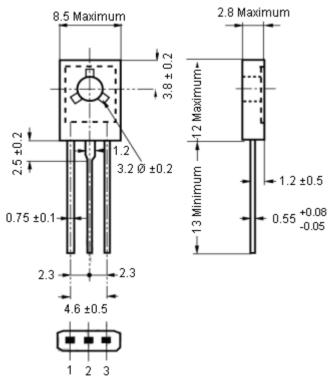
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Characteristics (T_i = 25°C Unless Otherwise Specified)

Symbol	Parameter	Conditions	Minimum	Typical	Maximum	Unit
h _{FE-2}	DC current gain	I _C = 0.5 A; V _{CE} = 1 V	85	-	140	-
h _{FE-3}	DC current gain	I _C = 2 A; V _{CE} = 1 V	40	-	-	-
f _T	Transition frequency	I _C = 250 mA; V _{CE} = 1 V	3	-	-	MHz

Package Outline



Dimensions : Millimetres

Fig. 2 Outline Dimensions

Part Number Table

Description	Part Number		
Silicon NPN Power Transistor	BD437		

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