# PMEG1020EH; PMEG1020EJ 10 V, 2 A ultra low V<sub>F</sub> MEGA Schottky barrier rectifiers Rev. 04 — 15 January 2010 Product da

Product data sheet

#### **Product profile** 1.

### 1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifiers with an integrated guard ring for stress protection, encapsulated in small and flat lead plastic SMD packages.

Table 1. **Product overview** 

Type number	Package	Package	
	NXP	JEITA	
PMEG1020EH	SOD123F	-	single diode
PMEG1020EJ	SOD323F	SC-90	single diode

### 1.2 Features

Forward current: ≤ 2 A

■ Reverse voltage: ≤ 10 V

Ultra low forward voltage

Small and flat lead SMD plastic packages

### 1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Inverse polarity protection
- Low power consumption applications

### 1.4 Quick reference data

Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>F</sub>	forward current	$T_{sp} \leq 55 ^{\circ}C$	-	-	2	Α
V <sub>R</sub>	reverse voltage		-	-	10	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 2 A	<u>[1]</u> -	350	460	mV

[1] Pulse test:  $t_p \le 300 \mu s$ ;  $\delta \le 0.02$ .



# 2. Pinning information

Table 3. Pinning

14510 01	9	
Pin	Description	Simplified outline Symbol
1	cathode	[1]
2	anode	1 2 2 sym001 001aab540

<sup>[1]</sup> The marking bar indicates the cathode.

# 3. Ordering information

Table 4. Ordering information

Type number	Package		
	Name	Description	Version
PMEG1020EH	-	plastic surface mounted package; 2 leads	SOD123F
PMEG1020EJ	SC-90	plastic surface mounted package; 2 leads	SOD323F

# 4. Marking

Table 5. Marking codes

Type number	Marking code
PMEG1020EH	A8
PMEG1020EJ	СВ

# 5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_R$	reverse voltage		-	10	V
I <sub>F</sub>	forward current	T <sub>sp</sub> ≤ 55 °C	-	2	Α
$I_{FRM}$	repetitive peak forward current	$t_p \leq 1 \text{ ms; } \delta \leq 0.5$	-	7	Α
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; t <sub>p</sub> = 8 ms	-	9	Α
$P_{tot}$	total power dissipation	$T_{amb} \le 25  ^{\circ}C$			
	PMEG1020EH		<u>[1]</u> -	375	mW
			[2] _	830	mW
	PMEG1020EJ		<u>[1]</u>	360	mW
			[2] -	830	mW
Tj	junction temperature		-	150	°C

Table 6. Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$T_{amb}$	ambient temperature		-65	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

### 6. Thermal characteristics

Table 7. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air					
	PMEG1020EH		[1][2]	-	-	330	K/W
			[2][3]	-	-	150	K/W
	PMEG1020EJ		[1][2]	-	-	350	K/W
			[2][3]	-	-	150	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		<u>[4]</u>				
	PMEG1020EH			-	-	60	K/W
	PMEG1020EJ			-	-	55	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

### 7. Characteristics

Table 8. Characteristics

 $T_{amb} = 25$  °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{F}$	forward voltage	$I_F = 0.01 A$	<u>[1]</u> -	100	130	mV
		I <sub>F</sub> = 0.1 A	<u>[1]</u> _	170	200	mV
		I <sub>F</sub> = 1 A	<u>[1]</u> _	280	350	mV
		I <sub>F</sub> = 2 A	<u>[1]</u> _	350	460	mV
$I_R$	reverse current	$V_R = 5 V$	-	0.7	2	mA
		V <sub>R</sub> = 8 V	-	1	2.5	mA
		V <sub>R</sub> = 10 V	-	1.2	3	mA
$C_{d}$	diode capacitance	$V_R = 5 V$ ; $f = 1 MHz$	-	40	50	pF

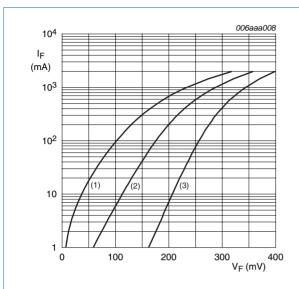
<sup>[1]</sup> Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

<sup>[2]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

<sup>[2]</sup> For Schottky barrier rectifiers thermal run-away has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses. Nomograms for determining the reverse power losses P<sub>R</sub> and I<sub>F(AV)</sub> rating are available on request.

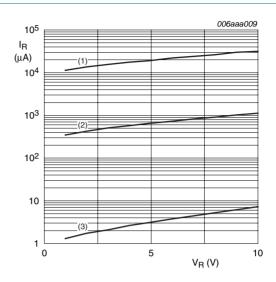
<sup>[3]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

<sup>[4]</sup> Soldering point of cathode tab.



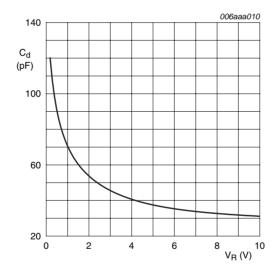
- (1)  $T_{amb} = 85 \, ^{\circ}C$
- (2)  $T_{amb} = 25 \, ^{\circ}C$
- (3)  $T_{amb} = -40 \, ^{\circ}C$

Fig 1. Forward current as a function of forward voltage; typical values



- (1)  $T_{amb} = 85 \, ^{\circ}C$
- (2)  $T_{amb} = 25 \, ^{\circ}C$
- (3)  $T_{amb} = -40 \, ^{\circ}C$

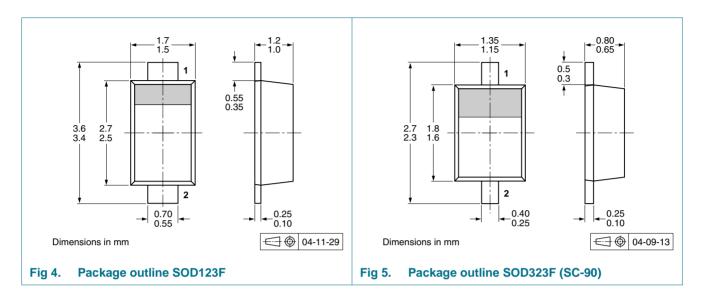
Fig 2. Reverse current as a function of reverse voltage; typical values



 $T_{amb} = 25 \, ^{\circ}C; f = 1 \, MHz$ 

Fig 3. Diode capacitance as a function of reverse voltage; typical values

# 8. Package outline



# 9. Packing information

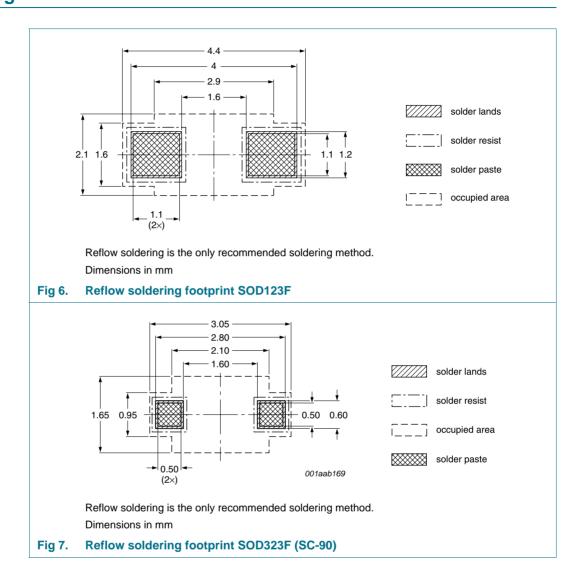
Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing (	Packing quantity	
			3000	10000	
PMEG1020EH	SOD123F	4 mm pitch, 8 mm tape and reel	-115	-135	
PMEG1020EJ	SOD323F				

[1] For further information and the availability of packing methods, see Section 13.

### 10. Soldering



# PMEG1020EH; PMEG1020EJ

10 V, 2 A ultra low V<sub>F</sub> MEGA Schottky barrier rectifiers

# 11. Revision history

### Table 10. Revision history

	-			
Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG1020EH_EJ_4	20100115	Product data sheet	-	PMEG1020EH_EJ_3
Modifications:		eet was changed to reflect to be legal definitions and disclar.		
PMEG1020EH_EJ_3	20050414	Product data sheet	-	PMEG1020EJ_2; PMEG1020EH_1
PMEG1020EJ_2	20041001	Product data sheet	-	PMEG1020EJ_1
PMEG1020EH_1	20050203	Objective data sheet	-	-

# PMEG1020EH; PMEG1020EJ

10 V, 2 A ultra low V<sub>F</sub> MEGA Schottky barrier rectifiers

### 12. Legal information

### 12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <a href="http://www.nxp.com">http://www.nxp.com</a>.

### 12.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

#### 12.3 Disclaimers

**General** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental

damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <a href="http://www.nxp.com/profile/terms">http://www.nxp.com/profile/terms</a>, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

### 12.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

8 of 9

### 13. Contact information

For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: <a href="mailto:salesaddresses@nxp.com">salesaddresses@nxp.com</a>

### **NXP Semiconductors**

# PMEG1020EH; PMEG1020EJ

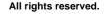
10 V, 2 A ultra low V<sub>F</sub> MEGA Schottky barrier rectifiers

### 14. Contents

1	Product profile	1
1.1	General description	1
1.2	Features	
1.3	Applications	1
1.4	Quick reference data	1
2	Pinning information	2
3	Ordering information	2
4	Marking	2
5	Limiting values	2
6	Thermal characteristics	3
7	Characteristics	3
8	Package outline	5
9	Packing information	5
10	Soldering	6
11	Revision history	7
12	Legal information	8
12.1	Data sheet status	8
12.2	Definitions	8
12.3	Disclaimers	8
12.4	Trademarks	8
13	Contact information	8
14	Contents	9

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.





Date of release: 15 January 2010

Document identifier: PMEG1020EH\_EJ\_4