

# EV32C6B5A1-4.032M TR [Click part number to visit Part Number Details page](#)

## REGULATORY COMPLIANCE (Data Sheet downloaded on Nov 10, 2017)



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## ITEM DESCRIPTION

Voltage Controlled Quartz Crystal Clock Oscillators VCXO LVC MOS/TTL (CMOS) 3.3Vdc 6 Pad 5.0mm x 7.0mm Ceramic Surface Mount (SMD) 4.032MHz  $\pm 50$ ppm Maximum  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$   $\pm 100$ ppm Minimum 10% Typical, 20% Maximum

## ELECTRICAL SPECIFICATIONS

Nominal Frequency	4.032MHz
Frequency Tolerance/Stability	$\pm 50$ ppm Maximum (Inclusive of all conditions: Calibration Tolerance at $25^{\circ}\text{C}$ , Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, Shock, and Vibration.)
Aging at $25^{\circ}\text{C}$	$\pm 2$ ppm/First Year Typical, $\pm 10$ ppm/10 Years Maximum
Operating Temperature Range	$-40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$
Supply Voltage	3.3Vdc $\pm 10\%$
Input Current	15mA Maximum
Output Voltage Logic High (Voh)	90% of Vdd Minimum (IOH = -4mA)
Output Voltage Logic Low (Vol)	10% of Vdd Maximum (IOL = +4mA)
Rise/Fall Time	5nSec Maximum (Measured at 20% to 80% of Waveform)
Duty Cycle	50 $\pm 10$ (%) (Measured at 50% of Waveform)
Load Drive Capability	10TTL Load or 30pF LVC MOS Load Maximum
Output Logic Type	CMOS
Absolute Pull Range	$\pm 100$ ppm Minimum (Inclusive of all conditions: Calibration Tolerance at $25^{\circ}\text{C}$ , Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, Shock, Vibration, and Aging over the Control Voltage (Vc).)
Control Voltage	0.3Vdc to 3.0Vdc (Test Condition for APR)
Control Voltage Range	0.0Vdc to Vdd
Linearity	10% Typical, 20% Maximum
Transfer Function	Positive Transfer Characteristic
Modulation Bandwidth	10kHz Minimum (Measured at -3dB, Vc = 1.65Vdc)
Input Impedance	50kOhms Minimum
Input Leakage Current	10 $\mu$ A Maximum
Phase Noise	All Values are Typical -70dBc/Hz at offset of 10Hz -100dBc/Hz at offset of 100Hz -130dBc/Hz at offset of 1kHz -147dBc/Hz at offset of 10kHz -152dBc/Hz at offset of 100kHz -155dBc/Hz at offset of 1MHz
Tri-State Input Voltage (Vih and Vil)	90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)
RMS Phase Jitter	1pSec Maximum (Fj = 12kHz to 20MHz; Random)
Start Up Time	10mSec Maximum
Storage Temperature Range	$-55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$

## ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

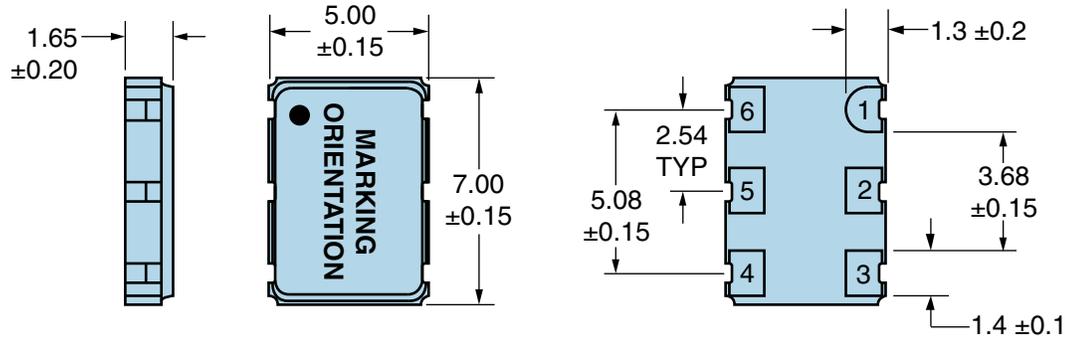
ESD Susceptibility	MIL-STD-883, Method 3015, Class 1, HBM: 1500V
Fine Leak Test	MIL-STD-883, Method 1014, Condition A
Flammability	UL94-V0
Gross Leak Test	MIL-STD-883, Method 1014, Condition C
Mechanical Shock	MIL-STD-883, Method 2002, Condition B

**EV32C6B5A1-4.032M TR** [Click part number to visit Part Number Details page](#)**ENVIRONMENTAL & MECHANICAL SPECIFICATIONS CONTINUED**

<b>Moisture Resistance</b>	MIL-STD-883, Method 1004
<b>Moisture Sensitivity</b>	J-STD-020, MSL 1
<b>Resistance to Soldering Heat</b>	MIL-STD-202, Method 210, Condition K
<b>Resistance to Solvents</b>	MIL-STD-202, Method 215
<b>Solderability</b>	MIL-STD-883, Method 2003
<b>Temperature Cycling</b>	MIL-STD-883, Method 1010, Condition B
<b>Vibration</b>	MIL-STD-883, Method 2007, Condition A

## EV32C6B5A1-4.032M TR [Click part number to visit Part Number Details page](#)

### MECHANICAL DIMENSIONS (all dimensions in millimeters)

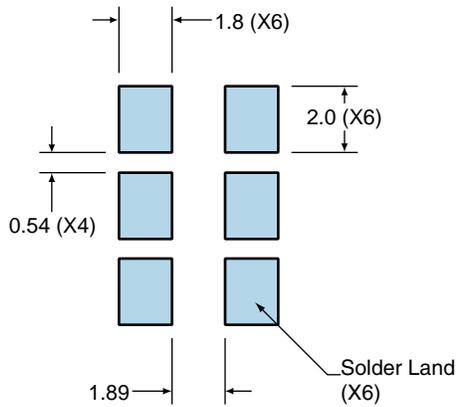


PIN	CONNECTION
1	Control Voltage
2	Tri-State
3	Case Ground
4	Output
5	No Connect
6	Supply Voltage

LINE	MARKING
1	ECLIPTEK
2	4.0320M
3	XXXXX XXXXX=Ecliptek Manufacturing Identifier

### Suggested Solder Pad Layout

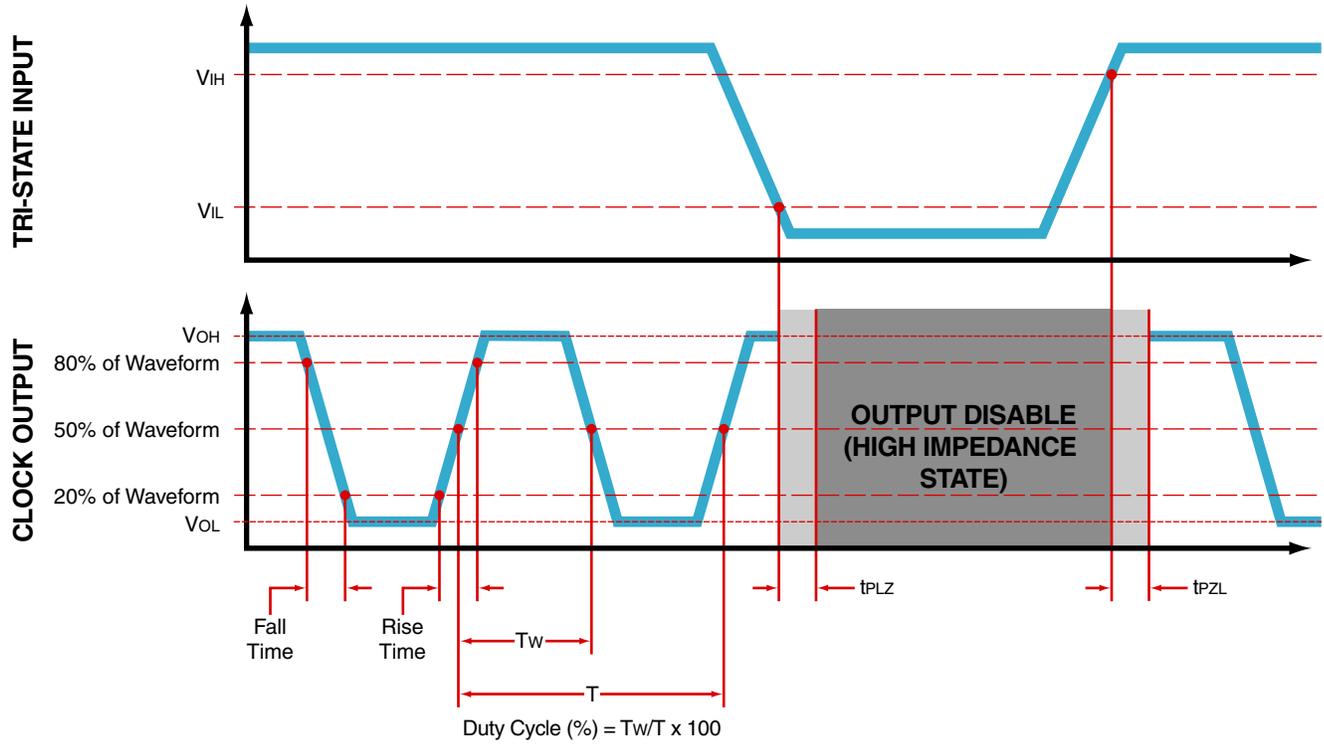
All Dimensions in Millimeters



All Tolerances are  $\pm 0.1$

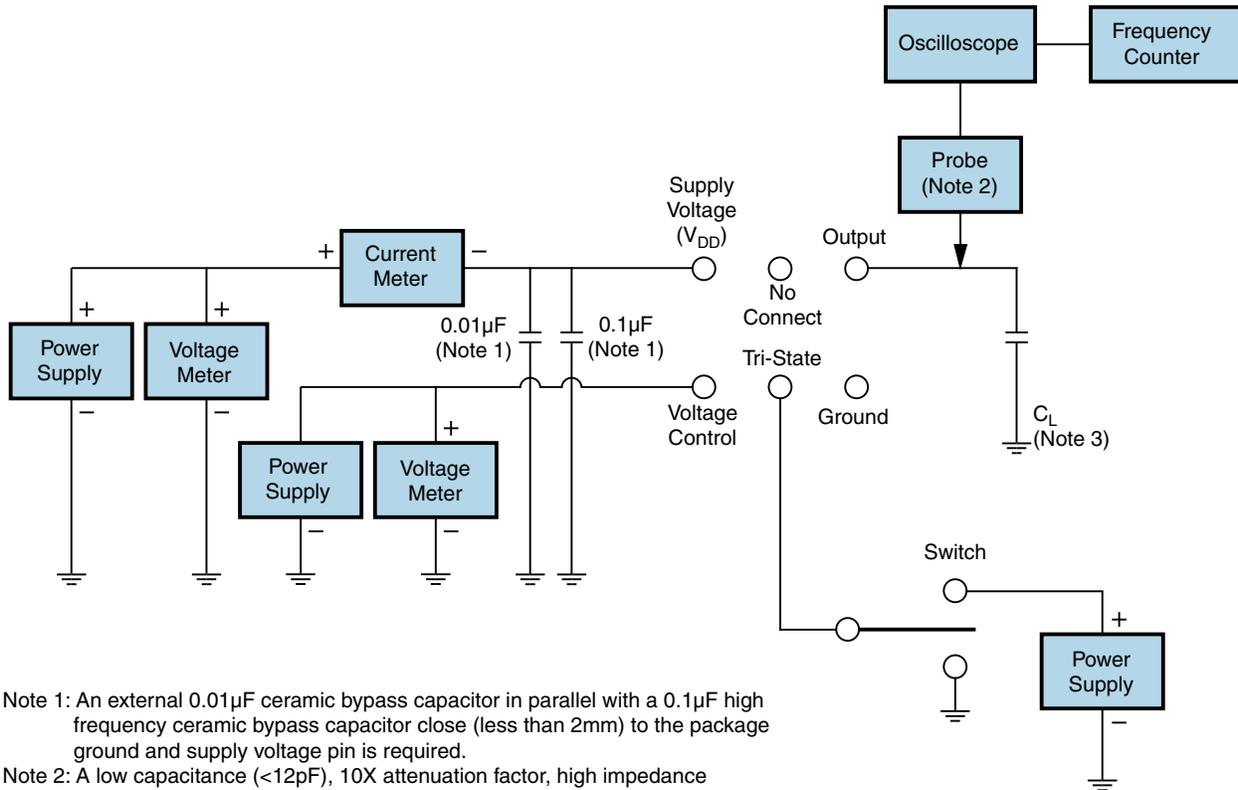
# EV32C6B5A1-4.032M TR [Click part number to visit Part Number Details page](#)

## OUTPUT WAVEFORM & TIMING DIAGRAM



# EV32C6B5A1-4.032M TR [Click part number to visit Part Number Details page](#)

## Test Circuit for CMOS Output



Note 1: An external  $0.01\mu\text{F}$  ceramic bypass capacitor in parallel with a  $0.1\mu\text{F}$  high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance ( $<12\text{pF}$ ), 10X attenuation factor, high impedance ( $>10\text{Mohms}$ ), and high bandwidth ( $>300\text{MHz}$ ) passive probe is recommended.

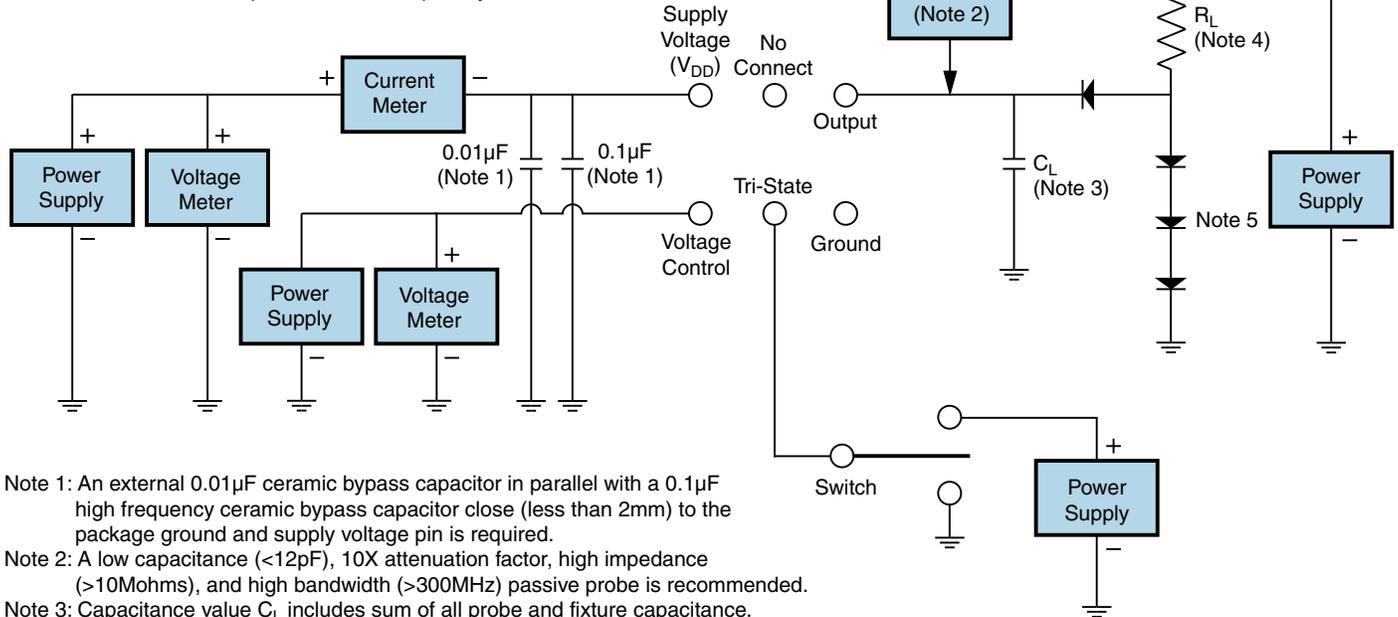
Note 3: Capacitance value  $C_L$  includes sum of all probe and fixture capacitance.

# EV32C6B5A1-4.032M TR [Click part number to visit Part Number Details page](#)

## Test Circuit for TTL Output

Output Load Drive Capability	$R_L$ Value (Ohms)	$C_L$ Value (pF)
10TTL	390	15

Table 1:  $R_L$  Resistance Value and  $C_L$  Capacitance Value Vs. Output Load Drive Capability



Note 1: An external  $0.01\mu\text{F}$  ceramic bypass capacitor in parallel with a  $0.1\mu\text{F}$  high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance ( $<12\text{pF}$ ), 10X attenuation factor, high impedance ( $>10\text{Mohms}$ ), and high bandwidth ( $>300\text{MHz}$ ) passive probe is recommended.

Note 3: Capacitance value  $C_L$  includes sum of all probe and fixture capacitance.

Note 4: Resistance value  $R_L$  is shown in Table 1. See applicable specification sheet for 'Load Drive Capability'.

Note 5: All diodes are MMBD7000, MMBD914, or equivalent.

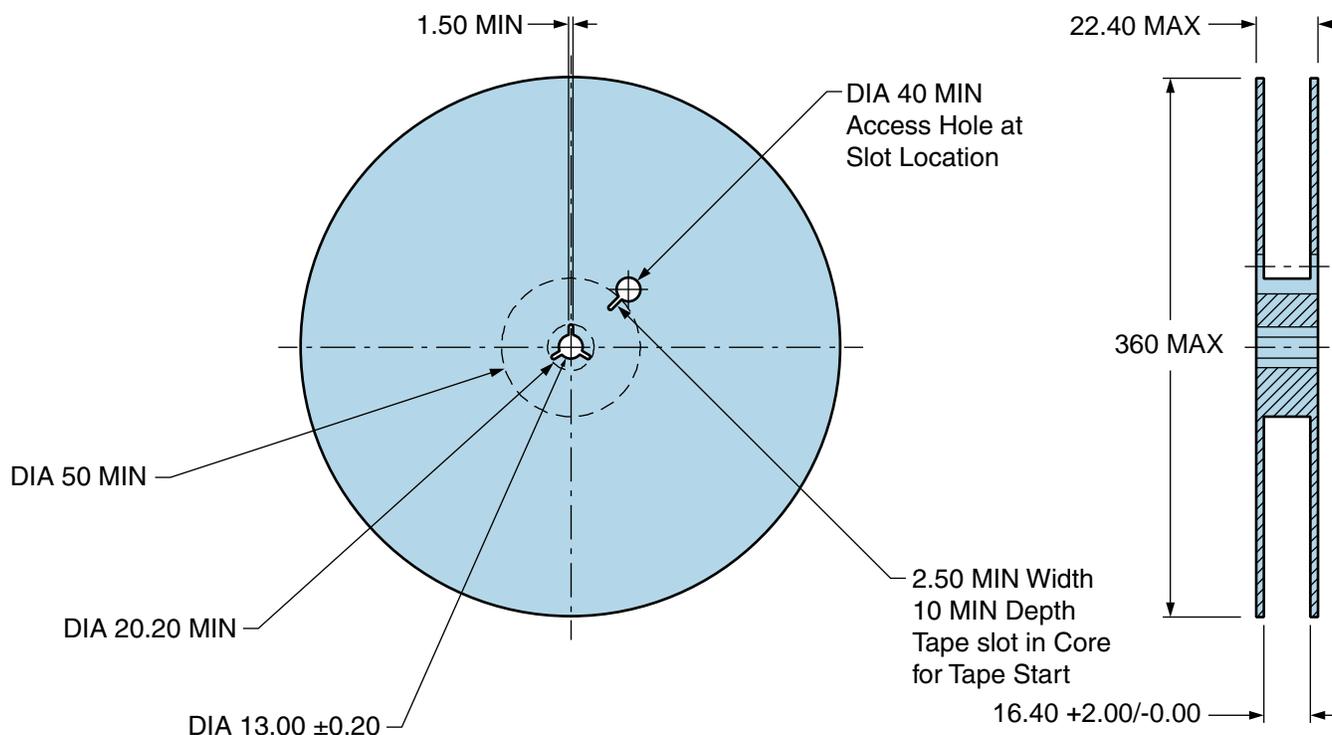
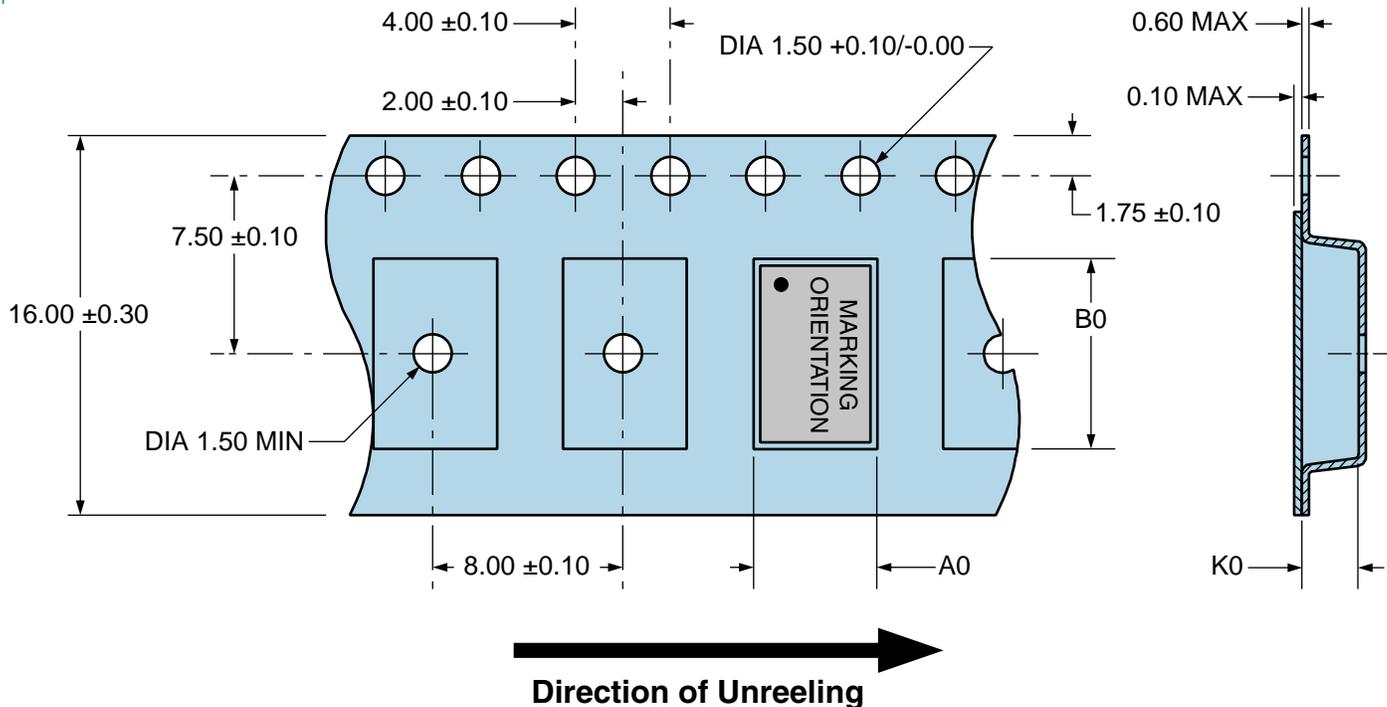
# EV32C6B5A1-4.032M TR

## Tape & Reel Dimensions

Quantity Per Reel: 1,000 units

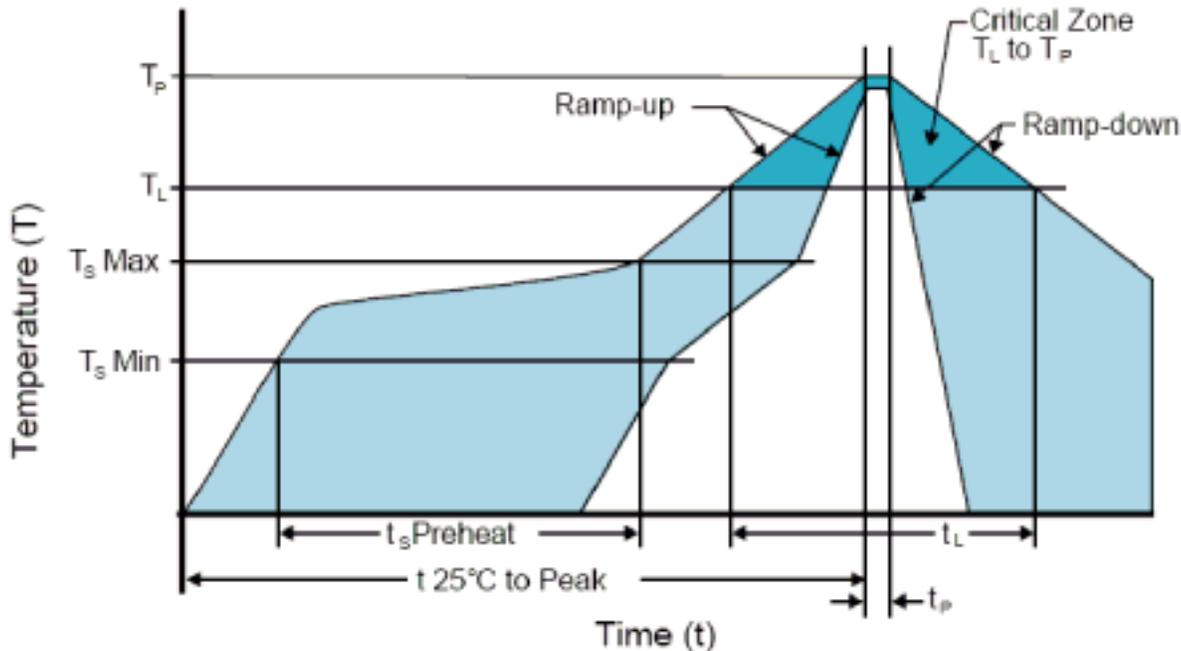
All Dimensions in Millimeters

Compliant to EIA-481



# EV32C6B5A1-4.032M TR [Click part number to visit Part Number Details page](#)

## Recommended Solder Reflow Methods

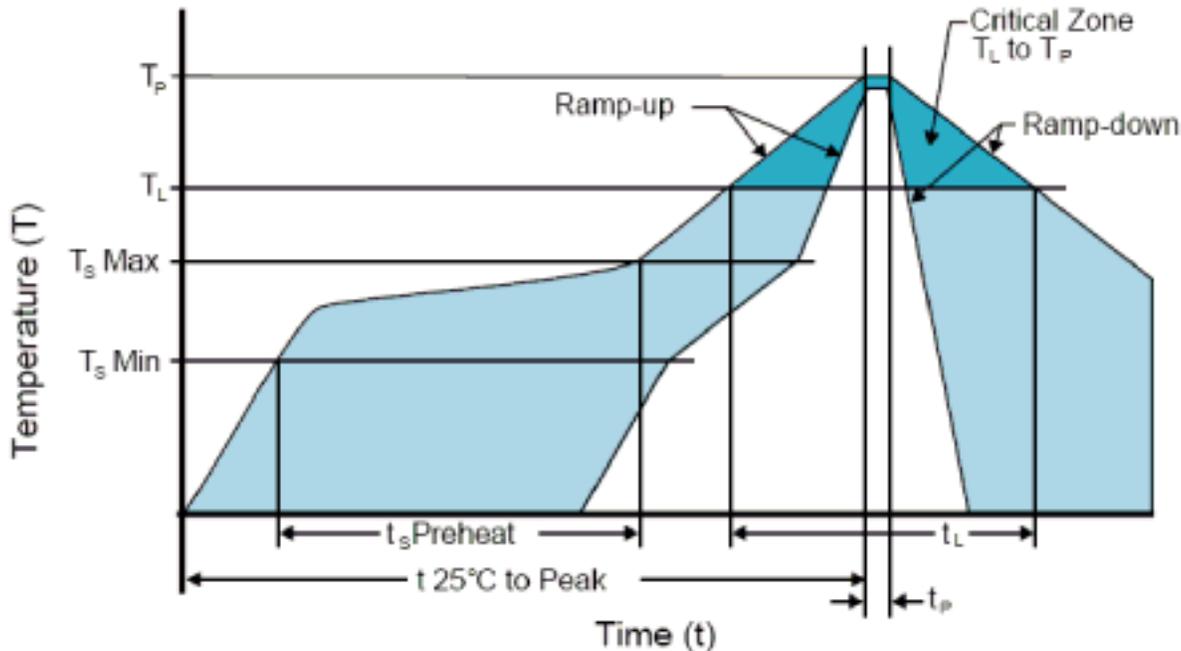


### High Temperature Infrared/Convection

<b>Ts MAX to Tl (Ramp-up Rate)</b>	3°C/Second Maximum
<b>Preheat</b>	
- Temperature Minimum (Ts MIN)	150°C
- Temperature Typical (Ts TYP)	175°C
- Temperature Maximum (Ts MAX)	200°C
- Time (ts MIN)	60 - 180 Seconds
<b>Ramp-up Rate (Tl to Tp)</b>	3°C/Second Maximum
<b>Time Maintained Above:</b>	
- Temperature (Tl)	217°C
- Time (tL)	60 - 150 Seconds
<b>Peak Temperature (Tp)</b>	260°C Maximum for 10 Seconds Maximum
<b>Target Peak Temperature (Tp Target)</b>	250°C +0/-5°C
<b>Time within 5°C of actual peak (tp)</b>	20 - 40 Seconds
<b>Ramp-down Rate</b>	6°C/Second Maximum
<b>Time 25°C to Peak Temperature (t)</b>	8 Minutes Maximum
<b>Moisture Sensitivity Level</b>	Level 1

# EV32C6B5A1-4.032M TR [Click part number to visit Part Number Details page](#)

## Recommended Solder Reflow Methods



### Low Temperature Infrared/Convection 240°C

Ts MAX to Tl (Ramp-up Rate)	5°C/Second Maximum
<b>Preheat</b>	
- Temperature Minimum (Ts MIN)	N/A
- Temperature Typical (Ts TYP)	150°C
- Temperature Maximum (Ts MAX)	N/A
- Time (ts MIN)	60 - 120 Seconds
<b>Ramp-up Rate (Tl to Tp)</b>	5°C/Second Maximum
<b>Time Maintained Above:</b>	
- Temperature (Tl)	150°C
- Time (tL)	200 Seconds Maximum
<b>Peak Temperature (Tp)</b>	240°C Maximum
<b>Target Peak Temperature (Tp Target)</b>	240°C Maximum 2 Times / 230°C Maximum 1 Time
<b>Time within 5°C of actual peak (tp)</b>	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time
<b>Ramp-down Rate</b>	5°C/Second Maximum
<b>Time 25°C to Peak Temperature (t)</b>	N/A
<b>Moisture Sensitivity Level</b>	Level 1

### Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum.

### High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum.