



30V DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} MAX	I _{D MAX} T _A = +25°C
001/	$35m\Omega$ @ $V_{GS} = 10V$	5.5A
30V	$45 \text{m}\Omega$ @ $V_{GS} = 4.5 \text{V}$	4.9A

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- DC Motor Control
- DC-AC Inverters

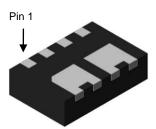
Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

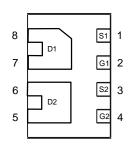
Mechanical Data

- Case: V-DFN3020-8 (Type N)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ⁶⁴
- Weight: 0.011 grams (Approximate)

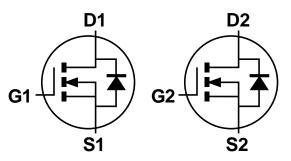
V-DFN3020-8 (Type N)



Bottom View



Bottom View Pin Configuration



Q1 N-Channel MOSFET

Q2 N-Channel MOSFET

Equivalent Circuit

Ordering Information (Note 4)

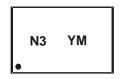
Part Number	Case	Packaging		
DMN3035LWN-7	V-DFN3020-8 (Type N)	3,000/Tape & Reel		
DMN3035LWN-13	V-DFN3020-8 (Type N)	10,000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/quality/lead_free.html.

Marking Information

V-DFN3020-8 (Type N)



N3 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date Code Key

Year	2016	2017	2018	2019	2020	2021	2022	2023
Code	D	Е	F	G	Н	ļ	J	K

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V_{DSS}	30	V	
Gate-Source Voltage		V _{GSS}	±20	V	
Continuous Drain Current (Note 6) V _{GS} = 10V	I _D	5.5 4.4	А		
Maximum Continuous Body Diode Forward Curre	ent (Note 6)		Is	1	Α
Pulsed Drain Current			I _{DM}	30	Α
Avalanche Current (Note 7) L = 0.1mH		I _{AS}	13	Α	
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	9.0	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Dawer Dissipation (Note 5)	T _A = +25°C	0.77		W
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	P _D	0.49	VV
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	162	°C/W
mermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	116	C/VV
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	0	1.78	W
Total Power Dissipation (Note 6)	$T_A = +70^{\circ}C$	PD	1.10	VV
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	71		°C/W
Thermal Resistance, Junction to Ambient (Note o)	t<10s	$R_{\theta JA}$	50	C/VV
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	10.7	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

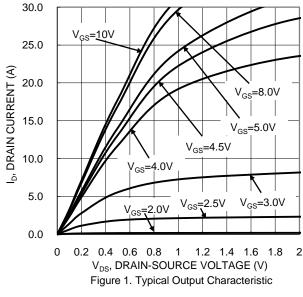
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)	Symbol	IAIIII	тур	IVIAA	Oiiit	rest condition
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	1.0	μA	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)			ı			
Gate Threshold Voltage	V _{GS(TH)}	1.0	_	2.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	26 34	35 45	mΩ	$V_{GS} = 10V, I_D = 4.8A$ $V_{GS} = 4.5V, I_D = 4.3A$
Diode Forward Voltage	V _{SD}	_	0.75	1.1	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 9)		l.	I	l .		1 66 . 5
Input Capacitance	C _{iss}	_	399	_	pF	151/1/
Output Capacitance	Coss	_	57	_	pF	$V_{DS} = 15V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	50	_	pF	T = 1.0IVID2
Gate Resistance	Rg	_	1.36	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	4.5	_	nC	
Total Gate Charge (V _{GS} = 10V)	Qg	_	9.9	_	nC	15V 1 . 5 9 A
Gate-Source Charge	Qgs	_	1.2	_	nC	$V_{DS} = 15V, I_{D} = 5.8A$
Gate-Drain Charge	Q _{gd}	_	1.8	_	nC	1
Turn-On Delay Time	t _{D(ON)}	_	3.0	_	ns	
Turn-On Rise Time	t _R	_	3.3	_	ns	V _{DD} = 15V, V _{GS} = 10V,
Turn-Off Delay Time	t _{D(OFF)}	_	10.6		ns	$R_L = 2.6\Omega$, $R_G = 3\Omega$
Turn-Off Fall Time	t _F	_	2.0	_	ns	1
Reverse Recovery Time	t _{RR}	_	7.9	_	ns	I _F = 4.8A, di/dt = 100A/μs
Reverse Recovery Charge	Q_{RR}	_	2.4	_	nC	I _F = 4.8A, di/dt = 100A/μs

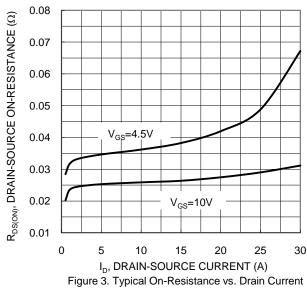
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

^{7.} I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$.

Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.







and Gate Voltage

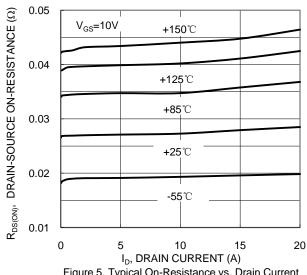
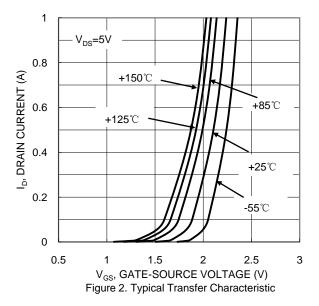
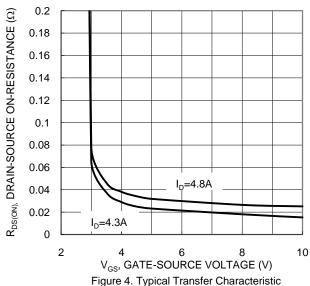


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





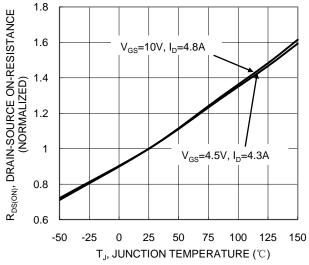
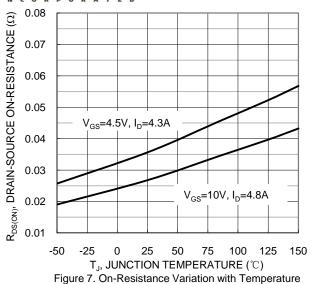
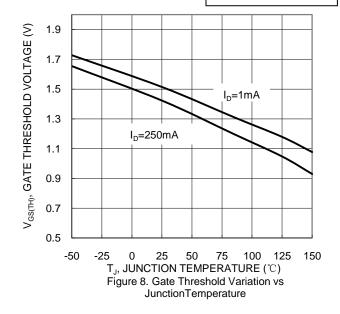


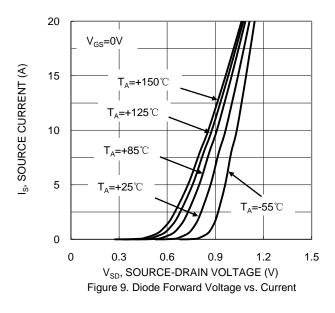
Figure 6. On-Resistance Variation with Temperature

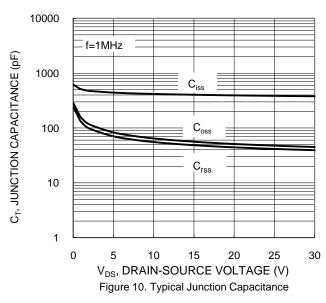


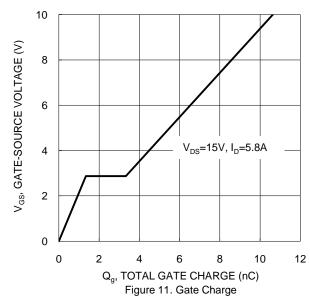
DMN3035LWN

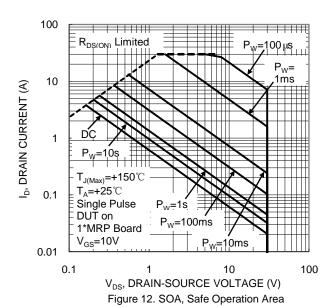














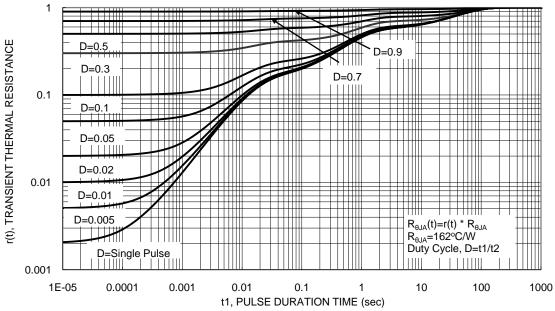


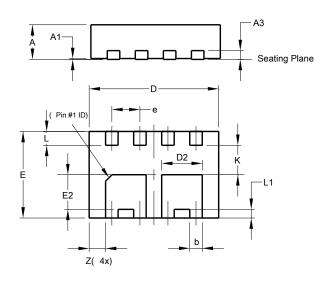
Figure 13 Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

V-DFN3020-8 (Type N)

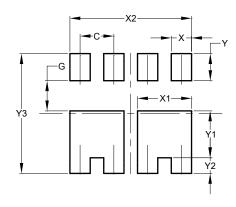


V-DFN3020-8								
(Type N)								
Dim	n Min Max Typ							
Α	0.77	0.83	0.80					
A1	0	0.05	0.02					
A3	-	-	0.203					
b	0.24	0.34	0.29					
D	2.95	3.05	3.00					
D2	0.84	1.04	0.94					
е	-	-	0.65					
Е	1.95	2.05	2.00					
E2	0.70	0.90	0.80					
L	0.27	0.37	0.32					
L1	0.15	0.25	0.20					
K	-	-	0.68					
Z	-	-	0.38					
All	Dimen	sions i	in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

V-DFN3020-8 (Type N)



Dimensions	Value
Difficitsions	(in mm)
С	0.650
G	0.580
X	0.390
X1	1.040
X2	2.340
Y	0.520
Y1	0.900
Y2	0.300
Y3	2.300



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