

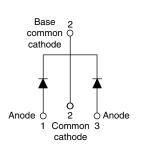
VS-30CTQ0...PbF Series, VS-30CTQ0...-N3 Series

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Vishay Semiconductors

Schottky Rectifier, 2 x 15 A

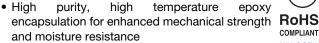




PRODUCT SUMMARY					
Package	TO-220AB				
I _{F(AV)}	2 x 15 A				
V_{R}	50 V, 60 V				
V _F at I _F	0.56 V				
I _{RM} max.	45 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Common cathode				
E _{AS}	13 mJ				

FEATURES

- 150 °C T_J operation
- · Very low forward voltage drop
- High frequency operation





- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS VALUES UNITS							
I _{F(AV)}	Rectangular waveform	30	Α					
V _{RRM}		50/60	V					
I _{FSM}	t _p = 5 μs sine	1000	Α					
V _F	15 A _{pk} , T _J = 125 °C (per leg)	0.56	V					
T _J	Range	- 55 to 150	°C					

VOLTAGE RATINGS						
PARAMETER SYMBOL					UNITS	
Maximum DC reverse voltage	V_R	50	50	60	60	V
Maximum working peak reverse voltage	V_{RWM}	30	50	00	00	V

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current	per device		50 % duty cycle at T _C = 105 °C, rectangular waveform		30		
See fig. 5	per leg	I _{F(AV)}			15	Α	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1000		
		I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	260		
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 1.50 A, L = 11.5 mH		13	mJ	
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.50	Α	



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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS		
		15 A	T 05.00	0.62	V		
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	30 A	T _J = 25 °C	0.82			
See fig. 1	V FM (1)	15 A	T 405.00	0.56			
		30 A	T _J = 125 °C	0.71			
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.80	mA		
See fig. 2		T _J = 125 °C		45			
Threshold voltage	V _{F(TO)}	T T mayimum		0.39	V		
Forward slope resistance	r _t	$T_J = T_J$ maximum		8.47	mΩ		
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		720	pF		
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs		

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55 to 150	°C			
Maximum thermal resistance, junction to case per leg	В	DC eneration	3.25				
Maximum thermal resistance, junction to case per package	- R _{thJC}	DC operation	1.63	°C/W			
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50				
Approximate weight			2	g			
Approximate weight			0.07	OZ.			
Mounting torque minimum			6 (5)	kgf · cm			
Mounting torque — maximum			12 (10)	(lbf \cdot in)			
Marking dayion		Case style TO-220AB	30CTQ050				
Marking device		Case Style 10-220AD	30CTQ060				

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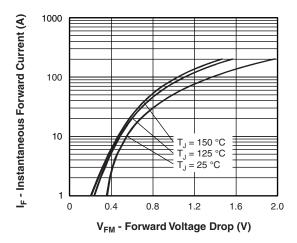


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

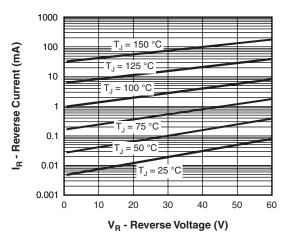


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

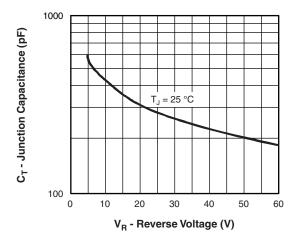


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

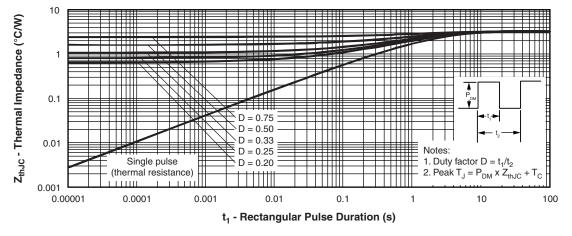


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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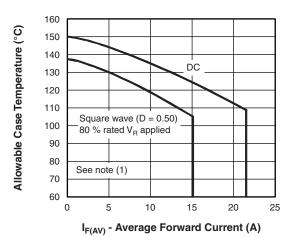


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

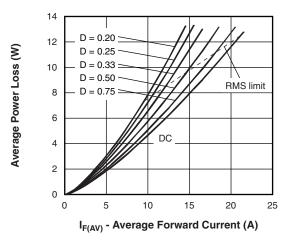


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

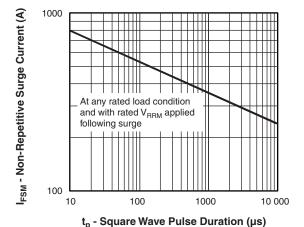


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

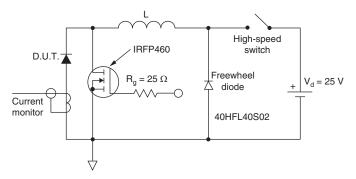


Fig. 8 - Unclamped Inductive Test Circuit

Note

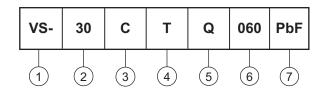
 $^{(1)}$ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = Forward power loss = I_{F(AV)} \times V_{FM} \ at \ (I_{F(AV)}/D) \ (see fig. 6); \\ Pd_{REV} = Inverse power loss = V_{R1} \times I_R \ (1 - D); I_R \ at \ V_{R1} = 10 \ V$

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (30 = 30 A)

- Circuit configuration:

C = Common cathode

4 - Package:

T = TO-220

5 - Schottky "Q" series

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-30CTQ050PbF	50	1000	Antistatic plastic tube				
VS-30CTQ050-N3	50	1000	Antistatic plastic tube				
VS-30CTQ060PbF	50	1000	Antistatic plastic tube				
VS-30CTQ060-N3	50	1000	Antistatic plastic tube				

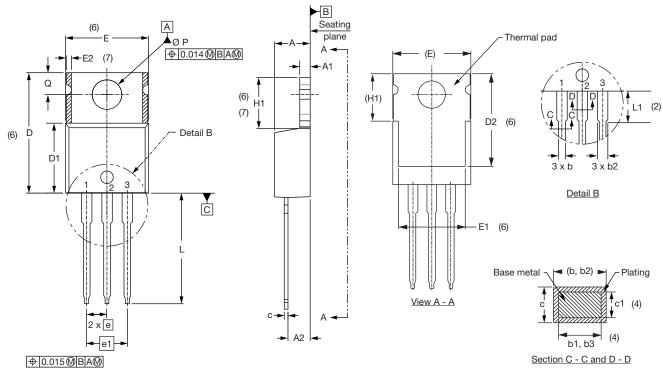
LINKS TO RELATED DOCUMENTS						
Dimensions		www.vishay.com/doc?95222				
Dort marking information	TO-220ABPbF	www.vishay.com/doc?95225				
Part marking information	TO-220AB-N3	www.vishav.com/doc?95028				



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TO-220AB

DIMENSIONS in millimeters and inches



Lead tip

Lead assignments

<u>Diodes</u>

- 1. Anode/open
- 2. Cathode
- 3. Anode

Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIN	IETERS	INC	NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Е	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2		0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° t	o 93°	90° t	o 93°	
	•	•	•	•	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and F1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



Legal Disclaimer Notice

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