

CAT25 - Thick Film Chip Resistor Arrays

Parameters	CAT25	Test Method
Number of Resistors	8	
Resistance Range E24	10 ohms to 1 megohm	
Resistance Tolerance	5 %	JIS-C-5202.5.1
Power Rating/Resistor	62.5 mW	
Rating Temperature	+70 °C	
T.C.R.	±200 ppm/°C	JIS-C-5202.5.2
Maximum Operating Voltage	25 V	
Operating Temperature	-55 °C to +125 °C	

Specification	Characteristics	Test Method
Short Time Overload	±(3 % +0.1 ohm)	JIS-C-5202.5.5
Load Life	±(5 % +0.1 ohm)	JIS-C-5202.7.10
Humidity Load Life	±(3 % +0.1 ohm)	JIS-C-5202.7.9
Resistance to Soldering Heat	±(1 % +0.1 ohm)	JIS-C-5202.6.4
Terminal Strength	±(1 % +0.1 ohm)	JIS-C-5202.6.
Temperature Cycle	±(2 % +0.1 ohm)	JIS-C-5202.7.4
Vibration	±(1 % +0.1 ohm)	JIS-C-5202.6.3
Insulation Resistance	1000 megohms minimum	JIS-C-5202.5.6
Dielectric Withstanding Voltage	50 VRMS	JIS-C-5202.5.7
Lead Solderability	>95 %	JIS-C-5202.6.5

CA T 25 - 103 J A LF

Product _____
CA = Chip Array

Pin Style _____
T = Concave

Model _____

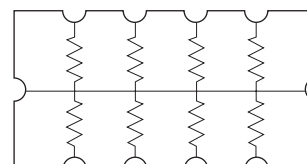
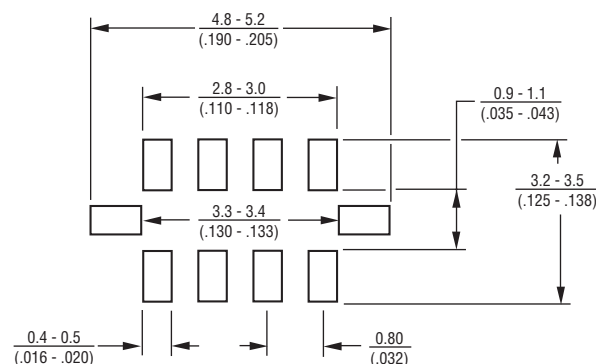
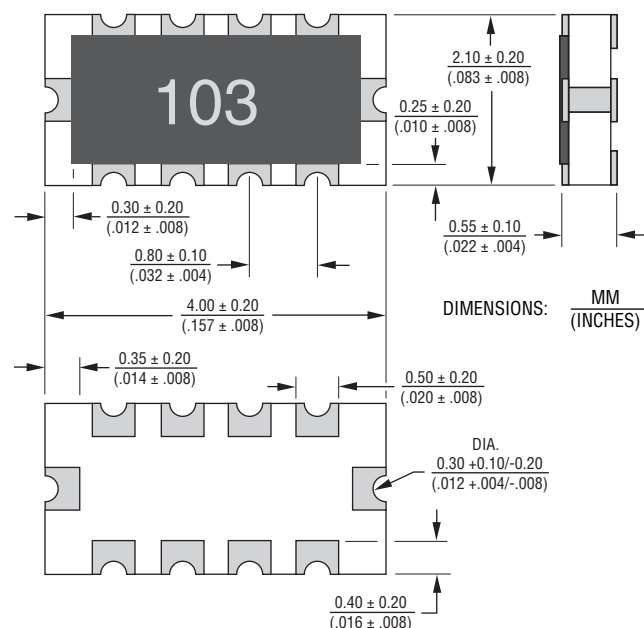
Resistance Value _____
103 = 10K ohms

Tolerance _____
J = 5 %

Electrical Circuit _____
A = 8 Resistors, Bussed Type

Terminations _____
LF = Tin-plated (RoHS compliant)

For Standard Values Used in Capacitors, Inductors, and Resistors, [click here](#).



*RoHS Directive 2002/95/EC Jan 27, 2003 including Annex.
Specifications are subject to change without notice.
Customers should verify actual device performance in their specific applications

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BOURNS®

Soldering Profile for RoHS Compliant Chip Resistors and Arrays



Packaging

12
(.472) embossed tape
4,000 pcs. per reel

Material

Substrate	Alumina 96
Element	Ruthenium Oxide
Coating	Glass
Terminal	Plated Ni+Sn