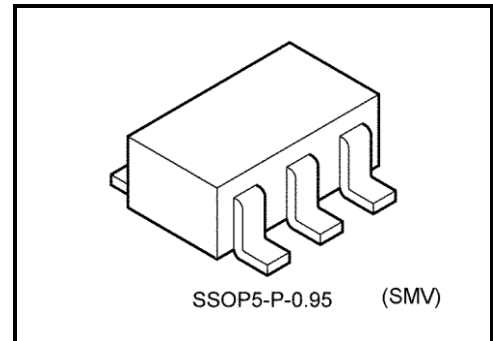


TC7SET125F

Bus Buffer

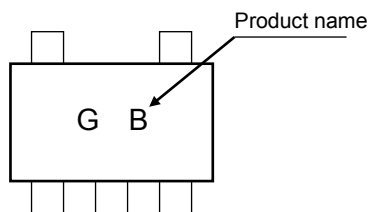
Features

- High speed : $t_{pd} = 3.7 \text{ ns (typ.)}$
at $V_{CC} = 5 \text{ V}$, $C_L = 15 \text{ pF}$
- Low power dissipation : $I_{CC} = 2 \mu\text{A (max)}$ at $T_a = 25^\circ\text{C}$
- Compatible with TTL outputs. : $V_{IL} = 0.8 \text{ V (max)}$
 $V_{IH} = 2.0 \text{ V (min)}$
- 5.5V tolerant input.



Weight: 0.016 g (typ.)

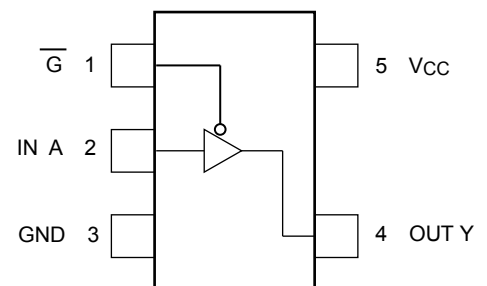
Marking



Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	-0.5 to 7.0	V
DC input voltage	V_{IN}	-0.5 to 7.0	V
DC output voltage	V_{OUT}	-0.5 to $V_{CC} + 0.5$	V
Input diode current	I_{IK}	-20	mA
Output diode current	I_{OK}	± 20 (Note 1)	mA
DC output current	I_{OUT}	± 25	mA
DC V_{CC} /ground current	I_{CC}	± 50	mA
Power dissipation	P_D	200	mW
Storage temperature	T_{stg}	-65 to 150	$^\circ\text{C}$
Lead temperature (10 s)	T_L	260	$^\circ\text{C}$

Pin Assignment (top view)



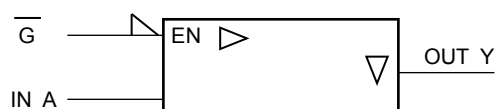
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note1: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Start of commercial production
2004-06

IEC Logic Symbol



Truth Table

$\overline{G}$	A	Y
H	X	Z
L	L	L
L	H	H

X: Don't care

Z: High impedance

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	4.5 to 5.5	V
Input voltage	V_{IN}	0 to 5.5	V
Output voltage	V_{OUT}	0 to V_{CC}	V
Operating temperature	T_{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 20	ns/V

DC Electrical Characteristics

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
				VCC (V)	Min	Typ.	Max	Min		Max
High-level input voltage	VIH	—		4.5 to 5.5	2.0	—	—	2.0	—	V
Low-level input voltage	VIL	—		4.5 to 5.5	—	—	0.8	—	0.8	V
High-level output voltage	VOH	VIN = VIH or VIL	IOH = -50 μA	4.5	4.4	4.5	—	4.4	—	V
			IOH = -8 mA	4.5	3.94	—	—	3.80	—	
Low-level output voltage	VOL	VIN = VIL	IOL = 50 μA	4.5	—	0.0	0.10	—	0.10	V
			IOL = 8 mA	4.5	—	—	0.36	—	0.44	
3-state output off-state current	IOZ	VIN = VIH or VIL VOUT = VCC or GND		5.5	—	—	±0.25	—	±2.5	μA
Input leakage current	IIN	VIN = 5.5 V or GND		0 to 5.5	—	—	±0.1	—	±1.0	μA
Quiescent supply current	ICC	VIN = VCC or GND		5.5	—	—	2.0	—	20.0	μA
	ICCT	Per Input :VIN = 3.4 V Other Input :VCC or GND		5.5	—	—	1.35	—	1.50	mA

AC Characteristics (input: $t_r = t_f = 3\text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
		VCC (V)	CL (pF)	Min	Typ.	Max	Min	Max	
Propagation delay time	t _{pLH} t _{pHL}	5.0 ± 0.5	15	—	3.7	6.0	1.0	6.9	ns
			50	—	6.0	10.4	1.0	11.9	
3-state output enable time	t _{pZL} t _{pZH}	5.0 ± 0.5	15	—	3.6	5.6	1.0	6.5	ns
			50	—	6.0	10.3	1.0	11.9	
3-state output disable time	t _{pLZ} t _{pHZ}	5.0 ± 0.5	50	—	7.3	10.0	1.0	11.5	ns
Input capacitance	C _{IN}			—	4	10	—	—	pF
Output capacitance	C _{OUT}			—	6	—	—	—	pF
Power dissipation capacitance	C _{PD}	(Note 2)		—	14	—	—	—	pF

Note 2: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

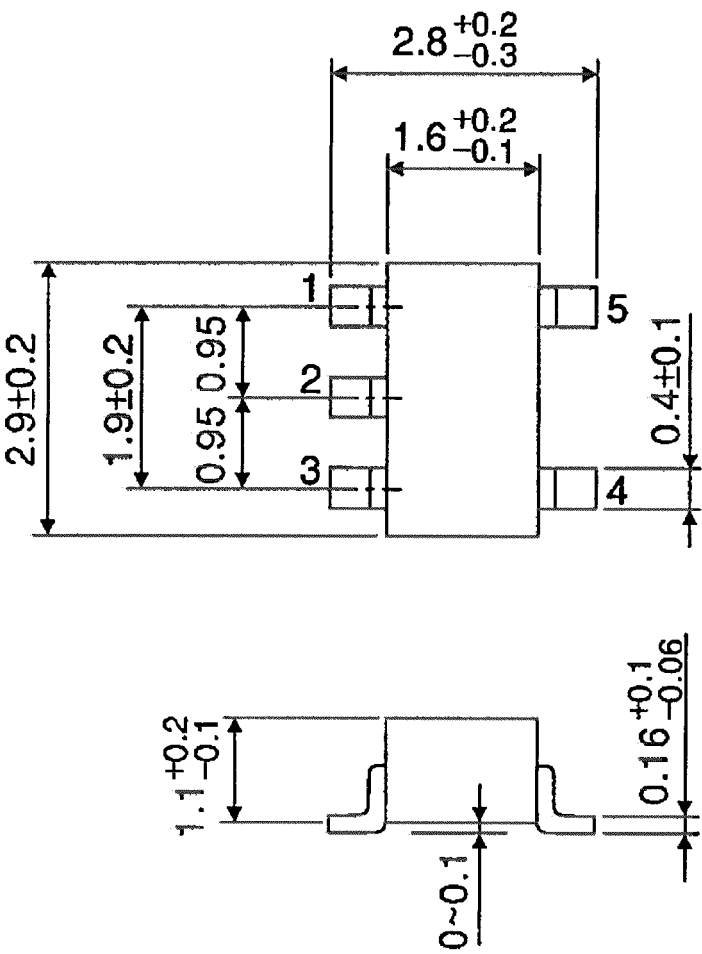
Average operating current can be obtained by the equation:

$$I_{CC}(\text{opr}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions

SSOP5-P-0.95

Unit : mm



Weight: 0.016 g (typ.)

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