

June 2008

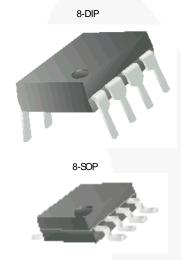
MC34063A / MC33063A SMPS Controller

Features

- Operation from 3.0 to 40V Input
- Short Circuit Current Limiting
- Low Standby Current
- Output Switch Current of 1.5A Without External Transistors
- Adjustable Output Voltage
- Frequency of Operation from 100Hz to 100KHz
- Step-up, Step Down, or Inverting Switching Regulators

Description

The MC34063A/MC33063A is a monolithic regulator subsystem intended for a DC to DC converter. The device contains a temperature-compensated bandgap reference, a duty cycle control oscillator, driver, and high-current output switch. It can be used for stepdown, step-up, or inverting switching and series pass regulators.



Ordering Information

Part Number	Operating Temperature Range		Package	
MC34063AP	0 ~ +70°C	RoHS	8-DIP	
MC34063AD	0 ~ +70°C	RoHS	8-SOP	
MC33063AP	-40 ~ +85°C	RoHS	8-DIP	
MC33063AD	-40 ~ +85°C	RoHS	8-SOP	

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Block Diagram

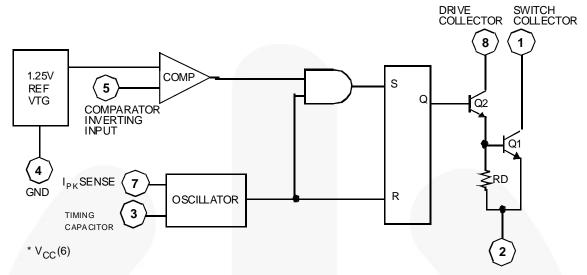


Figure 1. Block Diagram

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit	
V _{CC}	Supply Voltage			40	V
V _{I(COMP)}	Comparator Input Voltage Rai	-0.3	+40	V	
V _{C(SW)}	Switch Collector Voltage		40	V	
V _{E(SW)}	Switch Emitter Voltage		40	V	
V _{CE(SW)}	Switch Collector to Emitter Voltage			40	V
V _{C(DR)}	Driver Collector Voltage			40	V
I _{SW}	Switch Current			1.5	А
T _{STG}	Storage Temperature Range	torage Temperature Range		+150	°C
P _D	Power Dissipation	SOP		0.8	W
	Power Dissipation	DIP	1	1	VV

Electrical Characteristics

 $V_{CC} = 5.0V$, $T_A = 0$ °C to +70°C for MC34063, $T_A = -40$ °C to +85°C for MC33063, unless otherwise specified.

Symbol	Parameter		Conditions	Min.	Тур.	Max.	Units
Oscillator							
I _{CHG}	Charging Current		V _{CC} =5 to 40V, T _A =25°C	22	31	42	μA
I _{DISCHG}	Discharging Current		V _{CC} =5 to 40V, T _A =25°C	140	190	260	μA
V _(OSC)	Oscillator Amplitude		T _A =25°C		0.5		V
K	Discharge-to-Charge Current Ratio		V ₇ =V _{CC} , T _A =25°C	5.2	6.1	7.5	
V _{SENSE(CL)}	Current Limit Sense Voltage		I _{CHG} =I _{DISCHG} , T _A =25°C	250	300	350	mV
Output Sw	itch						7
V _{CE(SAT)1}	Saturation Voltage 1 ⁽¹⁾		I _{SW} =1.0A, V _{C(driver)} =V _{C(SW)}		0.95	1.30	V
V _{CE(SAT)2}	Saturation Voltage 2 ⁽¹⁾		I _{SW} =1.0A, V _{C(driver)} =50mA	·	0.45	0.70	V
G _{I(DC)}	DC Current Gain ⁽¹⁾		I _{SW} =1.0A, V _{CE} =5.0V, T _A =25°C	50	180		
I _{C(OFF)}	Collector Off-State Current ⁽¹⁾		V _{CE} =40V, T _A =25°C		0.01	100.00	μΑ
Comparato	or				37		-7
V_{TH}	Threshold Voltage			1.21	1.24	1.29	V
ΔV_{TH}	Threshold Voltage Line Regulation		V _{CC} =3 to 40V		2	5	mV
I _{BIAS}	Input Bias Current		V _I =0V		50	400	nA
Total Device	ce					•	
I _{CC}		34063	V _{CC} =5 to 40V, C _T =0.001μF, V ₇ =V _{CC} , V ₅ >V _{TH} , pin 2=GND			4	mA
	Supply Current MC3:	33063				5	

Note

^{1.} Output switch tests are performed under pulsed conditions to minimize power dissipation.

Typical Performance Characteristics

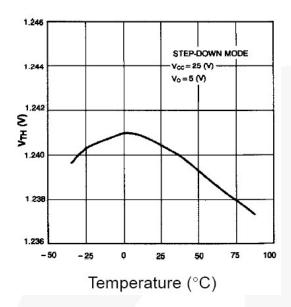


Figure 2. Temperature Drift (V_{TH})

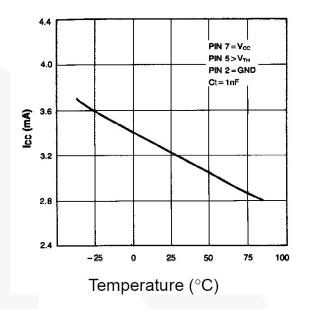
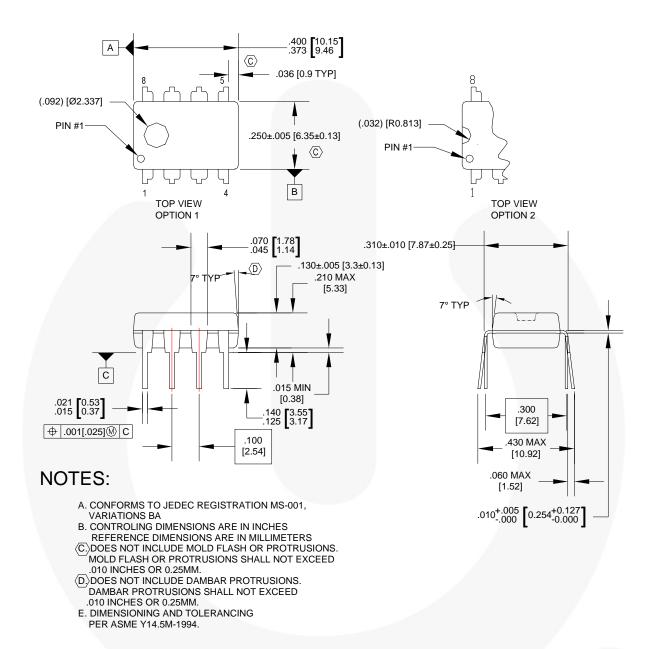


Figure 3. Temperature Drift (loc)

Physical Dimensions



N08EREVG

Figure 4. 8-Lead PDIP, JEDEC MS-001

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Physical Dimensions

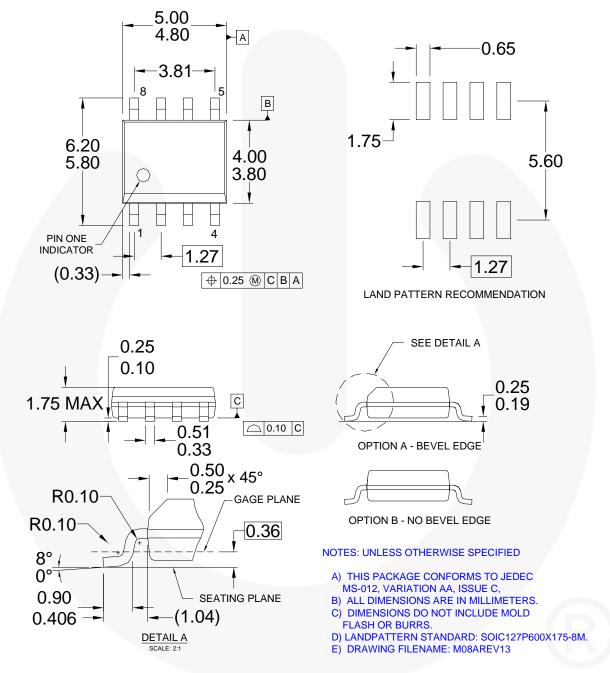


Figure 5. 8-Lead, SOIC, JEDEC MS-012, .150 inch Narrow Body

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