INTEGRATED CIRCUITS

DATA SHEET

LM219/LM319 Dual voltage comparator

Product data Supersedes data of 1994 Aug 31 File under Integrated Circuits, IC11 Handbook 2001 Aug 03





Dual voltage comparator

LM219/LM319

DESCRIPTION

The LM319 series are precision high-speed dual comparators fabricated on a single monolithic chip. They are designed to operate over a wide range of supply voltages down to a single 5 V logic supply and ground. Further, they have higher gain and lower input currents than devices like the $\mu A710$. The uncommitted collector of the output stage makes the LM319 compatible with RTL, DTL, and TTL as well as capable of driving lamps and relays at currents up to 25 mA.

Although designed primarily for applications requiring operation from digital logic supplies, the LM319 series are fully specified for power supplies up to ± 15 V. It features faster response than the LM111 at the expense of higher power dissipation. However, the high-speed, wide operating voltage range and low package count make the LM319 much more versatile than older devices like the μ A711.

FEATURES

- Two independent comparators
- Operates from a single 5 V supply
- Typically 80 ns response time at ±15 V
- Minimum fanout of 3 (each side)
- Maximum input current of 1 μA over temperature
- Inputs and outputs can be isolated from system ground
- High common-mode slew rate

PIN CONFIGURATION

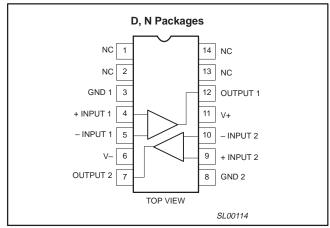


Figure 1. Pin Configuration

EQUIVALENT SCHEMATIC

0 \$R7 3K Q9 Q8 Q3 Q7 Q6 18pF \$R11 R10 ŠR12 13k \$R5 R4 13K \$R8 2K Q15 Q5 ₹R9 Q10 Q13 R15 18K 300 Q22 C14 O OUTPUT R22 R20 R18 60 3.6K 1.9I R13 .13 600 1.9K Q16 Q12 Q1 Q21 \$R14 ₹R16 600 R19 Q20 Q19 TO OTHER HALF \$R25 600 $\frac{1}{2}$ R24 Q18 Q12 R17 3 GND SL00115

Figure 2. Equivalent Schematic

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ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE	DWG #
14-Pin Plastic Small Outline (SO) Package	−25 °C to +85 °C	LM219D	SOT108-1
14-Pin Plastic Small Outline (SO) Package	0 °C to +70 °C	LM319D	SOT108-1
14-Pin Plastic Dual In-Line Package (DIP)	0 °C to +70 °C	LM319N	SOT27-1

ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
Vs	Total supply voltage	36	V
	Output to negative supply voltage	36	V
	Ground to negative supply voltage	25	V
	Ground to positive supply voltage	18	V
	Differential input voltage	±5	V
V _{IN}	Input voltage ¹	±15	V
	Maximum power dissipation, T _{amb} = 25 °C (still-air) ² N package D package	1420 1040	mW mW
	Output short-circuit duration	10	S
T _{amb}	Operating temperature range LM219 LM319	-25 to +85 0 to +70	°C
T _{stg}	Storage temperature range	-65 to +150	°C
T _{sld}	Lead soldering temperature (10 sec max)	230	°C

NOTES:

Derate above 25 °C, at the following rates:
 N package at 11.4 mW/°C

D package at 8.3 mW/°C

DC ELECTRICAL CHARACTERISTICS

 $V_S = \pm 15 \text{ V}; -25 \text{ °C} \le T_{amb} \le +85 \text{ °C}$ for LM219, $0 \text{ °C} \le T_{amb} \le +70 \text{ °C}$ for LM319, unless otherwise specified.

CVMDOL	DADAMETED	TEST COMPITIONS		LM219			LM319		LIMIT
SYMBOL	PARAMETER	TEST CONDITIONS	Min	Тур	Max	Min	Тур	Max	UNIT
Vos	Input offset voltage ^{1, 2}	$R_S \le 5 \text{ k}\Omega; T_{amb} = 25 \text{ °C}$ Over temp.		0.7	4.0 7		2.0	8.0 10	mV
los	Input offset current ^{1, 2}	T _{amb} = 25 °C Over temp.		30	75 100		80	200 300	nA
I _B	Input bias current ¹	T _{amb} = 25 °C Over temp.		150	500 1000		250	1000 1200	nA nA
A _V	Voltage gain	T _{amb} = 25 °C	8	40		8	40		V/mV
V _{OL}	Saturation voltage	$V_{IN} \le -10 \text{ mV}; I_{OUT} = 25 \text{ mA};$ $T_{amb} = 25 \text{ °C}; V+ \ge 4.5 \text{ V}; V- = 0 \text{ V}$		0.75	1.5		0.75	1.5	V
		$V_{IN} \le -10 \text{ mV}$; $I_{OUT} = 3.2 \text{ mA}$		0.3	0.6		0.3	0.4	
I _{OH}	Output leakage current	$V- = 0 \text{ V}; V_{IN} \ge 10 \text{ mV}$ $V_{OUT} = 35 \text{ V}; T_{amb} = 25 \text{ °C}$		0.2	10		0.2	10	μΑ
V _{IN}	Input voltage range	$V_S = \pm 15 \text{ V}$ V+ = 5V, V- = 0 V	1	±13	3	1	±13	3	٧
V _{ID}	Differential input voltage				±5			±5	V
l+	Positive supply current	V+=5V; V- = 0 V; T _{amb} = 25 °C		4.3			4.3		mA
l+	Positive supply current	V _S = ±15 V; T _{amb} = 25 °C		8.0	12.5		8.0	12.5	mA
I–	Negative supply current	$V_S = \pm 15 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$		3.0	5.0		3.0	5.0	mA

NOTES:

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^{1.} For supply voltages less than ± 15 V, the absolute maximum rating is equal to the supply voltage.

V_{OS}, I_{OS} and I_B specifications apply for a supply voltage range of V_S = ±15 V down to a single 5 V supply.
 The offset voltages and offset currents given are the maximum values required to drive the output to within 1 V of either supply with a 1 mA load. Thus these parameters define an error band and take into account the worst case effects of voltage gain and input impedance.

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AC ELECTRICAL CHARACTERISTICS

SYMBOL	DADAMETER	PARAMETER TEST CONDITIONS				
STWIBOL	FARAMETER	TEST CONDITIONS	Min	Тур	Max	UNIT
t _R	Response time ¹	$V_S = \pm 15 \text{ V}; T_{amb} = 25 \text{ °C}$ $R_L = 500 \Omega \text{ (see test figure)}$		80		ns

NOTE:

TEST CIRCUIT

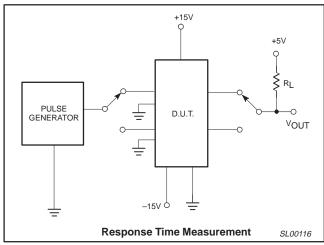


Figure 3. Test Circuit

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^{1.} The response time specified is for a 100 mV step with 5 mV overdrive.

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TYPICAL PERFORMANCE CHARACTERISTICS

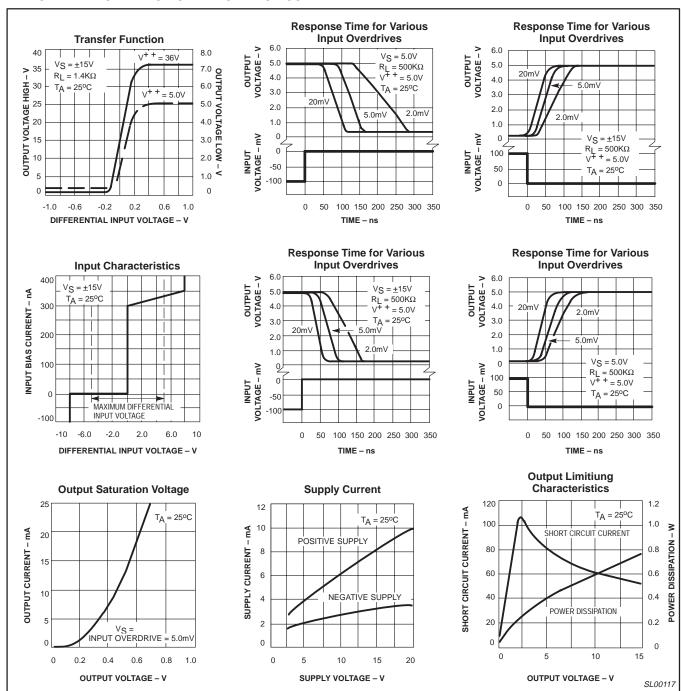


Figure 4. Typical Performance Characteristics

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TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

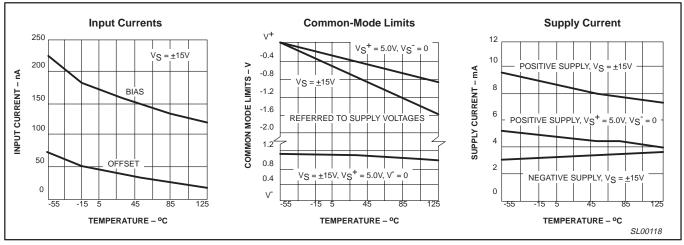


Figure 5. Typical Performance Characteristics

TYPICAL APPLICATIONS

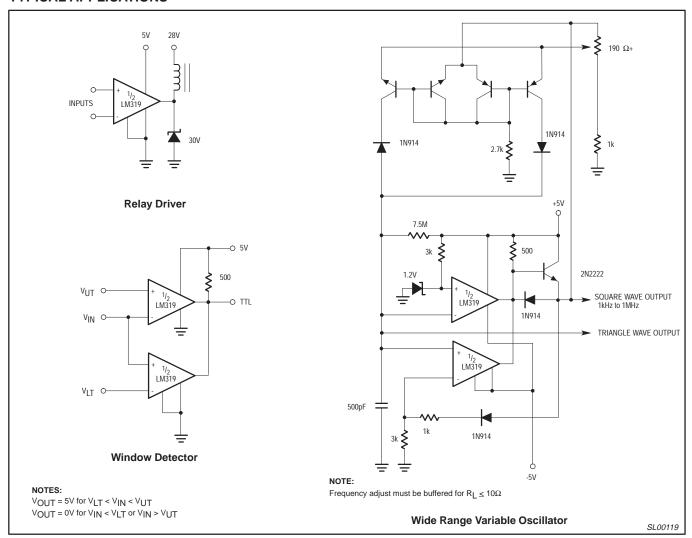


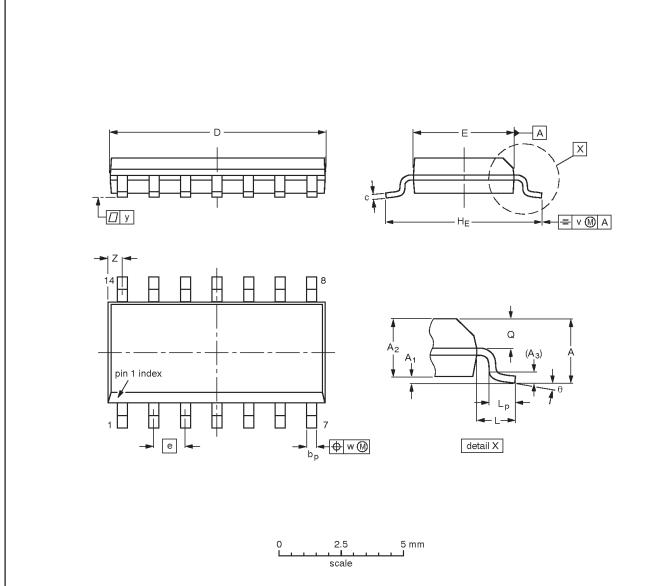
Figure 6. Typical Applications

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SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁	A ₂	А3	bp	С	D ⁽¹⁾	E ⁽¹⁾	е	HE	L	Lp	Q	>	w	у	Z ⁽¹⁾	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	8.75 8.55	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8°
inches	0.069	0.010 0.004	0.057 0.049	0.01	0.019 0.014	0.0100 0.0075	0.35 0.34	0.16 0.15	0.050	0.244 0.228	0.041	0.039 0.016		0.01	0.01	0.004	0.028 0.012	0°

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

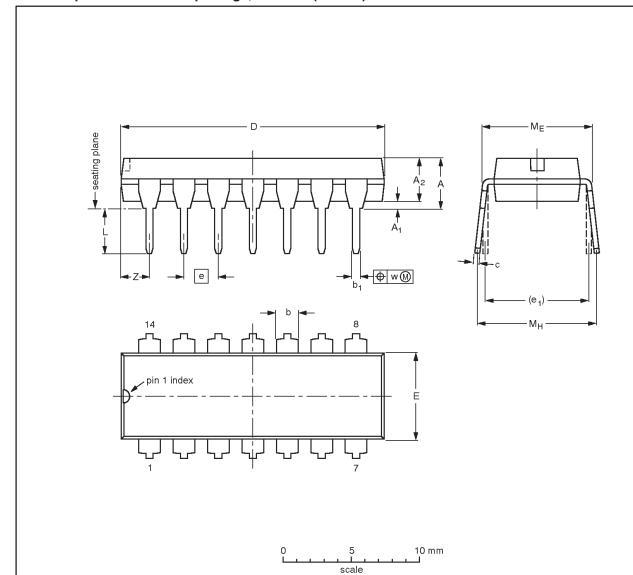
OUTLINE		REFER	RENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE
SOT108-1	076E06	MS-012			-97-05-22- 99-12-27

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DIP14: plastic dual in-line package; 14 leads (300 mil)

SOT27-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁ min.	A ₂ max.	b	b ₁	С	D ⁽¹⁾	E ⁽¹⁾	е	e ₁	L	ME	Мн	w	Z ⁽¹⁾ max.
mm	4.2	0.51	3.2	1.73 1.13	0.53 0.38	0.36 0.23	19.50 18.55	6.48 6.20	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	2.2
inches	0.17	0.020	0.13	0.068 0.044	0.021 0.015	0.014 0.009	0.77 0.73	0.26 0.24	0.10	0.30	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.087

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFEF	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC EIAJ		PROJECTION	ISSUE DATE	
SOT27-1	050G04	MO-001	SC-501-14		95-03-11 99-12-27	

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NOTES

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Data sheet status

Data sheet status ^[1]	Product status ^[2]	Definitions
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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