

Fast switching diode chip in EMCN 3-Technology

**FEATURES:**

- 1700V EMCN 3 technology 200 µm chip
- soft, fast switching
- low reverse recovery charge
- small temperature coefficient

**This chip is used for:**

- EUPEC power modules



**Applications:**

- resonant applications, drives

Chip Type	V <sub>R</sub>	I <sub>F</sub>	Die Size	Package	Ordering Code
SIDC32D170H	1700V	50A	5.7 x 5.7 mm <sup>2</sup>	sawn on foil	Q67050-A4174-A001

**MECHANICAL PARAMETER:**

Raster size	5.7 x 5.7	mm <sup>2</sup>
Area total / active	32.49 / 22.41	
Anode pad size	3.68 x 3.68	
Thickness	200	µm
Wafer size	150	mm
Flat position	180	deg
Max. possible chips per wafer	442 pcs	
Passivation frontside	Photoimide	
Anode metallization	3200 nm Al Si Cu	
Cathode metallization	Ni Ag -system suitable for epoxy and soft solder die bonding	
Die bond	electrically conductive glue or solder	
Wire bond	Al, ≤500µm	
Reject Ink Dot Size	Ø 0.65mm; max 1.2mm	
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C	

**Maximum Ratings**

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$		1700	V
Continuous forward current limited by $T_{jmax}$	$I_F$	$t_P = 10 \text{ ms sinusoidal}$	50	A
Single pulse forward current (depending on wire bond configuration)	$I_{FSM}$		310	
Maximum repetitive forward current limited by $T_{jmax}$	$I_{FRM}$		100	
Operating junction and storage temperature	$T_j, T_{stg}$		-55...+150	°C

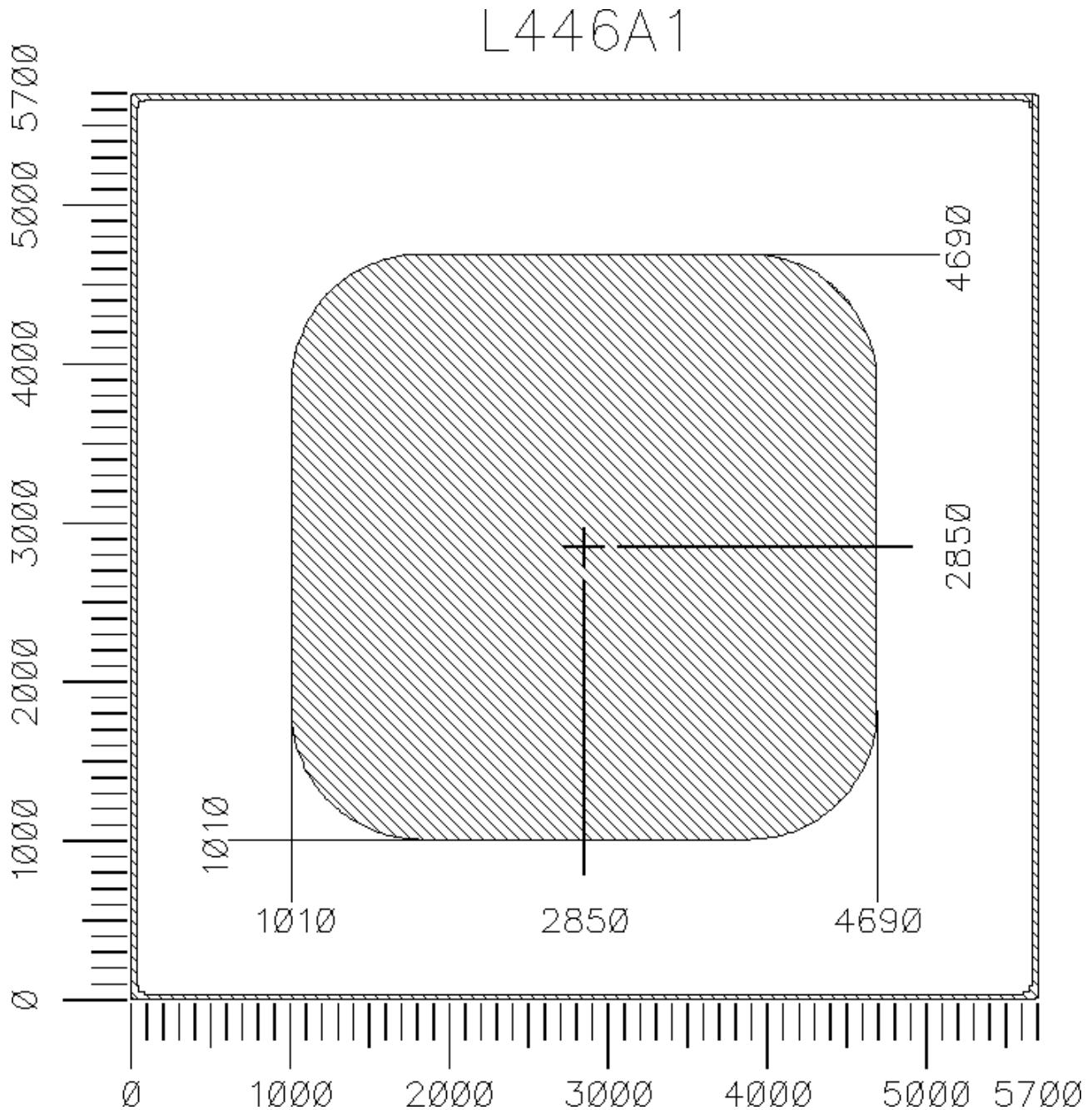
**Static Electrical Characteristics** (tested on chip),  $T_j=25 \text{ °C}$ , unless otherwise specified

Parameter	Symbol	Conditions		Value			Unit
				min.	Typ.	max.	
Reverse leakage current	$I_R$	$V_R=1700V$	$T_j=25 \text{ °C}$			27	µA
Cathode-Anode breakdown Voltage	$V_{Br}$	$I_R=0.25mA$	$T_j=25 \text{ °C}$	1700			V
Forward voltage drop	$V_F$	$I_F=50A$	$T_j=25 \text{ °C}$		1.8		V

**Dynamic Electrical Characteristics**, at  $T_j = 25 \text{ °C}$ , unless otherwise specified, tested at component

Parameter	Symbol	Conditions		Value			Unit
				min.	Typ.	max.	
Peak recovery current	$I_{RRM1}$	$I_F=50A$ $di/dt=730 \text{ A/ms}$ $V_R=900V$	$T_j = 25 \text{ °C}$		62		A
	$I_{RRM2}$		$T_j = 125 \text{ °C}$		67		
Reverse recovery charge	$Q_{rr1}$	$I_F=50A$ $di/dt=730 \text{ A/ms}$ $V_R=900V$	$T_j=25 \text{ °C}$		13.3		$\mu\text{C}$
	$Q_{rr2}$		$T_j=125 \text{ °C}$		21.7		
Reverse recovery energy	$E_{rec1}$	$I_F=50A$ $di/dt=730 \text{ A/ms}$ $V_R=900V$	$T_j=25 \text{ °C}$		6.7		$\text{mJ}$
	$E_{rec2}$		$T_j=125 \text{ °C}$		11.7		

CHIP DRAWING:



## FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the  
device data sheet

INFINEON TECHNOLOGIES /  
EUPEC

tbd

### Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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