## 2SD2358

### Silicon NPN epitaxial planar type

For low-frequency output amplification Complementary to 2SB1538

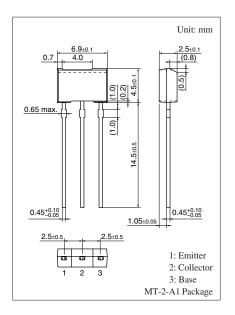
#### ■ Features

- Low collector-emitter saturation voltage  $V_{CE(sat)}$ : < 0.15 V
- Allowing supply with the radial taping

#### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	10	V	
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	10	V	
Emitter-base voltage (Collector open)	$V_{EBO}$	5	V	
Collector current	$I_{C}$	1	A	
Peak collector current	$I_{CP}$	1.2	A	
Collector power dissipation *	P <sub>C</sub>	1	W	
Junction temperature	$T_j$	150	°C	
Storage temperature	T <sub>sto</sub>	-55 to +150	°C	

Note) \*: Printed circuit board: Copper foil area of 1  $\rm cm^2$  or more, and the board thickness of 1.7 mm for the collector portion



### $\blacksquare$ Electrical Characteristics $~T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	$I_C = 10 \mu\text{A},  I_E = 0$	10			V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_C = 1 \text{ mA}, I_B = 0$	10			V
Emitter-base voltage (Collector open)	$V_{EBO}$	$I_E = 10 \ \mu A, I_C = 0$	5			V
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{CB} = 7 \text{ V}, I_{E} = 0$			1	μΑ
Forward current transfer ratio	$h_{FE}$	$V_{CE} = 2 \text{ V}, I_{C} = 100 \text{ mA}$	200		800	_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 500 \text{ mA}, I_B = 20 \text{ mA}$			0.15	V
Transition frequency	$f_T$	$V_{CB} = 5 \text{ V}, I_E = -50 \text{ mA}, f = 200 \text{ MHz}$		120		MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 20 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		30		pF
(Common base, input open circuited)						

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

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