SILICON PNP RF TRANSISTOR

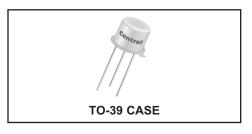


www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CM5160 is a silicon PNP RF transistor, mounted in a hermetically sealed package, designed for high frequency amplifier and non-saturated switching applications. This device is a replacement for the 2N5160.

MARKING: FULL PART NUMBER



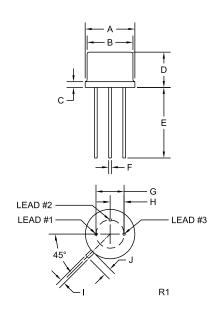
MAXIMUM RATINGS: (T _A =25°C unless otherwis	se noted)		
THANHOM RATINGS: (1A-23 O dilicas otherwise	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	60	V
Collector-Emitter Voltage	V_{CEO}	40	V
Emitter-Base Voltage	V_{EBO}	4.0	V
Continuous Collector Current	I _C	0.4	Α
Power Dissipation	P_{D}	1.0	W
Power Dissipation (T _C =25°C)	P_{D}	5.0	W
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +200	°C
Thermal Resistance	Θ_{JA}	175	°C/W
Thermal Resistance	Θ JC	35	°C/W
ELECTRICAL CHARACTERISTICS: (TA=25°C	unless otherwise no	oted)	
SYMBOL TEST CONDITIONS ICBO VCB=28V	MIN	MAX 1.0	UNITS μΑ

ELECTRICA	L CHARACTERISTICS: (TA=25°C uni	ess otherwise no	otea)	
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{CBO}	V _{CB} =28V		1.0	μΑ
ICES	V _{CE} =60V		100	μΑ
ICEO	V _{CE} =28V		20	μΑ
BVCEO	I _C =5.0mA	40		V
BV _{EBO}	I _E =100μA	4.0		V
h _{FE}	V_{CE} =5.0V, I_{C} =50mA	10		
fT	V_{CE} =15V, I_{C} =50mA, f=200MHz	500		MHz
C_{cb}	V_{CB} =28V, I_E =0, f=0.1 to 1.0MHz		4.0	pF

SILICON PNP RF TRANSISTOR



TO-39 CASE - MECHANICAL OUTLINE



DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
A (DIA)	0.335	0.370	8.51	9.40			
B (DIA)	0.315	0.335	8.00	8.51			
С	-	0.040	-	1.02			
D	0.240	0.260	6.10	6.60			
E	0.500	-	12.70	-			
F (DIA)	0.016	0.021	0.41	0.53			
G (DIA)	0.200		5.08				
Н	0.100		2.54				
	0.028	0.034	0.71	0.86			
J	0.029	0.045	0.74	1.14			

TO-39 (REV: R1)

LEAD CODE:

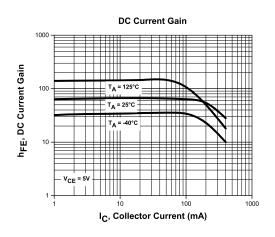
- 1) Emitter 2) Base
- 3) Collector

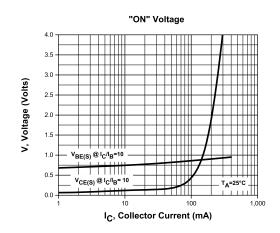
MARKING: FULL PART NUMBER

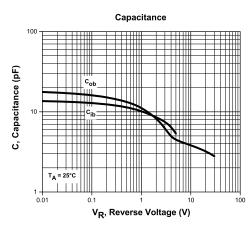
SILICON PNP RF TRANSISTOR

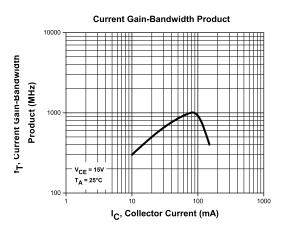


TYPICAL ELECTRICAL CHARACTERISTICS





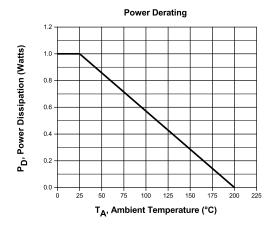


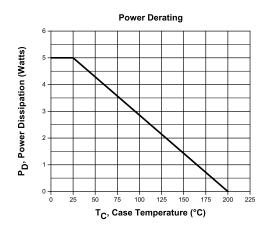


SILICON PNP RF TRANSISTOR



TYPICAL ELECTRICAL CHARACTERISTICS





OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- · Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA

Main Tel: (631) 435-1110 Main Fax: (631) 435-1824

Support Team Fax: (631) 435-3388

www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

Worldwide Distributors:

www.centralsemi.com/wwdistributors

For the latest version of Central Semiconductor's **LIMITATIONS AND DAMAGES DISCLAIMER**, which is part of Central's Standard Terms and Conditions of sale, visit: www.centralsemi.com/terms

www.centralsemi.com (001)