

## DUAL CHANNELS CURRENT SENSE HIGH SIDE SWITCH

### Features

- 24V battery system
- Over current shutdown
- Over temperature shutdown
- Current sensing
- Active clamp
- Low quiescent current
- ESD protection
- Optimized Turn On/Off for EMI
- Lead free and RoHS compliant

### Applications

- 21W Filament lamp
- Solenoid
- 24V truck loads

### Description

The AUIPS7142G is a fully protected dual high side switch specifically designed for driving lamp. It features current sensing, over-current, over-temperature, ESD protection and drain to source active clamp. The Ifb pin is used for current sensing. The over-current shutdown is higher than inrush current of the lamp.

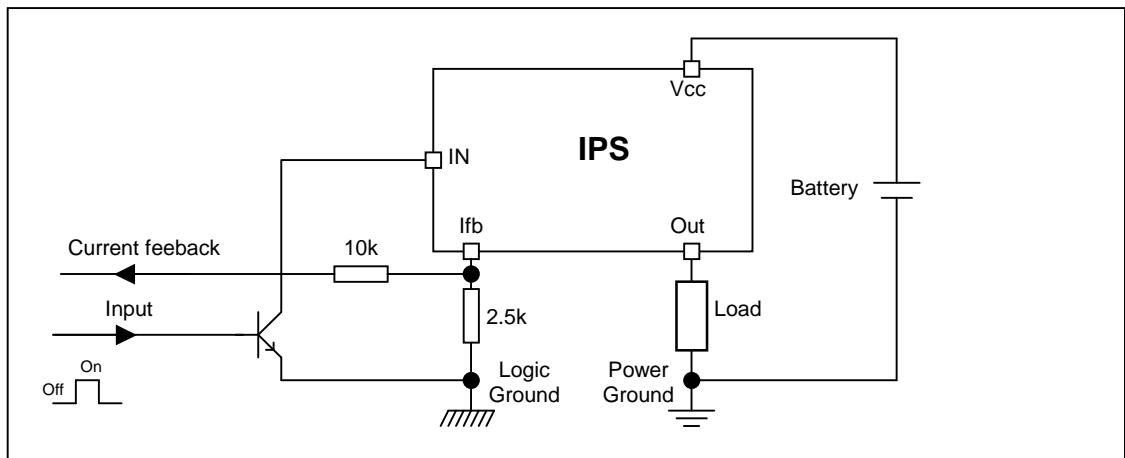
### Product Summary

Rds(on)	100mΩ max.
Vclamp	65V
Current shutdown	20A min.

### Package



### Typical Connection



**Qualification Information<sup>†</sup>**

<b>Qualification Level</b>		Automotive (per AEC-Q100 <sup>††</sup> )	
Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.			
<b>Moisture Sensitivity Level</b>		SOIC-16L WB	MSL2, 260°C (per IPC/JEDEC J-STD-020)
<b>ESD</b>	Machine Model	Class <b>M4</b> (+/-450V) (per AEC-Q100-003)	
	Human Body Model	Class <b>H2</b> (+/-4000V) (per AEC-Q100-002)	
	Charged Device Model	Class <b>C4</b> (+/-1000V) (per AEC-Q100-011)	
<b>IC Latch-Up Test</b>		Class II, Level A (per AEC-Q100-004)	
<b>RoHS Compliant</b>		Yes	

<sup>†</sup> Qualification standards can be found at International Rectifier's web site <http://www.irf.com/>

<sup>††</sup> Exceptions to AEC-Q100 requirements are noted in the qualification report.

## Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. ( $T_j = -40^\circ\text{C}..150^\circ\text{C}$ ,  $V_{cc}=6..50\text{V}$  unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
$V_{out}$	Maximum output voltage	$V_{cc}-60$	$V_{cc}+0.3$	V
$I_{rev}$	Maximum reverse pulsed current ( $t=100\mu\text{s}$ ) see page 8	—	30	A
$I_{sd\ cont.}$	Maximum diode continuous current $T_{ambient}=25^\circ\text{C}$ , $R_{th}=40^\circ\text{C}/\text{W}$ / per channel	—	1.7	
$V_{cc-Vin\ max.}$	Maximum $V_{cc}$ voltage	-16	60	V
$I_{ifb,\ max.}$	Maximum feedback current	-50	10	mA
$V_{cc\ sc.}$	Maximum $V_{cc}$ voltage with short circuit protection see page 8	—	50	V
$P_d$	Maximum power dissipation (internally limited by thermal protection) $R_{th}=40^\circ\text{C}/\text{W}$	—	3	W
$T_{j\ max.}$	Max. storage & operating junction temperature	-40	150	

## Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{th1}$	Thermal resistance junction to ambient $6\text{cm}^2$ footprint one Mosfet on	45	—	$^\circ\text{C}/\text{W}$
$R_{th2}$	Thermal resistance junction to ambient $6\text{cm}^2$ footprint two Mosfet on	40	—	

note :  $T_j-T_{ambient}=\text{Power dissipated in the 2 channel} \times R_{th}$

## Recommended Operating Conditions

These values are given for a quick design.

Symbol	Parameter	Min.	Max.	Units
$I_{out}$	Continuous output current, $T_{ambient}=85^\circ\text{C}$ , $T_j=125^\circ\text{C}$ $R_{th}=40^\circ\text{C}/\text{W}$ , $6\text{cm}^2$ footprint	—	1.5	A
$R_{ifb}$	$I_{fb}$ resistor	1.5	—	$\text{k}\Omega$

## Static Electrical Characteristics

T<sub>j</sub>=-40°C..150°C, V<sub>cc</sub>=6-50V (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
V <sub>cc</sub> op.	Operating voltage	6	—	60	V	
R <sub>ds(on)</sub>	ON state resistance T <sub>j</sub> =25°C	—	75	100	mΩ	I <sub>ds</sub> =2A
	ON state resistance T <sub>j</sub> =150°C(2)	—	135	180		
I <sub>cc</sub> off	Supply leakage current	—	1	3	μA	V <sub>in</sub> =V <sub>cc</sub> / V <sub>ifb</sub> =V <sub>gnd</sub>
I <sub>out</sub> off	Output leakage current	—	1	3	μA	V <sub>out</sub> =V <sub>gnd</sub> , T <sub>j</sub> =25°C
I <sub>in</sub> on	Input current while on	0.6	2	4	mA	V <sub>cc</sub> -V <sub>in</sub> =28V, T <sub>j</sub> =25°C
V clamp1	V <sub>cc</sub> to V <sub>out</sub> clamp voltage 1	60	64	—	V	I <sub>d</sub> =10mA
V clamp2	V <sub>cc</sub> to V <sub>out</sub> clamp voltage 2	60	65	72		I <sub>d</sub> =6A see fig. 2
V <sub>ih(1)</sub>	High level Input threshold voltage	—	3	5		I <sub>d</sub> =10mA
V <sub>il(1)</sub>	Low level Input threshold voltage	1.5	2.3	—		
V <sub>f</sub>	Forward body diode voltage T <sub>j</sub> =25°C	—	0.8	0.9		I <sub>f</sub> =1A
	Forward body diode voltage T <sub>j</sub> =125°C	—	0.65	0.75		

(1) Input thresholds are measured directly between the input pin and V<sub>cc</sub>.

## Switching Electrical Characteristics

V<sub>cc</sub>=28V, Resistive load=27Ω, T<sub>j</sub>=-40°C..150°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
T <sub>don</sub>	Turn on delay time to 20%	4	10	20	μs	See fig. 1
T <sub>r</sub>	Rise time from 20% to 80% of V <sub>cc</sub>	2	5	10		
T <sub>doff</sub>	Turn off delay time	20	40	80		
T <sub>f</sub>	Fall time from 80% to 20% of V <sub>cc</sub>	2.5	5	10		

## Protection Characteristics

T<sub>j</sub>=-40°C..150°C, V<sub>cc</sub>=6-50V (unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
T <sub>sd</sub>	Over temperature threshold	150(2)	165	—	°C	See fig. 3 and fig.11
I <sub>sd</sub>	Over-current shutdown	20	25	37	A	See fig. 3 and page 7
I <sub>fault</sub>	I <sub>fb</sub> after an over-current or an over-temperature (latched)	2.2	3	5	mA	See fig. 3

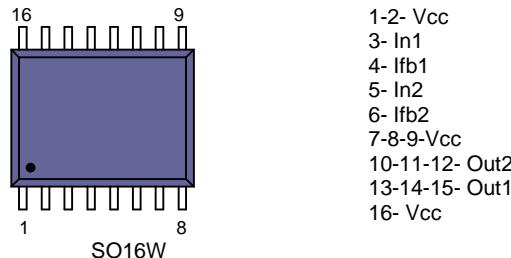
## Current Sensing Characteristics

T<sub>j</sub>=-40°C..150°C, V<sub>cc</sub>=6-50V (unless otherwise specified). Specified 500μs after the turn on. V<sub>cc</sub>-V<sub>ifb</sub>>4V

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Ratio	I <sub>load</sub> / I <sub>fb</sub> current ratio	2000	2400	2800		I <sub>load</sub> <4A
Ratio_TC	I <sub>load</sub> / I <sub>fb</sub> variation over temperature(2)	-5%	0	+5	%	T <sub>j</sub> =-40°C to +150°C
I <sub>offset</sub>	Load current offset	-0.02	0	0.02	A	I <sub>out</sub> <4A
I <sub>fb</sub> leakage	I <sub>fb</sub> leakage current On in open load	0	1	10	μA	I <sub>out</sub> =0A, V <sub>cc</sub> -V <sub>in</sub> =28V

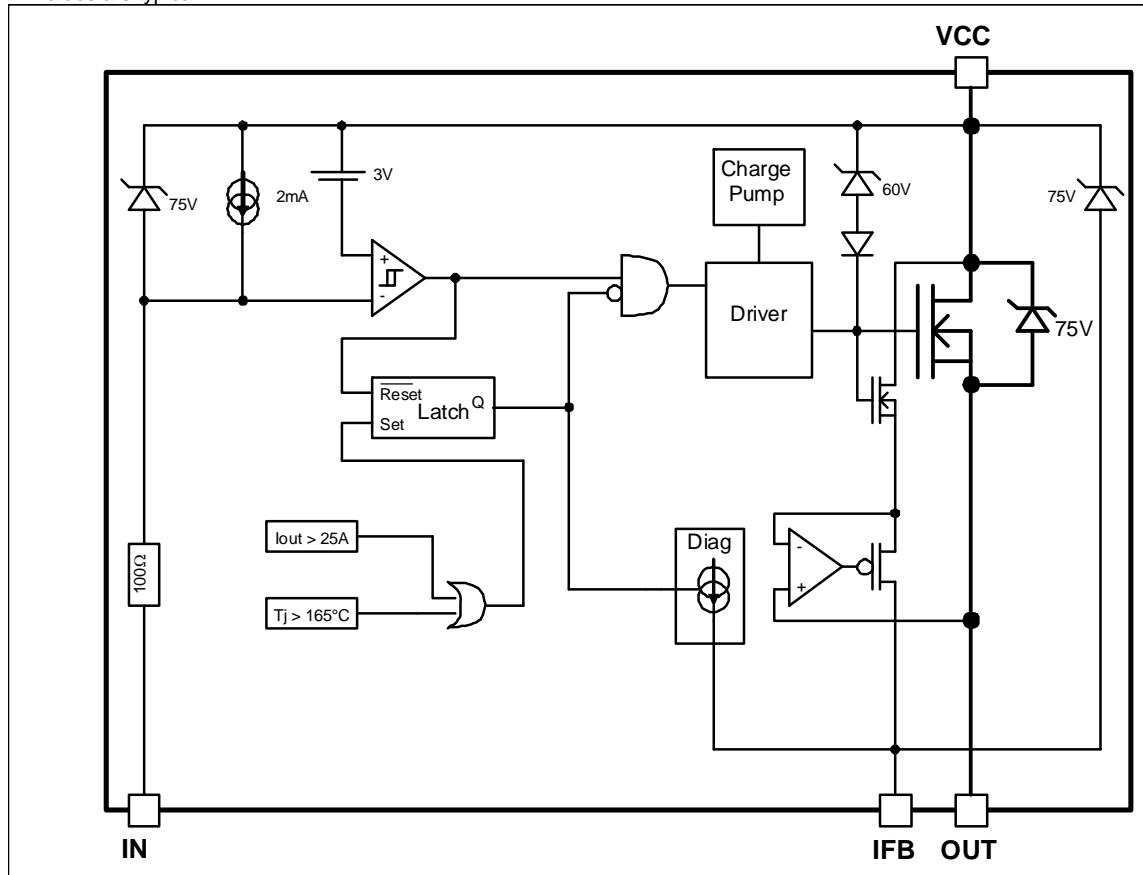
(2) Guaranteed by design

## Lead Assignments



## Functional Block Diagram

All values are typical



## Truth Table

Op. Conditions	Input	Output	Ifb pin voltage
Normal mode	H	L	0V
Normal mode	L	H	I load x Rfb / Ratio
Open load	H	L	0V
Open load	L	H	0V
Short circuit to GND	H	L	0V
Short circuit to GND	L	L	V fault (latched)
Over temperature	H	L	0V
Over temperature	L	L	V fault (latched)

## Operating voltage

**Maximum Vcc voltage** : this is the maximum voltage before the breakdown of the IC process.

**Operating voltage** : This is the Vcc range in which the functionality of the part is guaranteed. The AEC-Q100 qualification is run at the maximum operating voltage specified in the datasheet.

## Reverse battery

During the reverse battery the Mosfet is kept off and the load current is flowing into the body diode of the power Mosfet.

Power dissipation in the IPS :  $P = I \text{ load} \cdot V_f$

There is no protection, so  $T_j$  must be lower than 150°C in the worst case condition of current and ambient temperature.

If the power dissipation is too high in  $R_{fb}$ , a diode in serial can be added to block the current.

The transistor used to pull-down the input should be a bipolar in order to block the reverse current. The 100ohm input resistor can not sustain continuously 16V (see Vcc-Vin max. in the Absolute Maximum Ratings section)

## Active clamp

The purpose of the active clamp is to limit the voltage across the MOSFET to a value below the body diode break down voltage to reduce the amount of stress on the device during switching.

The temperature increase during active clamp can be estimated as follows:

$$\Delta T_j = P_{CL} \cdot Z_{TH}(t_{CLAMP})$$

Where:  $Z_{TH}(t_{CLAMP})$  is the thermal impedance at  $t_{CLAMP}$  and can be read from the thermal impedance curves given in the data sheets.

$P_{CL} = V_{CL} \cdot I_{CLavg}$  : Power dissipation during active clamp

$V_{CL} = 65V$  : Typical  $V_{CLAMP}$  value.

$I_{CLavg} = \frac{I_{CL}}{2}$  : Average current during active clamp

$t_{CL} = \frac{I_{CL}}{\frac{|di|}{|dt|}}$  : Active clamp duration

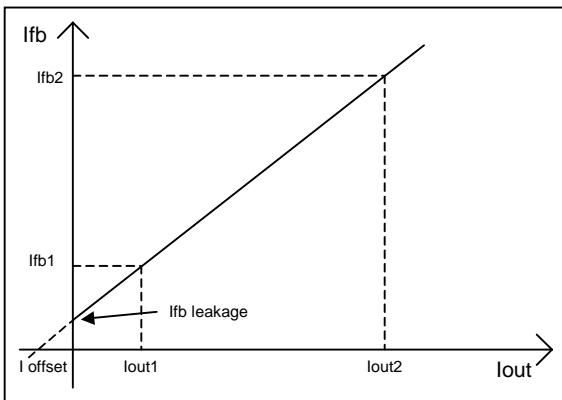
$\frac{di}{dt} = \frac{V_{Battery} - V_{CL}}{L}$  : Demagnetization current

Figure 9 gives the maximum inductance versus the load current in the worst case : the part switch off after an over temperature detection. If the load inductance exceed the curve, a free wheeling diode is required.

## Over-current protection

The threshold of the over-current protection is set in order to guarantee that the device is able to turn on a load with an inrush current lower than the minimum of  $I_{sd}$ . Nevertheless for high current and high temperature the device may switch off for a lower current due to the over-temperature protection. This behavior is shown in Figure 11.

## Current sensing accuracy



The current sensing is specified by measuring 3 points :

- Ifb1 for Iout1
- Ifb2 for Iout2
- Ifb leakage for Iout=0

The parameters in the datasheet are computed with the following formula :

$$\text{Ratio} = (I_{out2} - I_{out1}) / (Ifb2 - Ifb1)$$

$$I_{offset} = Ifb1 \times \text{Ratio} - I_{out1}$$

This allows the designer to evaluate the Ifb for any Iout value using :

$$Ifb = (I_{out} + I_{offset}) / \text{Ratio} \text{ if } Ifb > Ifb \text{ leakage}$$

For some applications, a calibration is required. In that case, the accuracy of the system will depends on the variation of the  $I_{offset}$  and the ratio over the temperature range. The ratio variation is given by  $\text{Ratio\_TC}$  specified on page 4.

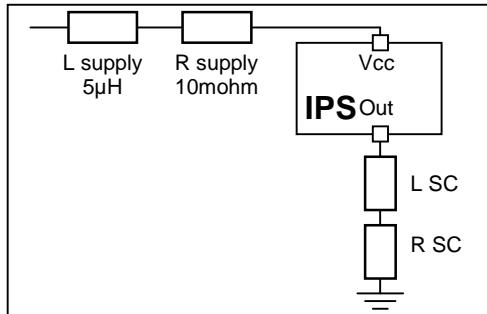
The  $I_{offset}$  variation depends directly on the  $R_{dson}$  :

$$I_{offset}@-40^{\circ}\text{C} = I_{offset}@25^{\circ}\text{C} / 0.8$$

$$I_{offset}@150^{\circ}\text{C} = I_{offset}@25^{\circ}\text{C} / 1.9$$

## Maximum Vcc voltage with short circuit protection

The maximum Vcc voltage with short circuit is the maximum voltage for which the part is able to protect itself under test conditions representative of the application. 2 kind of short circuits are considered : terminal and load short circuit.



	L SC	R SC
Terminal SC	0.1 µH	10 mohm
Load SC	10 µH	100 mohm

## Maximum current during reverse circulation

In case of short circuit to battery, a voltage drop of the Vcc may create a current which circulate in reverse mode. When the device is on, this reverse circulation current will not trigger the internal fault latch. This immunization is also true when the part turns on while a reverse current flows into the device. The maximum current ( $I_{rev}$ ) is specified in the maximum rating section.

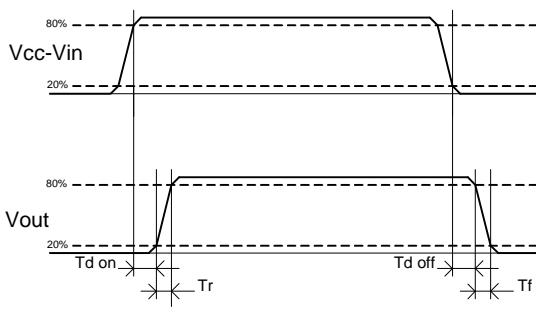


Figure 1 – IN rise time & switching definitions

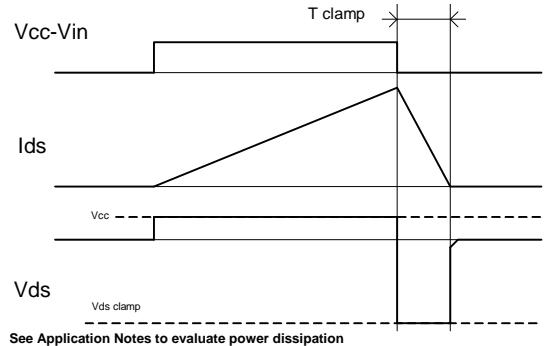


Figure 2 – Active clamp waveforms

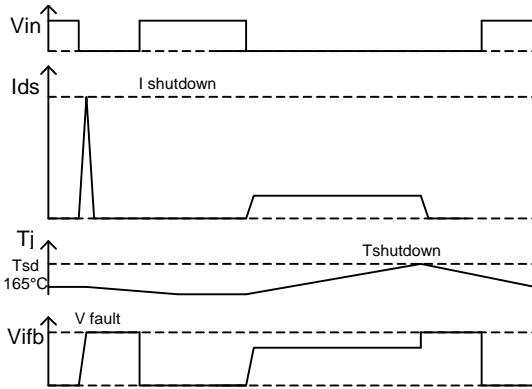


Figure 3 – Protection timing diagram

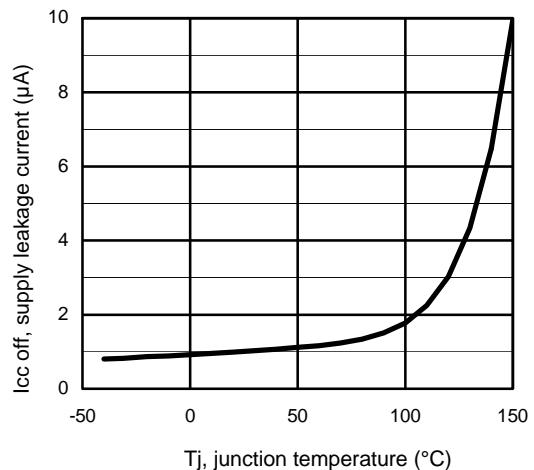


Figure 4 –  $I_{cc\ off}$  ( $\mu$ A) Vs  $T_j$  (°C)

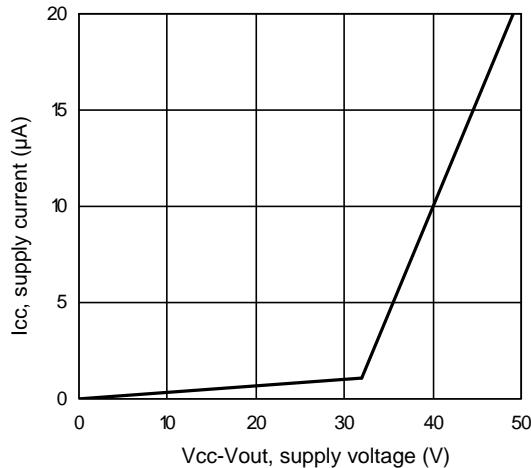


Figure 5 –  $I_{cc\ off}$  ( $\mu$ A) Vs  $V_{cc\ -V_{out}}$  (V)

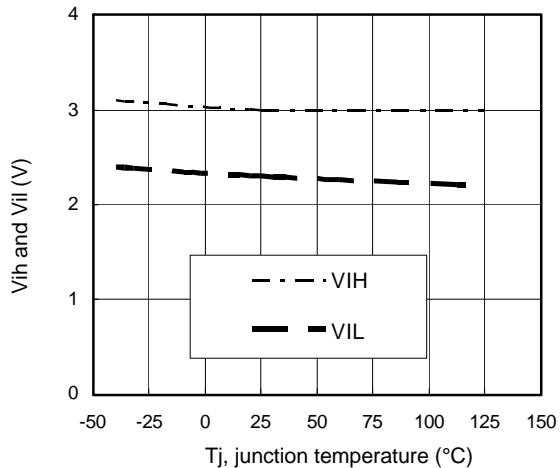


Figure 6 –  $Vi_H$  and  $Vi_L$  (V) Vs  $T_j$  (°C)

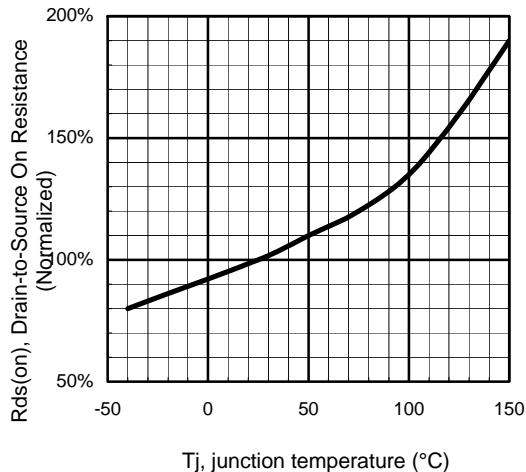


Figure 7 - Normalized  $R_{ds(on)}$  (%) Vs  $T_j$  (°C)

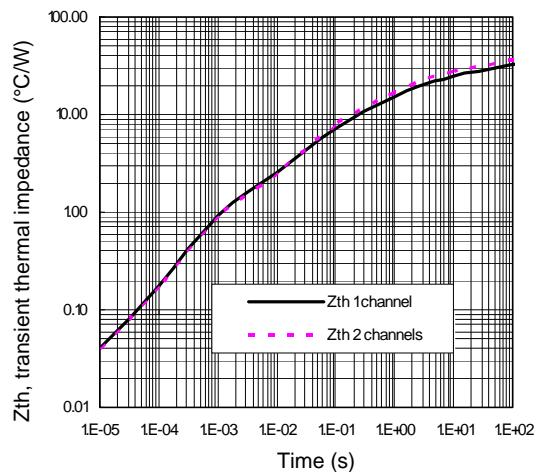


Figure 8 – Transient thermal impedance (°C/W) Vs time (s)

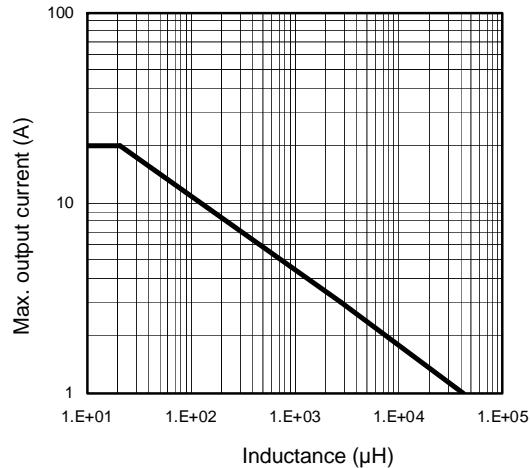


Figure 9 – Max. Iout (A) Vs inductance (μH)

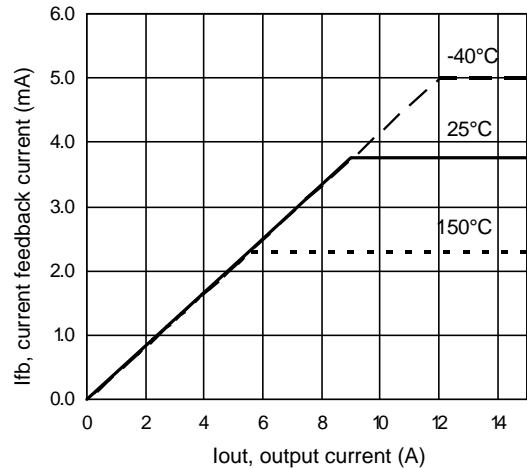


Figure 10 – Ifb (mA) Vs Iout (A)

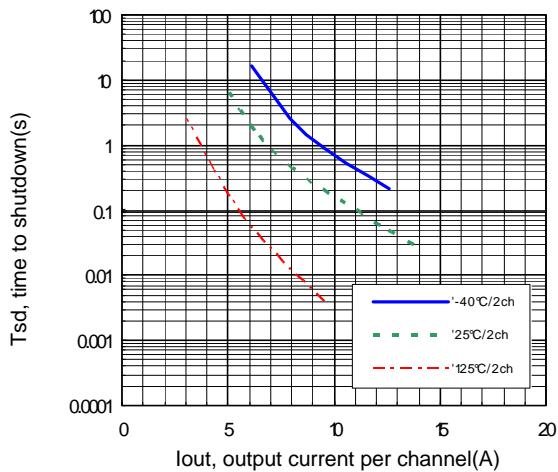
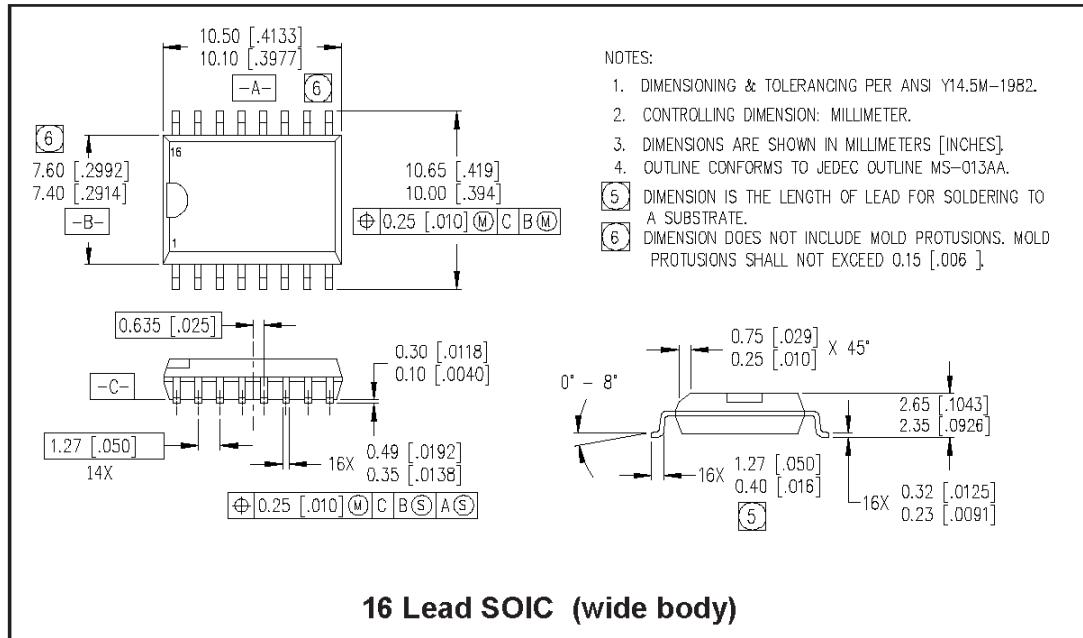
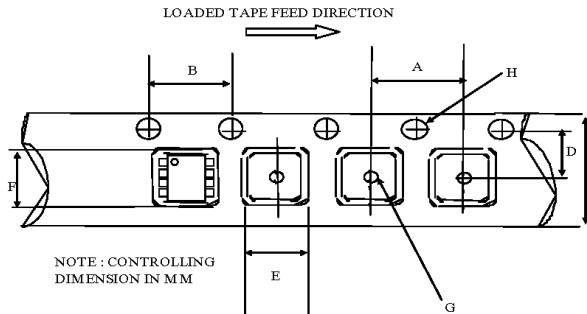


Figure 11 – Tsd (s) Vs Iout (A)  
2 channels on

**Case Outline SOIC16W**

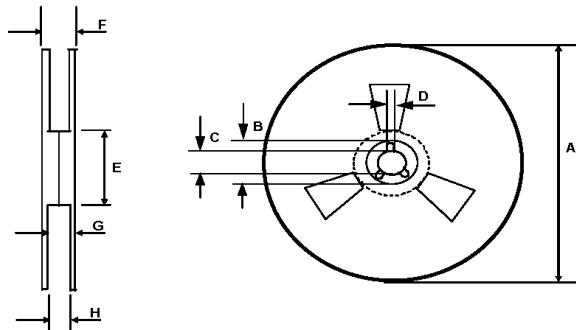


## Tape and Reel – SOIC16W



CARRIER TAPE DIMENSION FOR 16SOICW

Code	Metric		Imperial	
	Min	Max	Min	Max
A	11.90	12.10	0.468	0.476
B	3.90	4.10	0.153	0.161
C	15.70	16.30	0.618	0.641
D	7.40	7.60	0.291	0.299
E	10.80	11.00	0.425	0.433
F	10.60	10.80	0.417	0.425
G	1.50	n/a	0.059	n/a
H	1.50	1.60	0.059	0.062



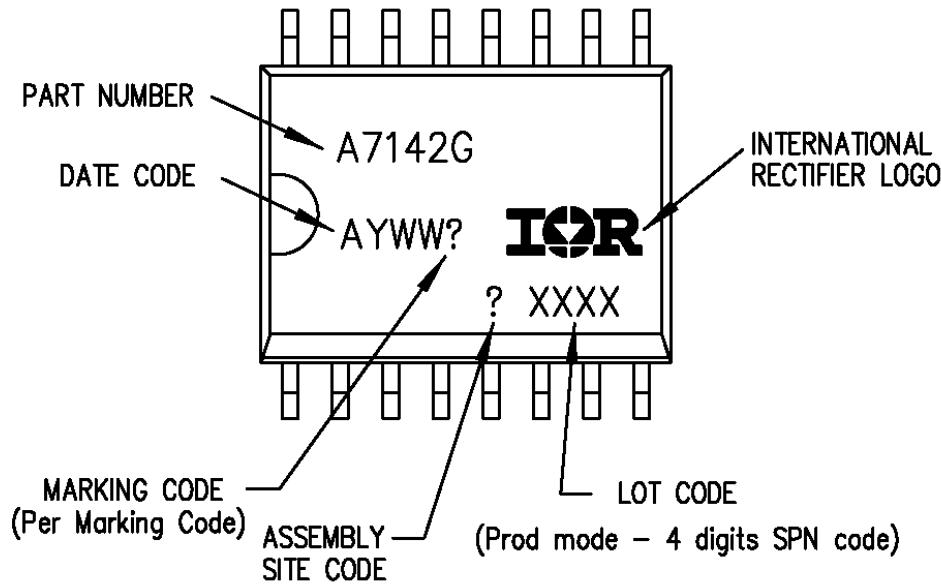
REEL DIMENSIONS FOR 16SOICW

Code	Metric		Imperial	
	Min	Max	Min	Max
A	329.60	330.25	12.976	13.001
B	20.95	21.45	0.824	0.844
C	12.80	13.20	0.503	0.519
D	1.95	2.45	0.767	0.096
E	98.00	102.00	3.858	4.015
F	n/a	22.40	n/a	0.881
G	18.50	21.10	0.728	0.830
H	16.40	18.40	0.645	0.724



## Part Marking Information

### TOP MARKING (LASER)



## Ordering Information

Base Part Number	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIPS7142G	SO28W	Tube	45	AUIPS7142G
		Tape and reel	1500	AUIPS7142GTR

## **IMPORTANT NOTICE**

Unless specifically designated for the automotive market, International Rectifier Corporation and its subsidiaries (IR) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or services without notice. Part numbers designated with the "AU" prefix follow automotive industry and / or customer specific requirements with regards to product discontinuance and process change notification. All products are sold subject to IR's terms and conditions of sale supplied at the time of order acknowledgment.

IR warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with IR's standard warranty. Testing and other quality control techniques are used to the extent IR deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

IR assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using IR components. To minimize the risks with customer products and applications, customers should provide adequate design and operating safeguards.

Reproduction of IR information in IR data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alterations is an unfair and deceptive business practice. IR is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of IR products or serviced with statements different from or beyond the parameters stated by IR for that product or service voids all express and any implied warranties for the associated IR product or service and is an unfair and deceptive business practice. IR is not responsible or liable for any such statements.

IR products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or in any other application in which the failure of the IR product could create a situation where personal injury or death may occur. Should Buyer purchase or use IR products for any such unintended or unauthorized application, Buyer shall indemnify and hold International Rectifier and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that IR was negligent regarding the design or manufacture of the product.

IR products are neither designed nor intended for use in military/aerospace applications or environments unless the IR products are specifically designated by IR as military-grade or "enhanced plastic." Only products designated by IR as military-grade meet military specifications. Buyers acknowledge and agree that any such use of IR products which IR has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

IR products are neither designed nor intended for use in automotive applications or environments unless the specific IR products are designated by IR as compliant with ISO/TS 16949 requirements and bear a part number including the designation "AU". Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, IR will not be responsible for any failure to meet such requirements.

For technical support, please contact IR's Technical Assistance Center

<http://www.irf.com/technical-info/>

**WORLD HEADQUARTERS:**

101 N Sepulbeda Blvd., El Segundo, California 90245

Tel: (310) 252-7105

## Revision History

Revision	Date	Notes/Changes
A3	April, 29 <sup>th</sup> 2010	Add tri-temp limits
A4	March, 17 <sup>th</sup> 2011	Au release
A5	March, 18 <sup>th</sup> 2011	Update lead free and RoHS 1 <sup>st</sup> page
A6	March, 24 <sup>th</sup> 2011	Add Tape and reel information