

< IGBT MODULES >

CM300DY-34A

HIGH POWER SWITCHING USE INSULATED TYPE

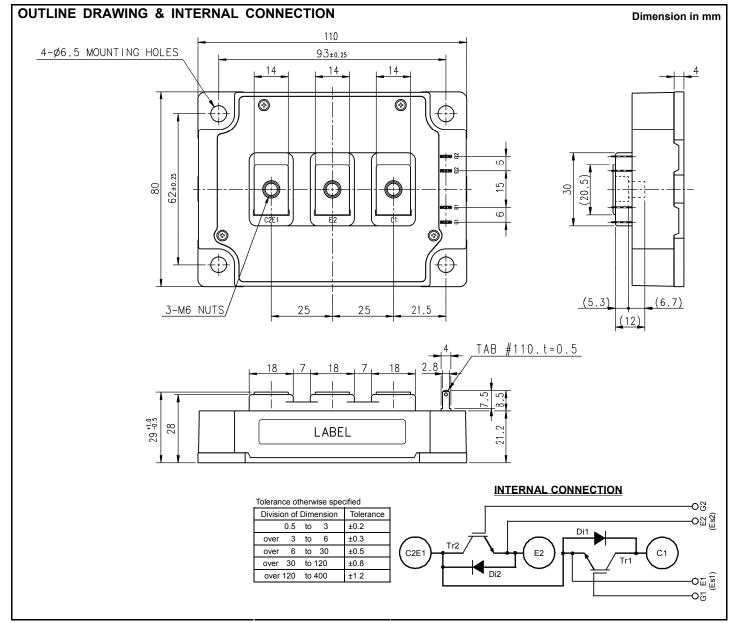


Dual (Half-Bridge)

- Flat base Type
- Copper base plate
- RoHS Directive compliant
- •UL Recognized under UL1557, File E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



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ABSOLUTE MAXIMUM RATINGS (T _i =25 °C, unless oth	nerwise specified)
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Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1700	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	±20	V
Ic	Collector current	DC, T _C =108 °C (Note.2, 4)	300	۸
I _{CRM}	Collector current	Pulse, Repetitive (Note.3)	600	Α
P _{tot}	Total power dissipation	T _C =25 °C (Note.2, 4)	2900	W
I _E (Note.1)	Emitter current	T _C =25 °C (Note.2, 4)	300	۸
I _{ERM} (Note.1)	Enliner current	Pulse, Repetitive (Note.3)	600	Α
Tj	Junction temperature	-	-40 ~ +150	°C
T _{stg}	Storage temperature -		-40 ~ +125	C
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	3500	V

ELECTRICAL CHARACTERISTICS (T_i=25 °C, unless otherwise specified)

Symbol	Item	Conditions		Limits			Unit
Symbol	item			Min.	Тур.	Max.	Ullit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited	V _{CE} =V _{CES} , G-E short-circuited		-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited	V _{GE} =V _{GES} , C-E short-circuited		-	2.0	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =30 mA, V _{CE} =10 V	I _C =30 mA, V _{CE} =10 V		7.0	8.5	V
V _{CEsat}	Collector-emitter saturation voltage	I _C =300 A (Note.5),	T _j =25 °C	-	2.2	2.8	V
V CEsat	Collector-entitler saturation voltage	V _{GE} =15 V,	T _j =125 °C	-	2.45	-	V
Cies	Input capacitance	V _{CE} =10 V, G-E short-circuited		-	-	74	
Coes	Output capacitance			-	-	8.4	nF
Cres	Reverse transfer capacitance			-	-	1.6	
Q_G	Gate charge	V _{CC} =1000 V, I _C =300 A, V _{GE} =15 V		-	2000	-	nC
t _{d(on)}	Turn-on delay time	─ V _{CC} =1000 V, I _C =300 A, V _{GE} =±15 V,		-	-	600	- ns
t _r	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time	R_G =1.6 Ω, Inductive load		-	-	850	
t _f	Fall time			-	-	350	
V _{EC} (Note.1)	Emitter-collector voltage	I _E =300 A (Note.5), G-E short-cir	I _E =300 A (Note.5), G-E short-circuited		2.3	3.0	V
t _{rr} (Note.1)	Reverse recovery time	V _{CC} =1000 V, I _E =300 A, V _{GE} =±15 V,		-	-	450	ns
Q _{rr} (Note.1)	Reverse recovery charge	R _G =1.6 Ω, Inductive load		-	30	-	μC
Eon	Turn-on switching energy per pulse	V_{CC} =1000 V, I_{C} = I_{E} =300 A, V_{GE} =±15 V, R_{G} =1.6 Ω, T_{j} =125 °C,		-	185.5	-	mJ
E _{off}	Turn-off switching energy per pulse			-	77.9	-	1113
E _{rr} (Note.1)	Reverse recovery energy per pulse	Inductive load		-	63.9	-	mJ
r _g	Internal gate resistance	Per switch, T _c =25 °C		-	5.0	-	Ω

THERMAL RESISTANCE CHARACTERISTICS

	EGIG IAITOE GIIAITAG I EITIG 1100					
Symbol Item	Itom	Conditions	Limits			Unit
	Conditions	Min.	Тур.	Max.	Offic	
$R_{th(j-c)Q}$	Thermal resistance (Note.2)	Junction to case, per IGBT	-	-	43	K/kW
$R_{th(j-c)D}$		Junction to case, per FWDi	-	-	72	K/kW
R _{th(c-s)} Contac	Contact thermal resistance (Note.2)	Case to heat sink, per 1/2 module,	-	20	-	K/kW
		Thermal grease applied (Note.6)				r/KVV

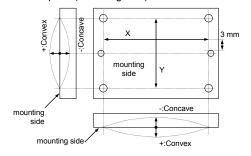
MECHANICAL CHARACTERISTICS

Symbol Item	Itom	Conditions		Limits			Unit
	Conditions		Min.	Тур.	Max.	Offic	
M _t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms		Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N·m
m	Weight	-		-	580	-	g
ec	Flatness of base plate	On the centerline X, Y (Note.7)		-100	-	+100	μm

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Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (FWDi).

- 2. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - The heat sink thermal resistance should measure just under the chips.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_i) dose not exceed T_{imax} rating.
- 4. Junction temperature (T_j) should not increase beyond T_{jmax} rating.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
- 6. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
- 7. Base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.

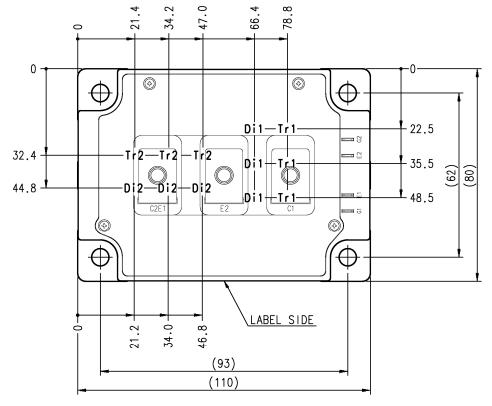


RECOMMENDED OPERATING CONDITIONS

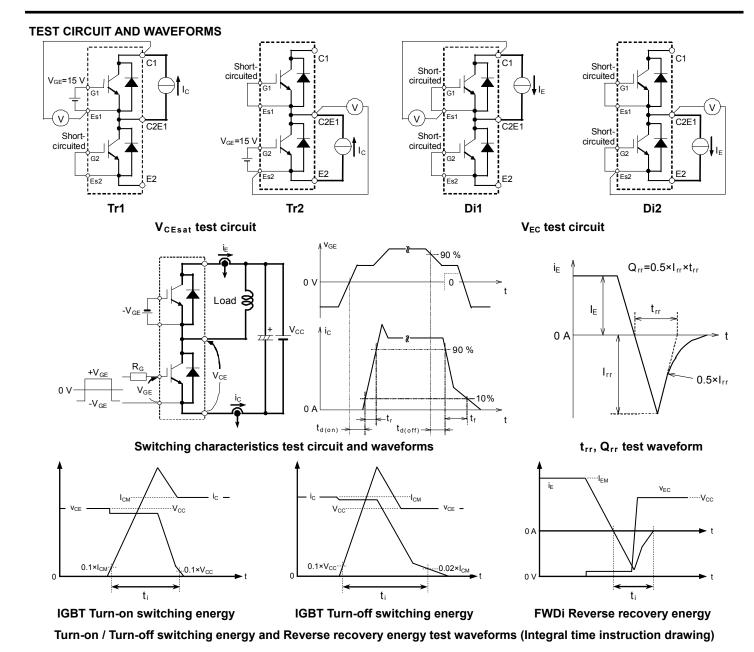
Symbol	Item	Conditions	Limits			Unit
			Min.	Тур.	Max.	Offic
Vcc	(DC) Supply voltage	Applied across C1-E2	-	1000	1100	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	1.6	-	16	Ω

CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm

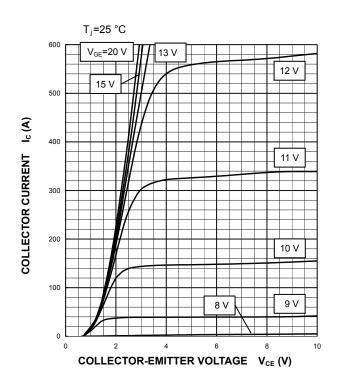


Tr1/Tr2: IGBT, Di1/Di2: FWDi

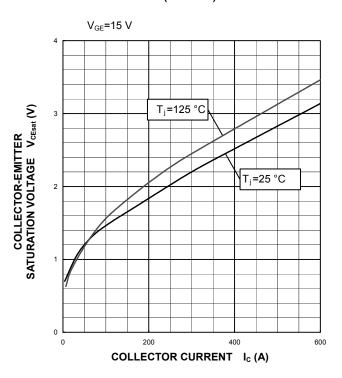


PERFORMANCE CURVES

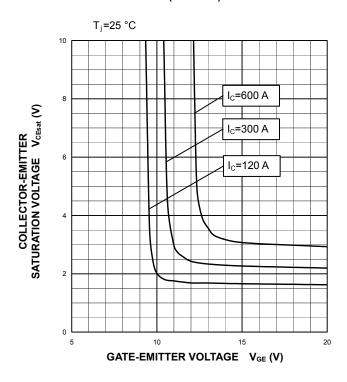
OUTPUT CHARACTERISTICS (TYPICAL)



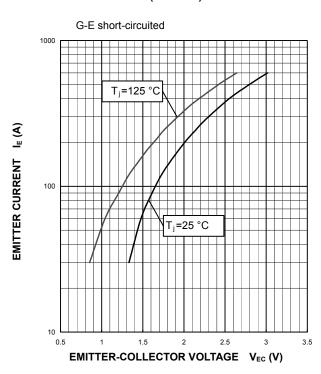
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



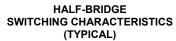
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



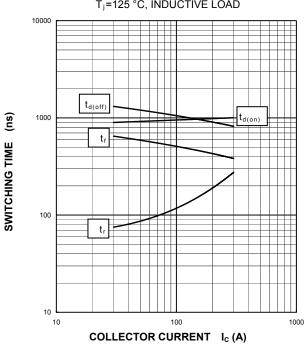
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



PERFORMANCE CURVES

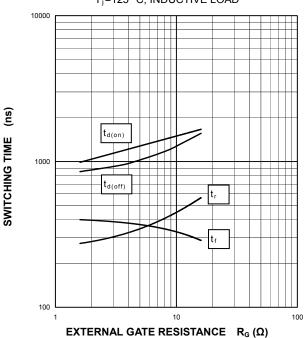


 V_{CC} =1000 V, V_{GE} =±15 V, R_{G} =1.6 Ω , T_j=125 °C, INDUCTIVE LOAD

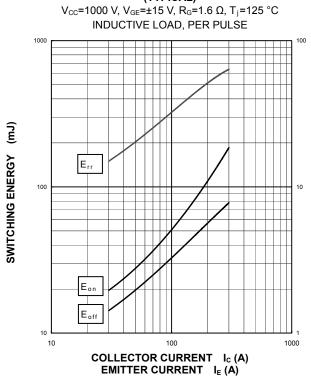


HALF-BRIDGE **SWITCHING CHARACTERISTICS** (TYPICAL)

 V_{CC} =1000 V, I_{C} =300 A, V_{GE} =±15 V, T_j=125 °C, INDUCTIVE LOAD

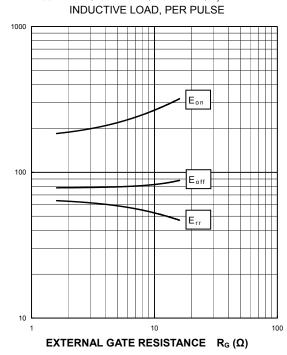


HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE **SWITCHING CHARACTERISTICS** (TYPICAL)

 V_{CC} =1000 V, I_C/I_E =300 A, V_{GE} =±15 V, T_j =125 °C



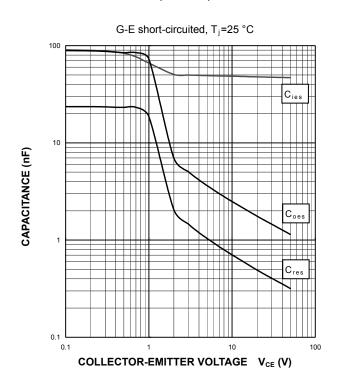
REVERSE RECOVERY ENERGY (mJ)

(m)

SWITCHING ENERGY (mJ) REVERSE RECOVERY ENERGY

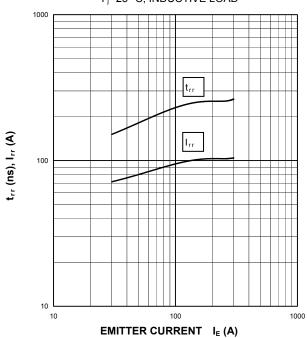
PERFORMANCE CURVES

CAPACITANCE CHARACTERISTICS (TYPICAL)

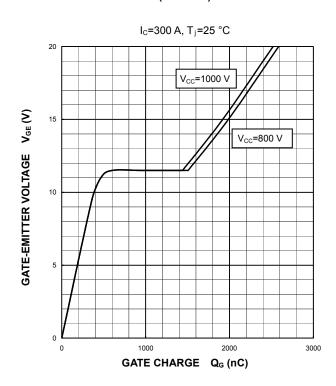


FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

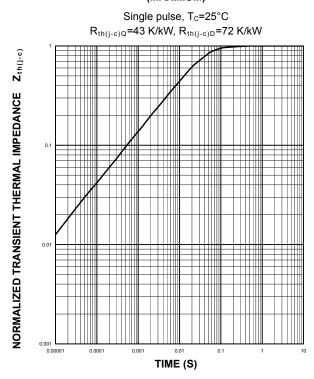
 V_{CC} =1000 V, V_{GE} =±15 V, R_{G} =1.6 Ω , T_{j} =25 °C, INDUCTIVE LOAD



GATE CHARGE CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



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