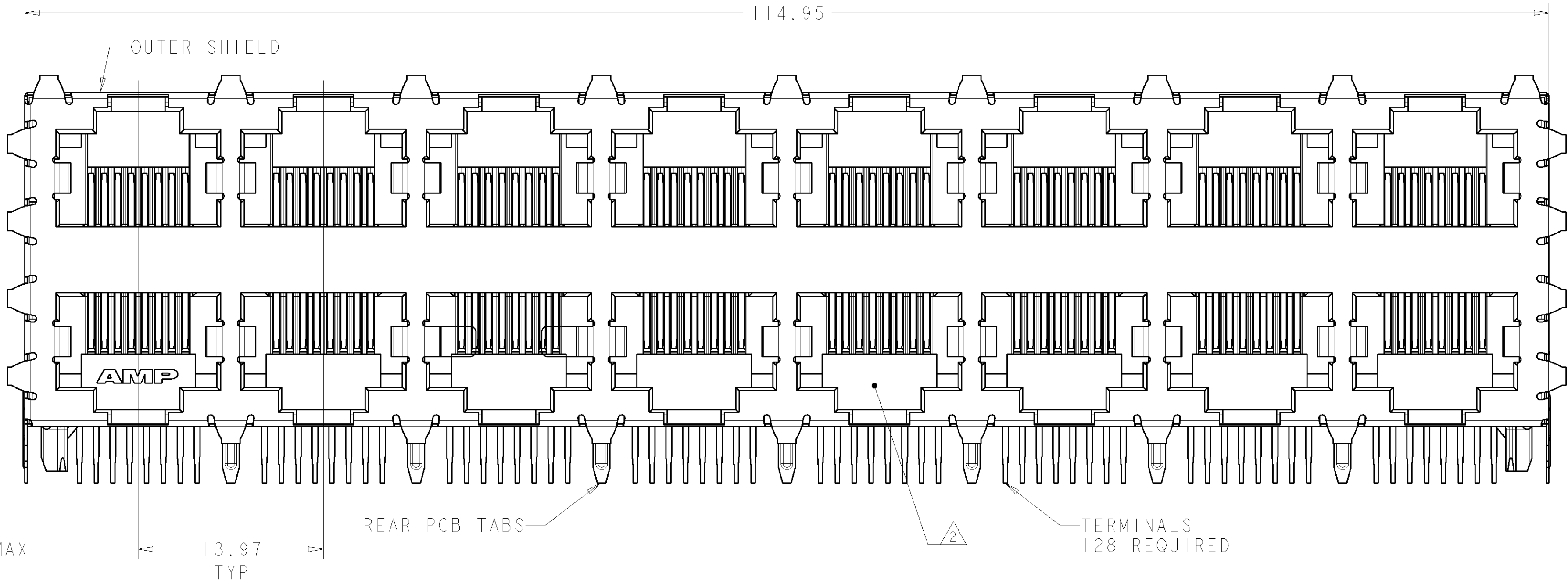
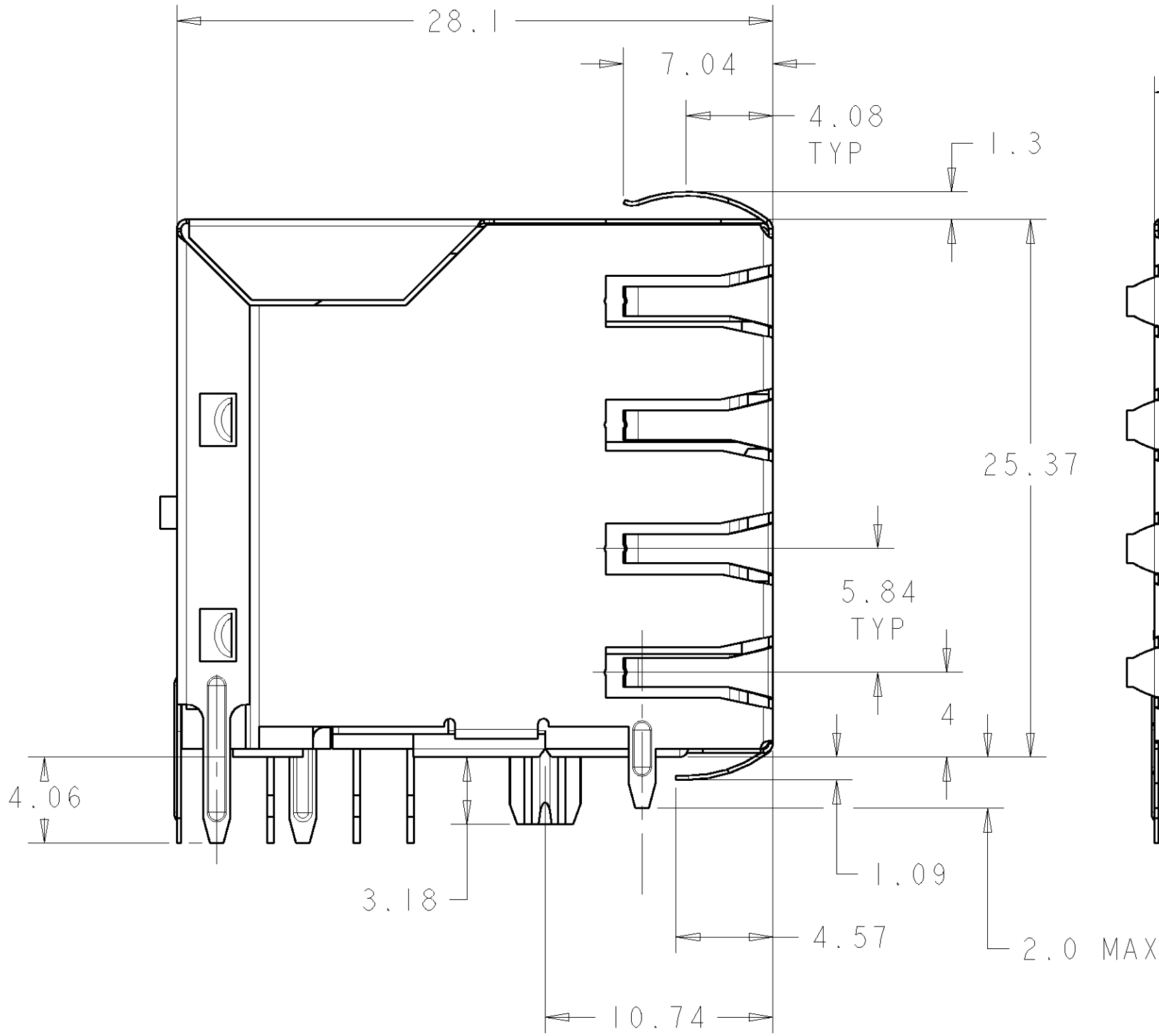
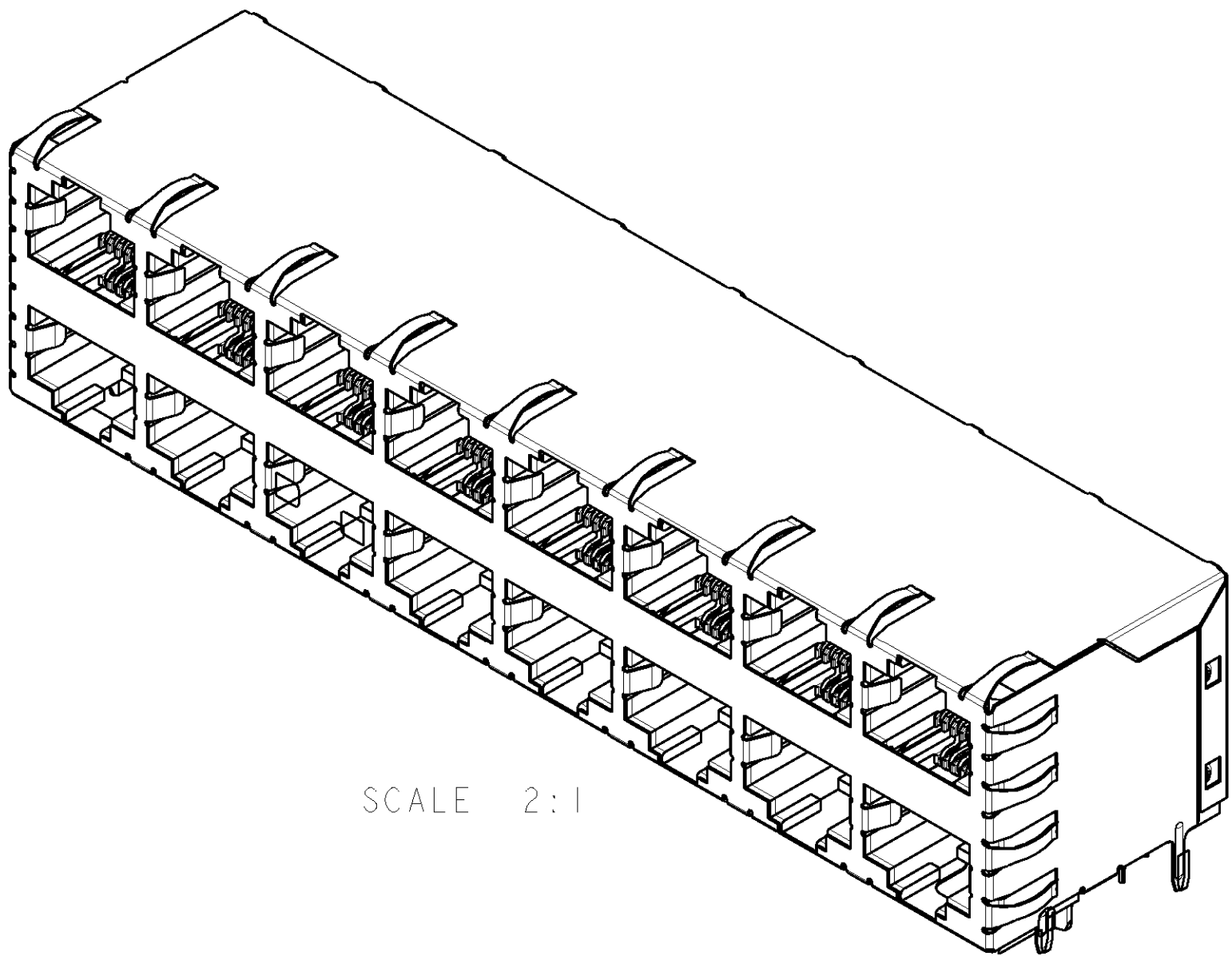
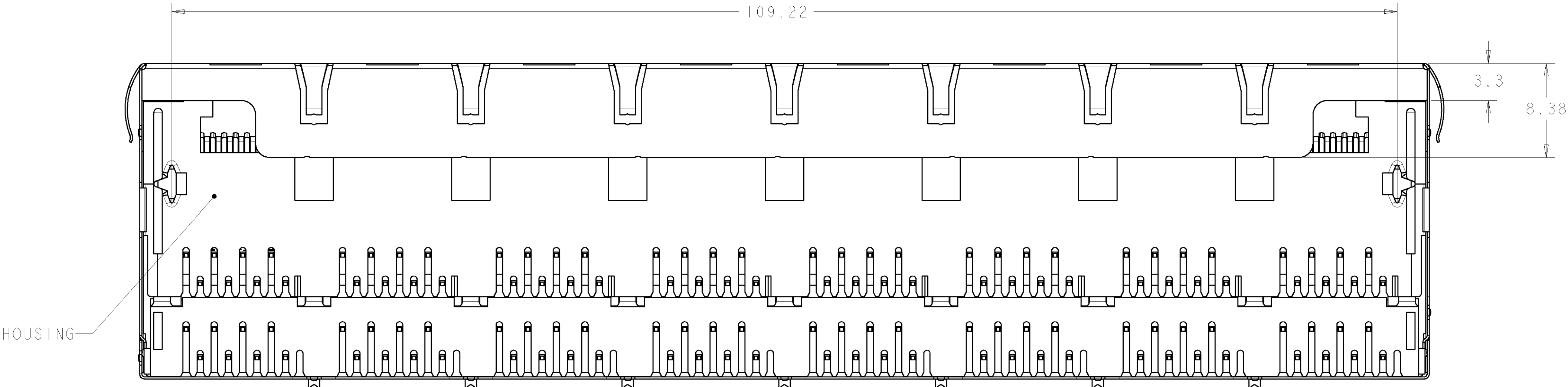


LOC		DIST		REVISIONS			
AA	22	P	LTR	DESCRIPTION	DATE	DWN	APVD
		A		REV PER ECOS11-0201-04	28JUL2005	AF	SF



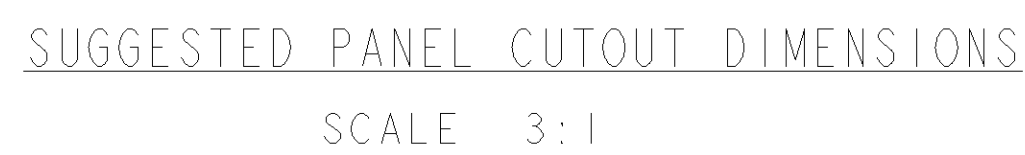
1. MATERIAL: HOUSING - PCT POLYESTER, BLACK, UL 94V-0
TERMINALS: 0.305 THICK PHOS BRONZE PLATED WITH 1.27µm MINIMUM THICK HARD GOLD IN LOCALIZED AREA AND 2µm MINIMUM THICK MATTE TIN IN SOLDER AREA OVER 1.27µm MINIMUM THICK NICKEL UNDERPLATE.
SHIELD - .196 THICK COPPER ALLOY PLATED WITH 1.27µm MINIMUM NICKEL AND 2.03µm MINIMUM HOT TIN DIP ON PCB GROUND TABS
2. JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS, PART 68 SUBPART F
3. USE #30 DRILL BIT OR 3.25mm DRILL BIT WHEN PRODUCING THESE PCB HOLES
4. RECOMMENDED APPLICATIONS: FAST ETHERNET, 10 BASE-T ETHERNET, AND 10/100 BASE-T ETHERNET (CAT 5 TWO PAIR 1/2, 3/6)



SCALE 2:1

7	6368296-1
NUMBER OF REAR PCB TABS	PART NUMBER

TYCO Electronics Harrisburg, PA 17105-3608		TYCO Electronics Harrisburg, PA 17105-3608	
NAME: 2X8 STACKED MODULAR JACK ASSEMBLY, 8 POSITION, SHIELDED, WAVE PANEL GROUND, 2.0 mm FRONT PCB GROUND TABS		SIZE: 114-2048	
WEIGHT: -		RESTRICTED TO: -	
CUSTOMER DRAWING		SCALE 4:1 SHEET 1 OF 2 REV A	

AMP 4805 REV 31MAR2000