

LVDS Interface ICs

# 35bit LVDS Transmitter 35:5 Serializer



**BU8254KVT** No.13057ECT06

#### Description

LVDS Interface IC of ROHM "Serializer" "Deserializer" operate from 8MHz to 150MHz wide clock range, and number of bits range is from 35 to 70. Data is transmitted seven times (7X) stream and reduce cable number by 3(1/3) or less. The ROHM's LVDS has low swing mode to be able to expect further low EMI.

#### Features

- 1) 35bits data of parallel LVCMOS level inputs are converted to five channels of LVDS data stream.
- 2) 30bits of RGB data and 5bits of timing and control data(HSYNC,VSYNC,DE,CNTL1,CNTL2) are transmitted up to 784Mbps effective rate per LVDS channel.
  - 3) Support clock frequency from 8MHz up to 112MHz.
  - 4) Support consumer video format including 480i, 480P, 720P and 1080i as well.
  - 5) Clock edge selectable
  - 6) Power down mode
  - 7) Support spread spectrum clock generator.
  - 8) Support reduced swing LVDS for low EMI.
  - 9) 30bit LVDS receiver is recommended to use BU90R104.

#### Applications

Flat Panel Display

#### Precaution

- ■This chip is not designed to protect from radioactivity.
- ■The chip is made strictly for the specific application or equipment.

Then it is necessary that the unit is measured as need.

■This document may be used as strategic technical data which subjects to COCOM regulations.

BU8254KVT Technical Note

#### Block Diagram

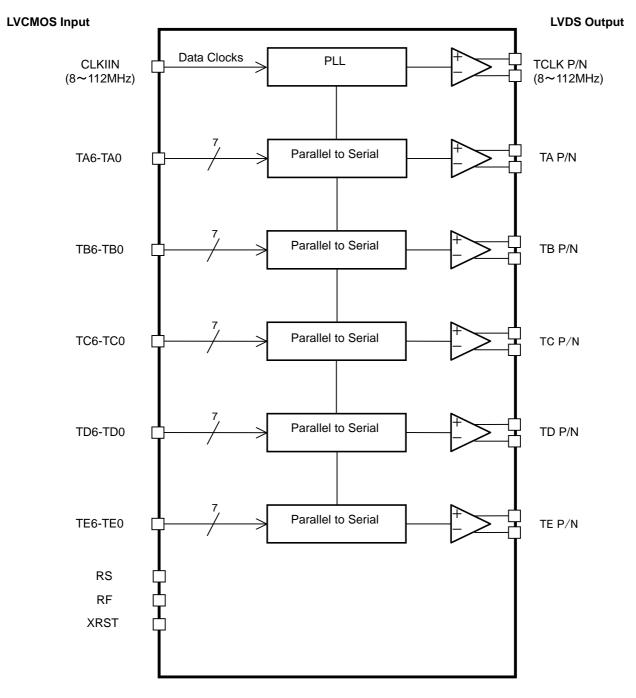


Fig.1 Block Diagram

●TQFP64V Package Outline and Specification

# TQFP64V

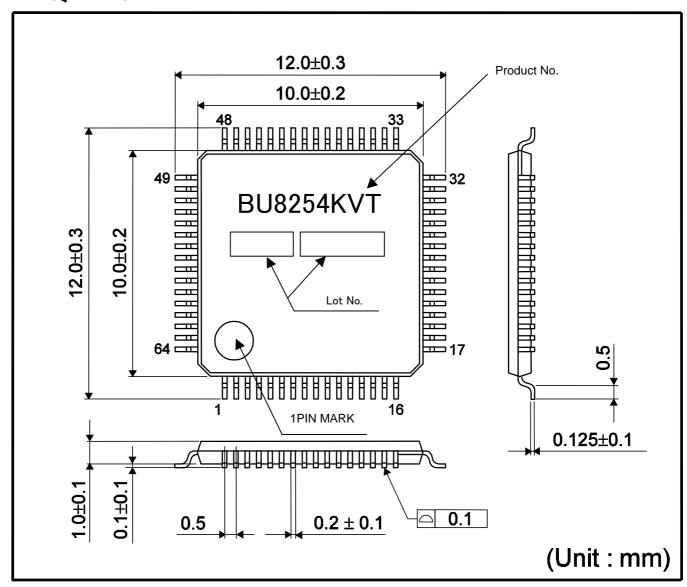


Fig.2 TQFP64V Package Outline and Specification

#### ●Pin configuration

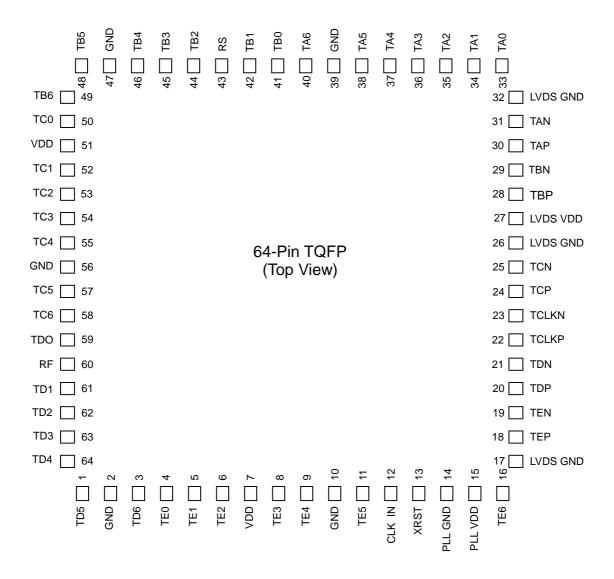


Fig.3 Pin Diagram (Top View)

#### ●Pin Description

Table 1 : Pin Description

Table 1 : Pin Desc	ription	1					
Pin Name	Pin No.	Туре		Descriptions			
TAP, TAN	30,31	LVDS OUT					
TBP, TBN	28,29	LVDS OUT					
TCP, TCN	24,25	LVDS OUT	LVDS data out.				
TDP, TDN	20,21	LVDS OUT	-				
TEP, TEN	18,19	LVDS OUT	-				
TCLKP, TCLKN	22,23	LVDS OUT	LVDS clock out.				
TA0∼TA6	33,34,35,36,37,38,40	IN					
TB0∼TB6	41,42,44,45,46,48,49	IN					
TC0~TC6	50,52,53,54,55,57,58	IN	Pixel data inputs.				
TD0~TD6	59,61,62,63,64,1,3	IN					
TE0∼TE6	4,5,6,8,9,11,16	IN					
XRST	13	IN	H : Normal operation, L : Power down (all outputs are Hi-Z)				
			LVDS swing mode, V <sub>REF</sub> *1select.				
			RS	LVDS Swing	Small Swing Input Support		
RS	43	IN	V <sub>DD</sub> 350mV N/A				
			0.6~1.4V	350mV	RS-V <sub>REF</sub>		
			GND	200mV	N/A		
			*1 V <sub>REF</sub> is Input Refere	ence Voltage.			
RF	60	IN	Input clock triggeri H: Rising edge, L	ng edge select. : Falling edge.			
VDD	51,7	Power	Power supply pins	for LVCMOS input	s and digital core.		
CLKIN	12	IN	Clock input.				
GND	2,10,39,47,56	Ground	Ground pins for LVCMOS inputs and digital core.				
LVDS VDD	27	Power	Power supply pins	for LVDS outputs.			
LVDS GND	17,26,32	Ground	Ground pins for LVDS outputs.				
PLLVDD	15	Power	Power supply pin for PLL core.				
PLLGND	14	Ground	Ground pins for PLL core.				

#### Electrical characteristics

#### ■Rating

Table 2: Absolute Maximum Ratings

Parameter	Symbol	Rat	Units	
Faiametei	Symbol	Min	Max	Ullits
Supply Voltage	$V_{DD}$	-0.3	4.0	V
Input Voltage	V <sub>IN</sub>	-0.3	V <sub>DD</sub> +0.3	٧
Output Voltage	V <sub>OUT</sub>	-0.3	V <sub>DD</sub> +0.3	V
Storage Temperature Range	Tstg	-55	125	°C

Table 3: Package Power

PACKAGE	Power Dissipation (mW)	De-rating (mW/°C) *1
TQFP64V	700	7.0
TQFF04V	1000 <sup>*2</sup>	10.0*2

At temperature Ta >25°C

Package power when mounting on the PCB board.

The size of PCB board :70 × 70 × 1.6(mm³)

The material of PCB board :The FR4 glass epoxy board.(3% or less copper foil area)

(It is recommended to apply the above package power requirement to PCB board

when the small swing input mode is used)

Table 4: Recommended Operating Conditions

Parameter	Symbol			Units	Conditions	
Farameter	Min Typ Max		Units	Conditions		
Supply Voltage	$V_{DD}$	3.0	3.3	3.6	V	VDD,LVDSVDD,PLLVDD
Operating	Tonr	-40	-	85	°C	Clock frequency from 8MHz up to 90MHz
Temperature Range	Topr	0	-	70	°C	Cock frequency from 90MHz up to 112MHz

#### **■DC** characteristics

Table 5 : LVCMOS DC Specifications (VDD=3.0V~3.6V, Ta=-40°C~85°C)

Parameter	Symbol	Symbol Rating		Units	Conditions		
Falametei	Symbol	Min	Тур	Max	Office	Conditions	
High Level Input Voltage	V <sub>IH</sub>	V <sub>DD</sub> × 0.8	-	$V_{DD}$	V	avaluda BS nin	
Low Level Input Voltage	V <sub>IL</sub>	GND	-	V <sub>DD</sub> × 0.2	V	exclude RS pin	
High Level Input Voltage	V <sub>IHRS</sub>	V <sub>DD</sub> × 0.8	-	$V_{DD}$		RS pin	
Low Level Input Voltage	V <sub>ILRS</sub>	GND	-	0.2			
Small Swing Voltage	$V_{DDQ}^{*1}$	1.2	-	2.8	V		
Input Reference Voltage	$V_{REF}$	-	V <sub>DDQ</sub> /2	-	-	Small Swing(RS=V <sub>DDQ</sub> /2)	
Small Swing High Level Input Voltage	V <sub>SH</sub> <sup>*2</sup>	V <sub>DDQ</sub> /2 +200mV	-	-	V	V <sub>REF</sub> =V <sub>DDQ</sub> /2	
Small Swing Low Level Input Voltage	V <sub>SL</sub> *2	-	-	V <sub>DDQ</sub> /2 -200mV	V	V <sub>REF</sub> =V <sub>DDQ</sub> /2	
Input Current	I <sub>INC</sub>	-	-	±10	μA	$0V \leq V_{IN} \leq V_{DD}$	

<sup>\*1:</sup> V<sub>DDQ</sub> voltage defines max voltage of small swing input. It is not an actual input voltage.

Table 6 : LVDS Transmitter DC Specifications(VDD=3.0V~3.6V, Ta=-40°C~85°C)

Parameter	Symbol	Rating			Units	Conditions		
i arameter	Symbol	Min	Тур	Min	Office		oriditions	
Differential Output Voltage	V <sub>OD</sub>	250	350	450	mV	RL=100Ω	Normal swing RS=V <sub>DD</sub>	
Differential Output Voltage	V OD	100	200	300	mV	KL=10032	Reduced swing RS=GND	
Change in VOD between complementary output states	ΔV <sub>OD</sub>	-	-	35	mV	RL=100Ω		
Common Mode Voltage	V <sub>OC</sub>	1.125	1.25	1.375	V			
Change in VOC between complementary output states	ΔV <sub>OC</sub>	-	-	35	mV			
Output Short Circuit Current	I <sub>OS</sub>	-	-	-24	mA	$V_{OUT}$ =0 $V$ , RL=100 $\Omega$		
Output TRI-STATE Current	l <sub>OZ</sub>	-	-	±10	μA	XRST=0V, V <sub>OUT</sub> =0V to V <sub>DD</sub>		

<sup>\*2:</sup> Small swing signal is applied to TA[6:0], TB[6:0], TC[6:0], TD[6:0] TE[6:0], CLKIN.

### ■Supply Current

Table 7 : Supply Current

Dovernator	Coursels al	Rating			l lesite	O an althiorn		
Parameter	Symbol	Min	Тур	Max	Units	Conditions		
Transmitter Supply		-	57	-	mA	RL= $100 \Omega$ , CL= $5pF$ $V_{DD}=3.3V$ , RS= $V_{DD}$ f= $600$ Gray Scale Pattern	85MHz	
Current	Ітсс	-	42	-	mA	$\begin{array}{c} \text{RL=100}\Omega,\text{CL=5pF} \\ \text{V}_{\text{DD}}\text{=}3.3\text{V},\text{RS=GND} \\ \text{Gray Scale Pattern} \end{array} \hspace{0.2cm} \text{f=8}$	85MHz	
Transmitter Supply Current	<b>I</b>	-	62	-	mA	RL=100 Ω ,CL=5pF V <sub>DD</sub> =3.3V,RS=V <sub>DD</sub> Worst Case pattern f=8	85MHz	
	ITCCW -	45	-	mA	RL=100 Ω ,CL=5pF V <sub>DD</sub> =3.3V,RS=GND Worst Case pattern	85MHz		
Transmitter Power Down Supply Current	I <sub>TCCS</sub>	-	-	10	μΑ	XRST=L		

#### **Gray Scale Pattern**

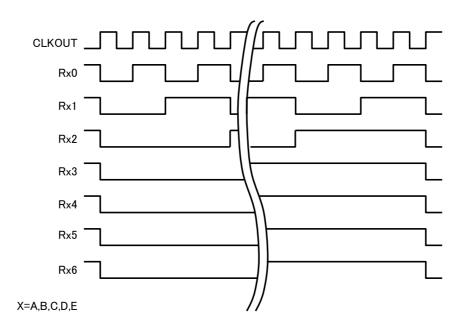


Fig.4 Gray scale pattern

#### **Worst Case Pattern (Maximum Power condition)**

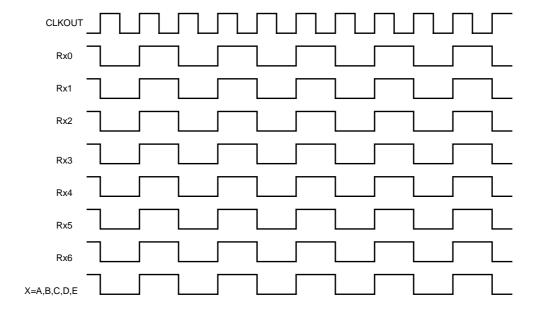


Fig.5 Worst Case Pattern

#### ■AC characteristics

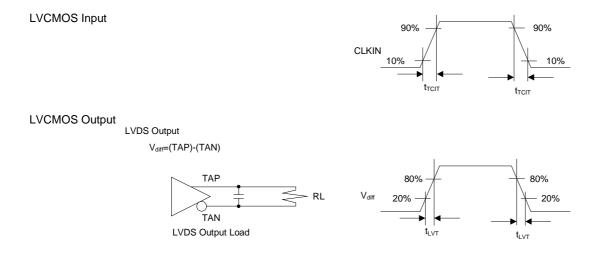
Table 8: Switching Characteristics

Table 8 : Switching Characteristics					
Parameter	Symbol	Min	Тур	Max	Units
CLK IN Transition time	tтсіт	-	-	5.0	ns
CLK IN Period	t <sub>TCP</sub>	8.93	-	125.0	ns
CLK IN High Time	t <sub>TCH</sub>	0.35t <sub>TCP</sub>	0.5t <sub>TCP</sub>	0.65t <sub>TCP</sub>	ns
CLK IN Low Time	t <sub>TCL</sub>	0.35t <sub>TCP</sub>	0.5t <sub>TCP</sub>	0.65t <sub>TCP</sub>	ns
CLK IN to TCLK+/-Delay	t <sub>TCD</sub>	-	t <sub>TCP</sub>	-	ns
LVSMOS Data Set up to CLK IN	t <sub>TS</sub>	2.5	-	-	ns
LVCMOS Data Hold from CLK IN	tтн	0	-	-	ns
LVDS Transition Time	t <sub>LVT</sub>	-	0.6	1.5	ns
Output Data Position 0	t <sub>TOP1</sub>	-0.2	0.0	+0.2	ns
Output Data Position 1	t <sub>TOP0</sub>	$\frac{\text{trcp}}{7}$ -0.2	<u>ttcp</u> 7	$\frac{\text{tTCP}}{7} + 0.2$	ns
Output Data Position 2	t <sub>TOP6</sub>	$2\frac{\text{tTCP}}{7}$ -0.2	2 trcp 7	$2\frac{\text{tTCP}}{7} + 0.2$	ns
Output Data Position 3	t <sub>TOP5</sub>	$3\frac{\text{tTCP}}{7}$ -0.2	3 ttcp 7	$3\frac{\text{tTCP}}{7} + 0.2$	ns
Output Data Position 4	t <sub>TOP4</sub>	$4\frac{\text{tTCP}}{7}$ -0.2	4 ttcp 7	$4\frac{\text{tTCP}}{7} + 0.2$	ns
Output Data Position 5	t <sub>TOP3</sub>	$5\frac{\text{tTCP}}{7}$ -0.2	5 ttcp 7	$5\frac{\text{tTCP}}{7} + 0.2$	ns
Output Data Position 6	t <sub>TOP2</sub>	$6\frac{\text{tTCP}}{7}$ -0.2	6 ttcp 7	$6\frac{\text{tTCP}}{7} + 0.2$	ns
Phase Locked Loop Set Time	t <sub>TPLL</sub>	-	-	10.0	ms

BU8254KVT Technical Note

#### AC Timing

#### **■**AC Timing Diagrams



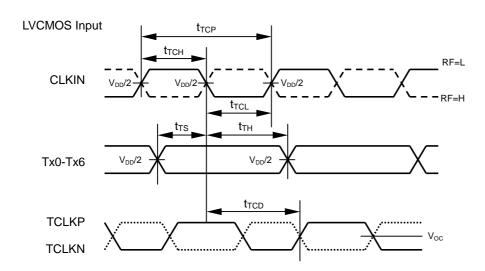


Fig.6 AC Timing Diagrams

#### **■Small Swing Inputs**

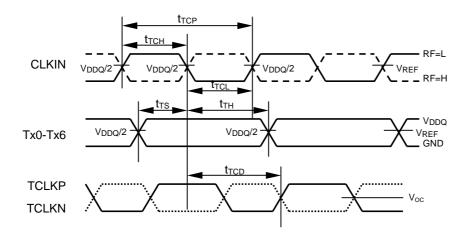


Fig.7 Small Swing Inputs

Technical Note

#### **■**AC Timing Diagrams

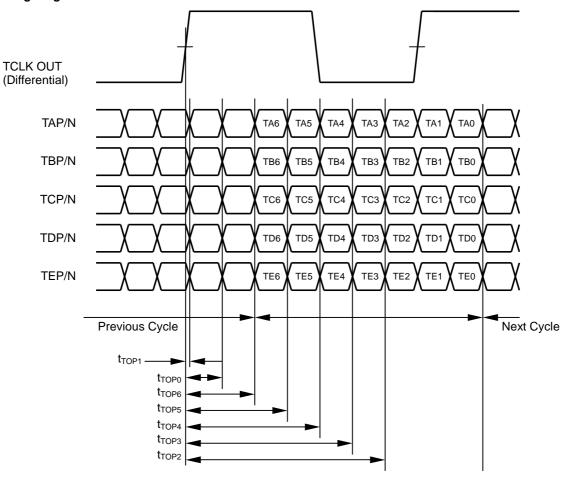


Fig.8 AC Timing Diagrams

#### **■**Phase Locked Loop Set Time

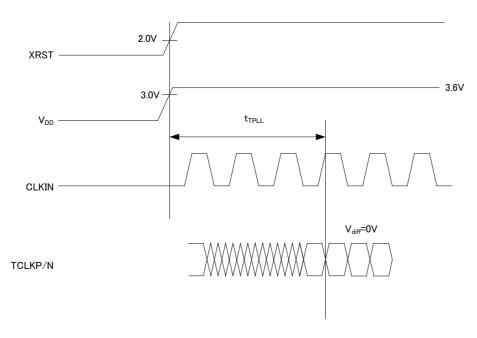


Fig.9 Phase Locked Loop Set Time

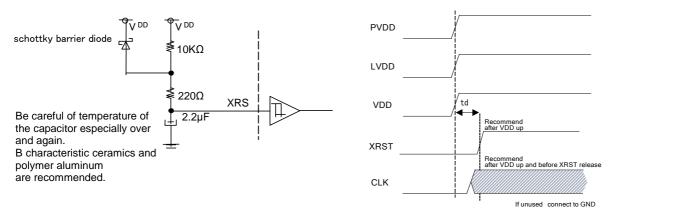
#### System Timing Requirement

System Timing Requirement is mandatory by following two methods.

- ①The method of using CR circuit.( In the case that CLK does not stop after power supply)
- ②The method of using external specific IC. (In the case that CLK turns on/off after power supply)

It is recommend to do enough examination for target application.

(1) The method of using CR circuit. (In the case that CLK does not stop after power supply)



td is approximately equal to 20ms when the left RC coleus are applied.

Fig.10 The method of using CR circuit.

#### 2 The method of using external specific IC. (In the case that CLK turns on/off after power supply)

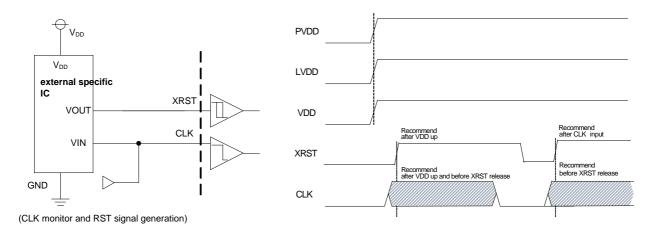


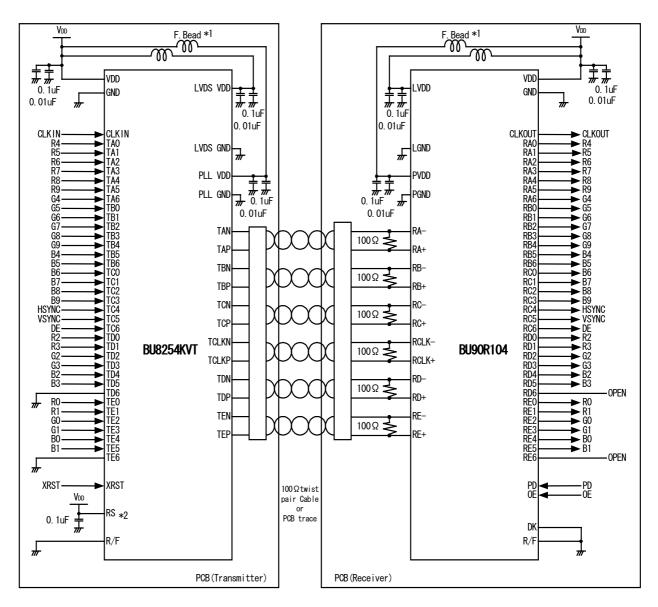
Fig.11 The method of using external specific IC.

#### ●10bit LVCMOS Level Input

Example:

BU8254KVT: LVCMOS level input/Falling edge/Normal swing

BU90R104: Falling edge



<sup>\*1 :</sup> Recommended Parts: F.Bead : BLM18A-Series (Murata Manufacturing)

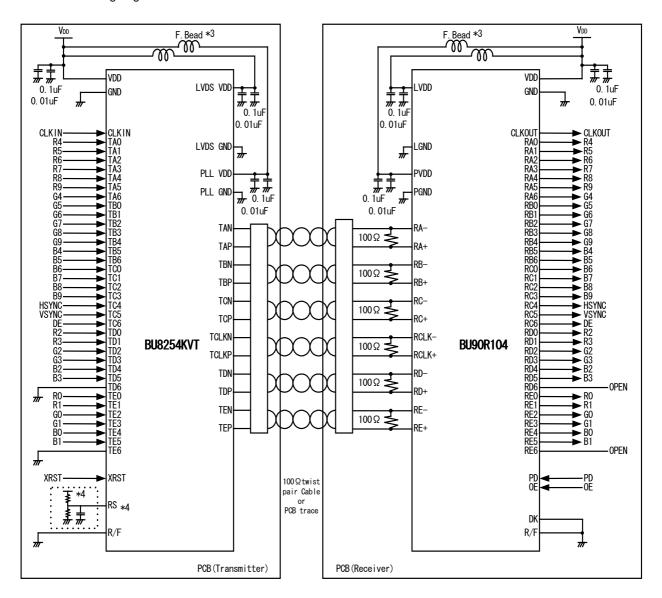
<sup>\*2</sup> If RS pin is tied to VDD, LVDS swing is 350m V. If RS pin is tied to GND, LVDS swing is 200m V.

#### ●10bit Small Swing Input

Example:

BU8254KVT: LVCMOS level input/Falling edge/Normal swing

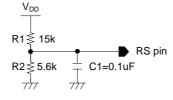
BU90R104: Falling edge



\*3 : Recommended Parts:

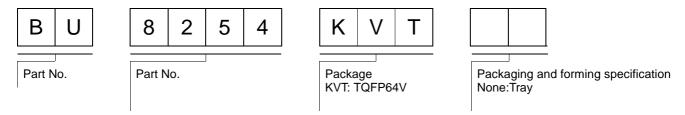
F.Bead : BLM18A-Series (Murata Manufacturing)

\*4 : RS pin acts as VREF input pin when input voltage is set to half of high level signal input. We recommend to locate by-pass condenser near the RS pin.

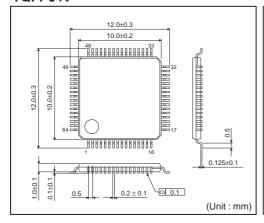


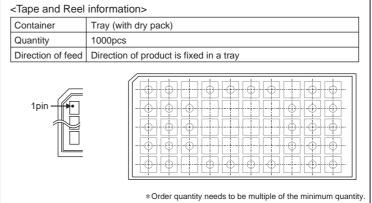
Example for LVCMOS(1.8V input)(R1,R2)=(1.5k $\Omega$ ,5.6k $\Omega$ )

#### Ordering Part Number



#### TQFP64V





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JÁPAN	USA	EU	CHINA
CLASSI	СГУССШ	CLASS II b	СГУССШ
CLASSIV	CLASSII	CLASSIII	— CLASSⅢ

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

#### **Precaution for Product Label**

QR code printed on ROHM Products label is for ROHM's internal use only.

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