

# Dual Matched General Purpose Transistor

## PNP Matched Pair

### NST30010MXV6T1G, NSVT30010MXV6T1G

These transistors are housed in an ultra-small SOT563 package ideally suited for portable products. They are assembled to create a pair of devices highly matched in all parameters, eliminating the need for costly trimming. Applications are Current Mirrors; Differential, Sense and Balanced Amplifiers; Mixers; Detectors and Limiters.

#### Features

- Current Gain Matching to 10%
- Base-Emitter Voltage Matched to 2 mV
- Drop-In Replacement for Standard Device
- AEC-Q101 Qualified and PPAP Capable
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These are Pb-Free Devices\*

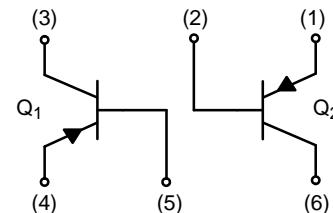
#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$	-30	V
Collector-Base Voltage	$V_{CBO}$	-30	V
Emitter-Base Voltage	$V_{EBO}$	-5.0	V
Collector Current – Continuous	$I_C$	-100	mAdc

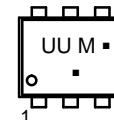
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



SOT-563  
CASE 463A  
PLASTIC



#### MARKING DIAGRAMS



UU = Device Code  
M = Date Code  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

#### ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
NST30010MXV6T1G	SOT-563 (Pb-Free)	4,000 / Tape & Reel
NSVT30010MXV6T1G	SOT-563 (Pb-Free)	4,000 / Tape & Reel

<sup>†</sup> For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, [BRD8011/D](#).

\* For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, [SOLDERRM/D](#).

# NST30010MXV6T1G, NSVT30010MXV6T1G

## THERMAL CHARACTERISTICS

Characteristic	Parameter	Symbol	One Device Heated	Both Devices Heated	Unit
Total Device Dissipation, $T_A = 25^\circ\text{C}$ (Note 1) Derate above $25^\circ\text{C}$ (Note 1) $T_A = 25^\circ\text{C}$ (Note 2) Derate above $25^\circ\text{C}$ (Note 2)	Two Devices Heated Total Package	$P_D$	357 2.9 429 3.4	500 (250 ea) 4.0 661 (331 ea) 5.3	mW mW/ $^\circ\text{C}$ mW mW/ $^\circ\text{C}$
Thermal Resistance Junction-to-Ambient (Note 1) Junction-to-Ambient (Note 2)	One Heated Device	$R_{\theta JA}$	350 291	250 189	$^\circ\text{C}/\text{W}$
Thermal Resistance Junction-to-Ambient (Note 1) Junction-to-Ambient (Note 2)	Unheated Device Heated by Heated Device	$\Psi_{JA}$	149 88	— —	$^\circ\text{C}/\text{W}$
Thermal Resistance Junction-to-Lead (Note 1) Junction-to-Lead (Note 2)	Lead Attached to Heated Device	$\Psi_{JL}$	128 152	76 85	$^\circ\text{C}/\text{W}$
Thermal Resistance Junction-to-Lead (Note 1) Junction-to-Lead (Note 2)	Heated Device Heating Lead Attached to Unheated Device	$\Psi_{JL}$	224 222	— —	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature Range		$T_J, T_{\text{stg}}$	-55 to +150		

1. PCB with 51 square millimeter of 2 oz (0.070mm thick) copper heat spreading connected to package leads. Mounted on a FR4 PCB 76x76x1.5mm Single layer traces. Natural convection test according to JEDEC 51.
2. PCB with 250 square millimeter of 2 oz (0.070mm thick) copper heat spreading connected to package leads. Mounted on a FR4 PCB 76x76x1.5mm Single layer traces. Natural convection test according to JEDEC 51.

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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### OFF CHARACTERISTICS

Collector-Emitter Breakdown Voltage, ( $I_C = -10\text{ mA}$ )	$V_{(\text{BR})\text{CEO}}$	-30	—	—	V
Collector-Emitter Breakdown Voltage, ( $I_C = -10\text{ }\mu\text{A}$ , $V_{EB} = 0$ )	$V_{(\text{BR})\text{CES}}$	-30	—	—	V
Collector-Base Breakdown Voltage, ( $I_C = -10\text{ }\mu\text{A}$ )	$V_{(\text{BR})\text{CBO}}$	-30	—	—	V
Emitter-Base Breakdown Voltage, ( $I_E = -1.0\text{ }\mu\text{A}$ )	$V_{(\text{BR})\text{EBO}}$	-5.0	—	—	V
Collector Cutoff Current ( $V_{CB} = -30\text{ V}$ ) ( $V_{CB} = -30\text{ V}$ , $T_A = 150^\circ\text{C}$ )	$I_{CBO}$	—	—	-15 -4.0	nA $\mu\text{A}$

### ON CHARACTERISTICS

DC Current Gain ( $I_C = -10\text{ }\mu\text{A}$ , $V_{CE} = -5.0\text{ V}$ ) ( $I_C = -2.0\text{ mA}$ , $V_{CE} = -5.0\text{ V}$ ) ( $I_C = -2.0\text{ mA}$ , $V_{CE} = -5.0\text{ V}$ ) (Note 3)	$h_{FE}$ $h_{FE(1)}/h_{FE(2)}$	270 420 0.9	— 520 1.0	— 800 —	—
Collector-Emitter Saturation Voltage ( $I_C = -10\text{ mA}$ , $I_B = -0.5\text{ mA}$ ) ( $I_C = -100\text{ mA}$ , $I_B = -5.0\text{ mA}$ )	$V_{CE(\text{sat})}$	— —	— —	-0.30 -0.60	V
Base-Emitter Saturation Voltage ( $I_C = -10\text{ mA}$ , $I_B = -1.0\text{ mA}$ ) ( $I_C = -100\text{ mA}$ , $I_B = -10\text{ mA}$ )	$V_{BE(\text{sat})}$	— —	-0.75 -0.90	— —	V
Base-Emitter On Voltage ( $I_C = -2.0\text{ mA}$ , $V_{CE} = -5.0\text{ V}$ ) ( $I_C = -10\text{ mA}$ , $V_{CE} = -5.0\text{ V}$ ) ( $I_C = -2.0\text{ mA}$ , $V_{CE} = -5.0\text{ V}$ ) (Note 4)	$V_{BE(\text{on})}$ $V_{BE(1)} - V_{BE(2)}$	-0.60 — —	— — 1.0	-0.75 -0.82 2.0	V mV

### SMALL-SIGNAL CHARACTERISTICS

Current-Gain - Bandwidth Product, ( $I_C = -10\text{ mA}$ , $V_{CE} = -5\text{ Vdc}$ , $f = 100\text{ MHz}$ )	$f_T$	100	—	—	MHz
Output Capacitance, ( $V_{CB} = -10\text{ V}$ , $f = 1.0\text{ MHz}$ )	$C_{ob}$	—	—	4.5	pF
Noise Figure, ( $I_C = -0.2\text{ mA}$ , $V_{CE} = -5\text{ Vdc}$ , $R_S = 2\text{ k}\Omega$ , $f = 1\text{ kHz}$ , $BW = 200\text{ Hz}$ )	NF	—	—	10	dB

3.  $h_{FE(1)}/h_{FE(2)}$  is the ratio of one transistor compared to the other transistor within the same package. The smaller  $h_{FE}$  is used as numerator.
4.  $V_{BE(1)} - V_{BE(2)}$  is the absolute difference of one transistor compared to the other transistor within the same package.

# NST30010MXV6T1G, NSVT30010MXV6T1G

## TYPICAL CHARACTERISTICS

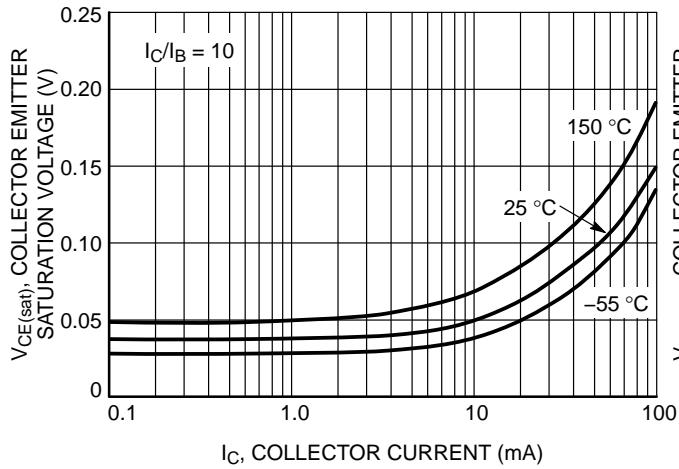


Figure 1. Collector Emitter Saturation Voltage vs. Collector Current

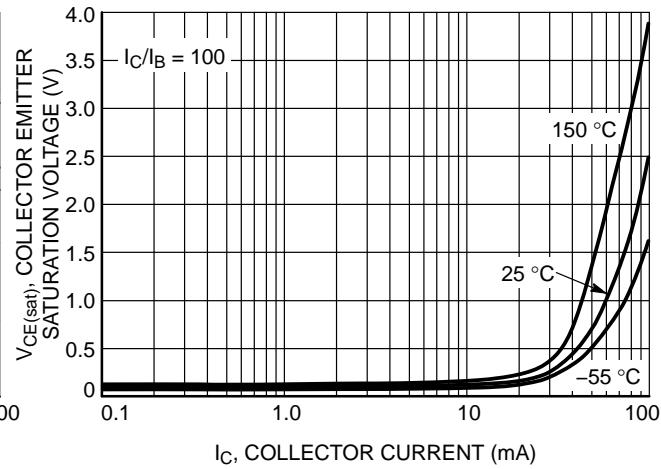


Figure 2. Collector Emitter Saturation Voltage vs. Collector Current

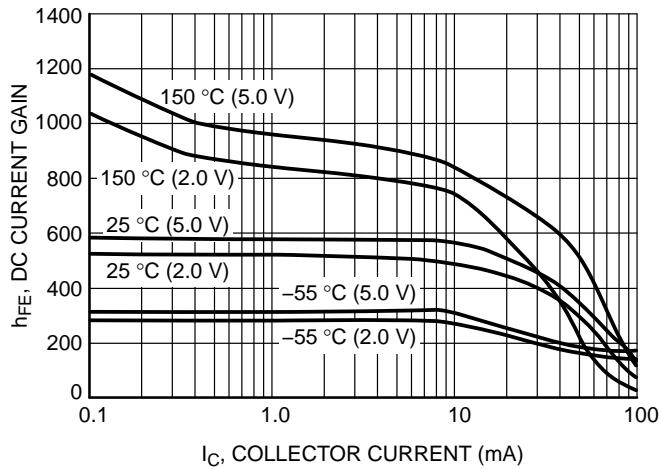


Figure 3. DC Current Gain vs. Collector Current

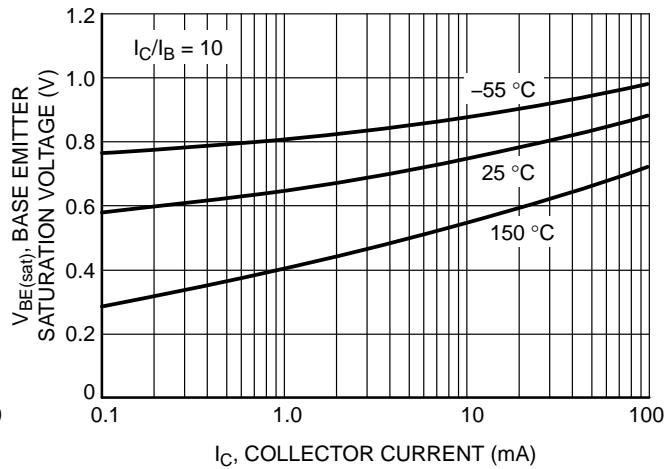


Figure 4. Base Emitter Saturation Voltage vs. Collector Current

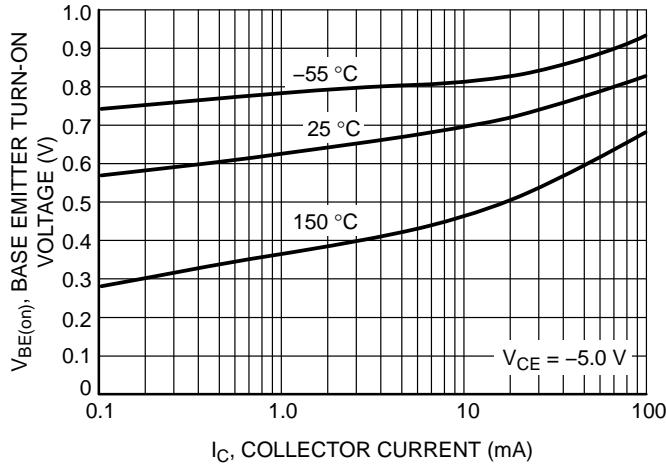


Figure 5. Base Emitter Turn-On Voltage vs. Collector Current

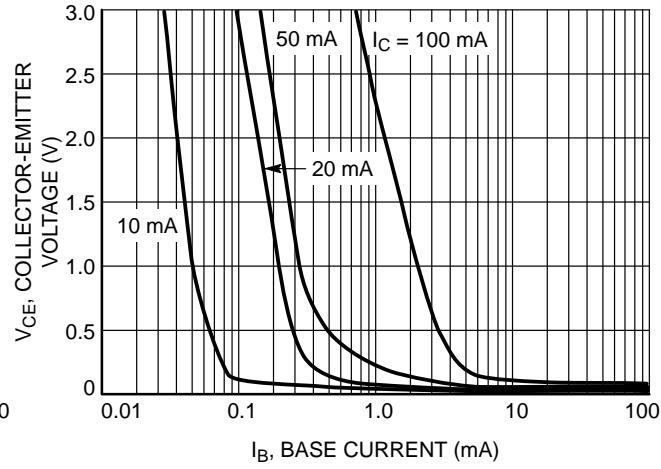


Figure 6. Saturation Region @ 25 °C

# NST30010MXV6T1G, NSVT30010MXV6T1G

## TYPICAL CHARACTERISTICS

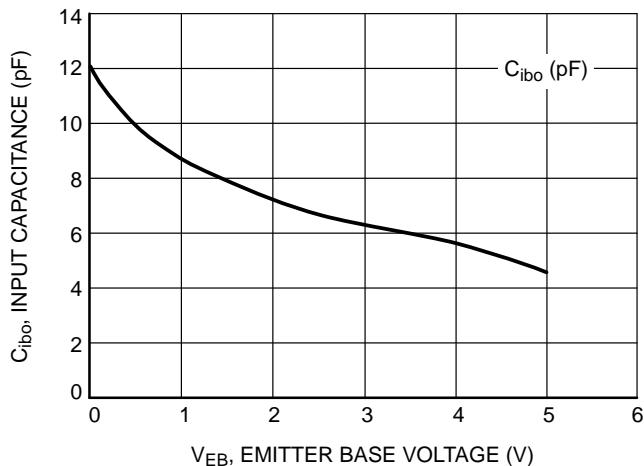


Figure 7. Input Capacitance

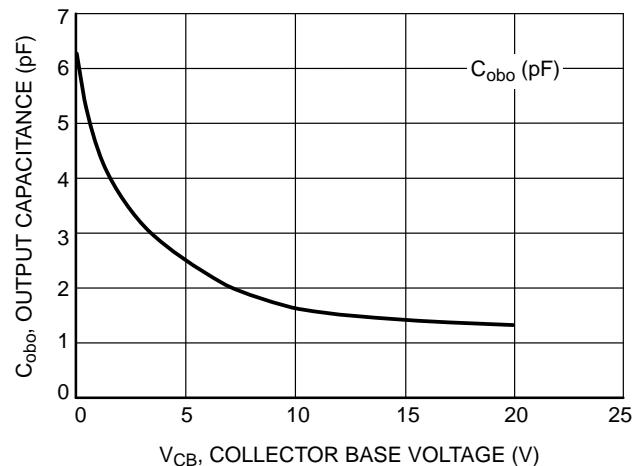
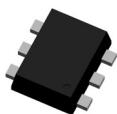


Figure 8. Output Capacitance

# **NST30010MXV6T1G, NSVT30010MXV6T1G**

## **REVISION HISTORY**

Revision	Description of Changes	Date
2	Rebranded the Data Sheet to <b>onsemi</b> format.	6/18/2025



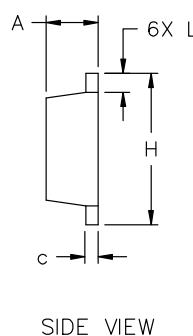
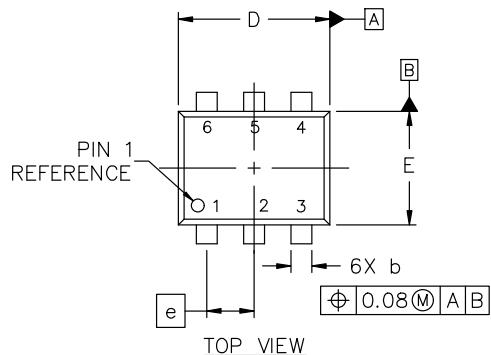
## SOT-563-6 1.60x1.20x0.55, 0.50P

CASE 463A  
ISSUE J

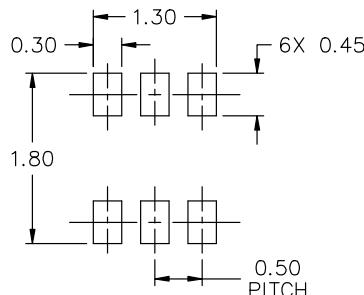
DATE 15 FEB 2024

## NOTES:

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
2. ALL DIMENSION ARE IN MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.



DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.50	0.55	0.60
b	0.17	0.22	0.27
c	0.08	0.13	0.18
D	1.50	1.60	1.70
E	1.10	1.20	1.30
e 0.50 BSC			
H	1.50	1.60	1.70
L	0.10	0.20	0.30



## RECOMMENDED MOUNTING FOOTPRINT\*

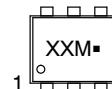
STYLE 1: PIN 1. Emitter 1	STYLE 2: PIN 1. Emitter 1	STYLE 3: PIN 1. Cathode 1
2. Base 1	2. Emitter 2	2. Cathode 1
3. Collector 2	3. Base 2	3. Anode/Anode 2
4. Emitter 2	4. Collector 2	4. Cathode 2
5. Base 2	5. Base 1	5. Cathode 2
6. Collector 1	6. Collector 1	6. Anode/Anode 1

STYLE 4: PIN 1. Collector	STYLE 5: PIN 1. Cathode	STYLE 6: PIN 1. Cathode
2. Collector	2. Cathode	2. Anode
3. Base	3. Anode	3. Cathode
4. Emitter	4. Anode	4. Cathode
5. Collector	5. Cathode	5. Cathode
6. Collector	6. Cathode	6. Cathode

STYLE 7: PIN 1. Cathode	STYLE 8: PIN 1. Drain	STYLE 9: PIN 1. Source 1
2. Anode	2. Drain	2. Gate 1
3. Cathode	3. Gate	3. Drain 2
4. Cathode	4. Source	4. Source 2
5. Anode	5. Drain	5. Gate 2
6. Cathode	6. Drain	6. Drain 1

STYLE 10: PIN 1. Cathode 1	STYLE 11: PIN 1. Emitter 2
2. N/C	2. Base 2
3. Cathode 2	3. Collector 1
4. Anode 2	4. Emitter 1
5. N/C	5. Base 1
6. Anode 1	6. Collector 2

\* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

GENERIC  
MARKING DIAGRAM\*

XX = Specific Device Code

M = Month Code

■ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	SOT-563-6 1.60x1.20x0.55, 0.50P	PAGE 1 OF 1

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