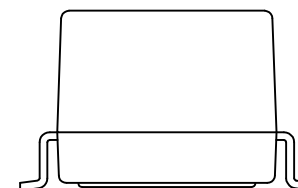
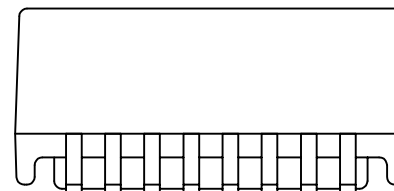
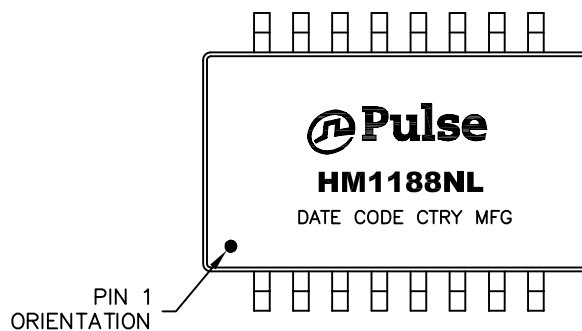


NOTES:

1. ROHS COMPLIANT
2. STORAGE TEMPERATURE: -50°C TO +125°C
3. COMPLIANCE TO J-STD:
 - A. J-STD-002: SOLDERABILITY AT 245°C REFLOW PROFILE
 - B. J-STD-020: LEVEL 1, NO MOISTURE SENSITIVE
 - C. J-STD-075: R7, 245°C MAXIMUM THROUGH REFLOW SOLDER
4. TO ORDER TAPE & REEL PACKAGING ADD A "T" SUFFIX
TO THE PART NUMBER(i.e HXXXXXXNL BECOMES HXXXXXXNLT).



FINAL OUTLINE

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PRODUCT DESCRIPTION
MDL,SIN,100D,1:1,SM,TU

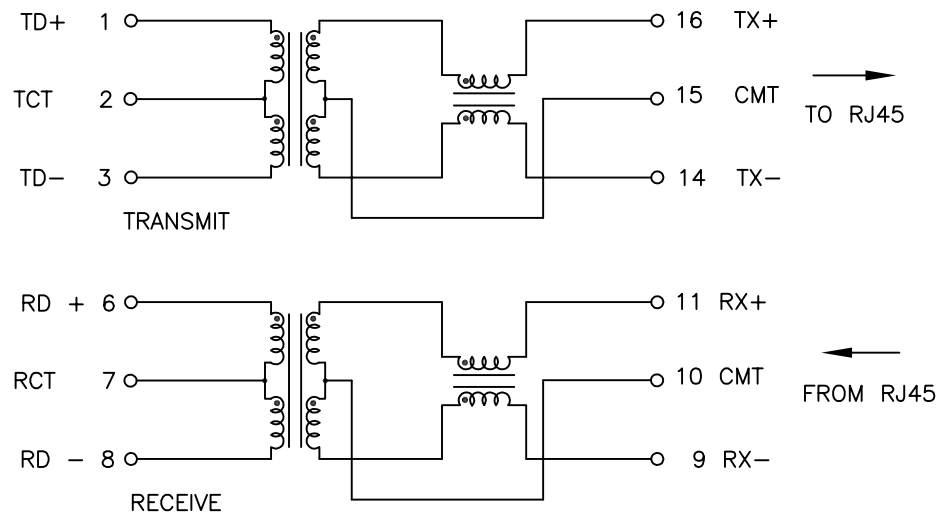
TLA DRAWING
HM1188NL-10

PS DRAWING
PS-2005.001-F

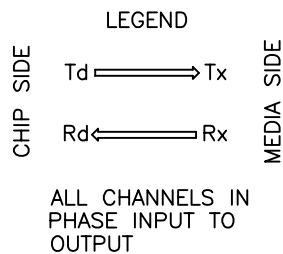
SHEET
1 OF 3

PART NO.
HM1188NL

DATASHEET REV.
E

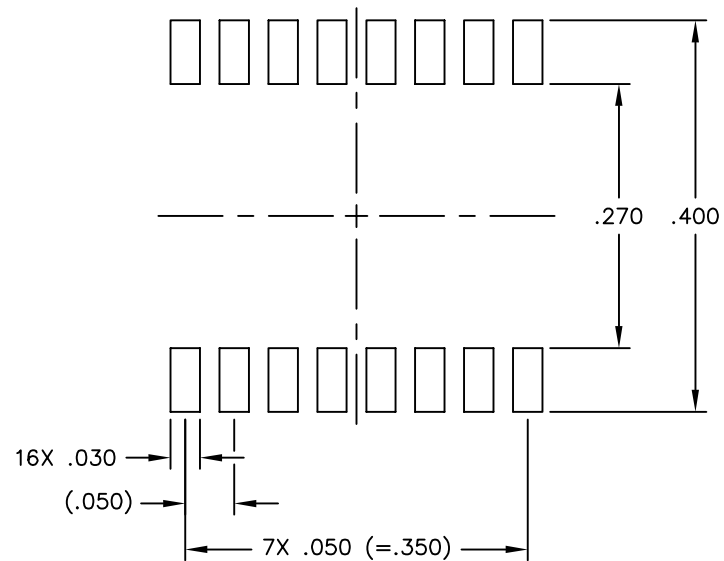
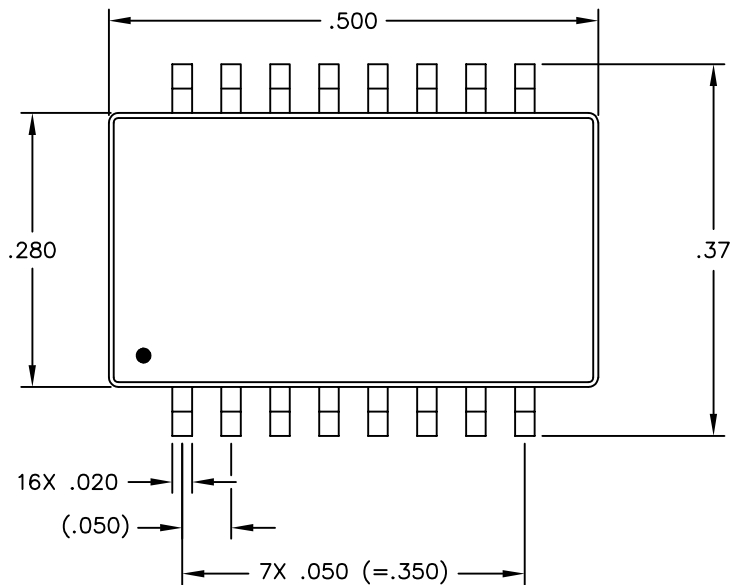


SCHEMATIC

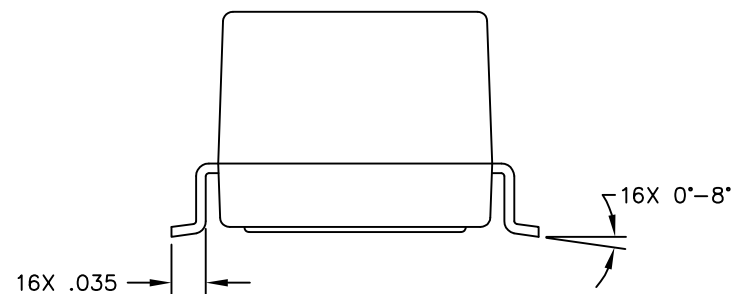
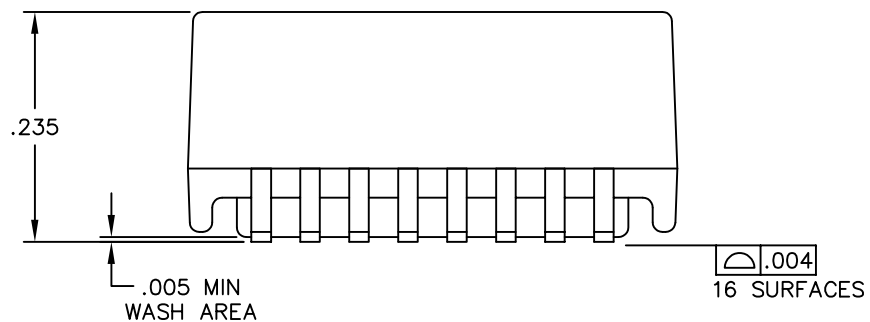


ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED
MEETS IEEE 802.3 SPECIFICATION

PARAMETER	SPECIFICATIONS	
OPERATING TEMP	-40°C TO +105°C	
URNS RATIO	1 : 1 $\pm$ 2%	
POLARITY	PER SCHEMATIC	
INDUCTANCE (OCL) (MEDIA SIDE, -40°C~+105°C)	350 μ H MIN (MEASURED AT 100 KHz, 100 mVRMS) (AND WITH 8 mA DC BIAS)	
INSERTION LOSS	1-100 MHz	125 MHz
	-1.0 dB MAX	-3.0 dB MAX
RETURN LOSS $Z_{OUT} = 100$ OHMS	5 MHz	30-80 MHz
	-18 dB MIN	-16 + 26LOG ₁₀ (f/30 MHz) dB MIN
CROSSTALK (TX-RX)	1 MHz	5-100 MHz
	-62 dB MIN	-45 + 21LOG ₁₀ (f/30 MHz) dB MIN
DIFFERENTIAL TO COMMON MODE REJECTION	5-200 MHz	
	-50 + 17LOG ₁₀ (f/5 MHz) dB MIN	
INPUT - OUTPUT ISOLATION	1500 VAC MINIMUM FOR 60 SECONDS	



SUGGESTED LAND PATTERN



DIMENSIONS ARE IN INCHES WITH THE FOLLOWING TOLERANCES:

.XX= ± 0.01
.XXX= ± 0.005

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,100D,1:1,SM,TU	HM1188NL-10	PS-2005.001-F	3 OF 3	HM1188NL	E