

### **DM74LS244**

# Octal 3-STATE Buffers/Line Drivers/Line Receivers

### **General Description**

These buffers/line drivers are designed to improve both the performance and PC board density of 3-STATE buffers/ drivers employed as memory-address drivers, clock drivers, and bus-oriented transmitters/receivers. Featuring 400 mV of hysteresis at each low current PNP data line input, they provide improved noise rejection and high fanout outputs and can be used to drive terminated lines down to 133 $\Omega$ .

#### **Features**

- 3-STATE outputs drive bus lines directly
- PNP inputs reduce DC loading on bus lines
- Hysteresis at data inputs improves noise margins

■ Typical I<sub>OL</sub> (sink current)

54LS . 12 mA

74LS 24 mA

■ Typical I<sub>OH</sub> (source current)

54LS -12 mA 74LS -15 mA

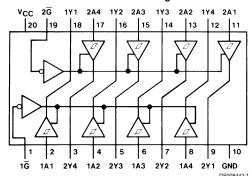
■ Typical propagation delay times Inverting 10.5 ns

Noninverting 12 ns

- Typical enable/disable time
- Typical power dissipation (enabled) Inverting 130 mW Noninverting 135 mW

### **Connection Diagram**

#### **Dual-In-Line Package**



Order Number 54LS244DMQB, 54LS244FMQB, 54LS244LMQB, DM74LS244WM or DM74LS244N See Package Number E20A, J20A, M20B, N20A or W20A

#### **Function Table**

Inp	uts	Output		
G	Α	Y		
L	L	L		
L	Н	н		
Н	X	Z		

L = Low Logic Level

H = High Logic Level

X = Either Low or High Logic Level

Z = High Impedance

**Absolute Maximum Ratings** (Note 1)

54LS DM74LS -55°C to +125°C 0°C to +70°C

Supply Voltage Input Voltage

7V 7V

Storage Temperature Range

-65°C to +150°C

Operating Free Air Temperature Range

# **Recommended Operating Conditions**

Symbol	Parameter		54LS244			DM74LS24	4	Units
		Min	Nom	Max	Min	Nom	Max	
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V <sub>IH</sub>	High Level Input Voltage	2			2			V
V <sub>IL</sub>	Low Level Input Voltage			0.7			0.8	V
I <sub>OH</sub>	High Level Output Current			-12			-15	mA
I <sub>OL</sub>	Low Level Output Current			12			24	mA
T <sub>A</sub>	Free Air Operating Temperature	-55		125	0		70	°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

#### **Electrical Characteristics**

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions			Min	Typ (Note 2)	Max	Units
Vı	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>I</sub> = -18 mA					-1.5	V
HYS	Hysteresis (V <sub>T+</sub> – V <sub>T-</sub> ) Data Inputs Only	V <sub>CC</sub> = Min			0.2	0.4		V
V <sub>OH</sub>	High Level Output Voltage	V <sub>CC</sub> = Min, V <sub>IH</sub> = Min DM74			2.7			
		V <sub>IL</sub> = Max, I <sub>OH</sub> =	/ <sub>IL</sub> = Max, I <sub>OH</sub> = -1 mA					
		V <sub>CC</sub> = Min, V <sub>IH</sub> =	$V_{CC} = Min, V_{IH} = Min$ 54LS/DM74		2.4	3.4		V
		$V_{IL} = Max$ , $I_{OH} = -3 \text{ mA}$						
		V <sub>CC</sub> = Min, V <sub>IH</sub> =	= Min	54LS/DM74	2			
		$V_{IL} = 0.5V, I_{OH} =$	- Max					
V <sub>OL</sub>	Low Level Output Voltage	V <sub>CC</sub> = Min	I <sub>OL</sub> = 12 mA	54LS/DM74			0.4	
		V <sub>IL</sub> = Max	I <sub>OL</sub> = Max	DM74			0.5	V
		V <sub>IH</sub> = Min						
I <sub>OZH</sub>	Off-State Output Current,	V <sub>CC</sub> = Max	V <sub>O</sub> = 2.7V	•			20	μA
	High Level Voltage Applied	V <sub>IL</sub> = Max						
I <sub>OZL</sub>	Off-State Output Current,	V <sub>IH</sub> = Min	V <sub>O</sub> = 0.4V				-20	μA
	Low Level Voltage Applied							
I <sub>I</sub>	Input Current at Maximum	V <sub>CC</sub> = Max	V <sub>I</sub> = 7V (DM74)				0.1	mA
	Input Voltage		V <sub>I</sub> = 10V (54LS)					
I <sub>IH</sub>	High Level Input Current	V <sub>CC</sub> = Max	V <sub>I</sub> = 2.7V				20	μA
I <sub>IL</sub>	Low Level Input Current	V <sub>CC</sub> = Max	V <sub>I</sub> = 0.4V		-0.5		-200	μA
Ios	Short Circuit Output Current V <sub>CC</sub> = Max (Note 3) 54LS		54LS	-50		-225	mA	
				DM74	-40			
I <sub>cc</sub>	Supply Current	V <sub>CC</sub> = Max,	Outputs High			13	23	
		Outputs Open	Outputs Low			27	46	mA
			Outputs Disabled			32	54	

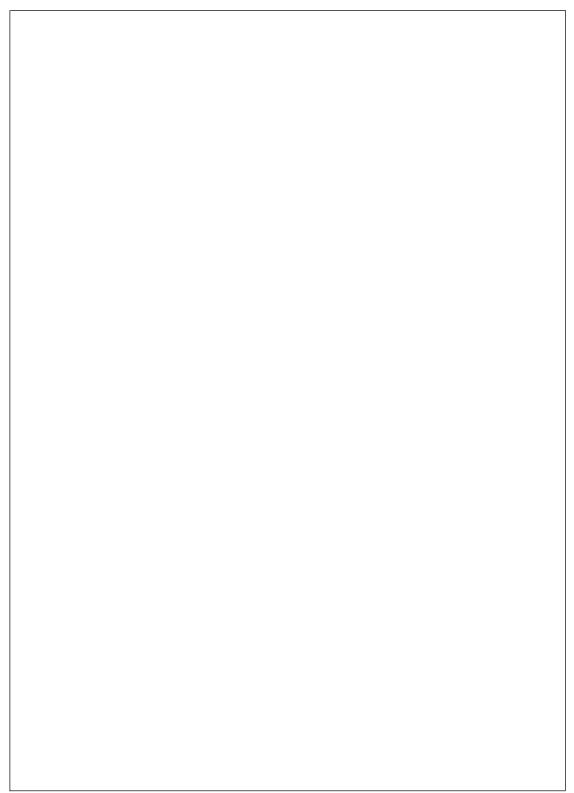
Note 2: All typicals are at  $V_{CC}$  = 5V,  $T_A$  = 25°C.

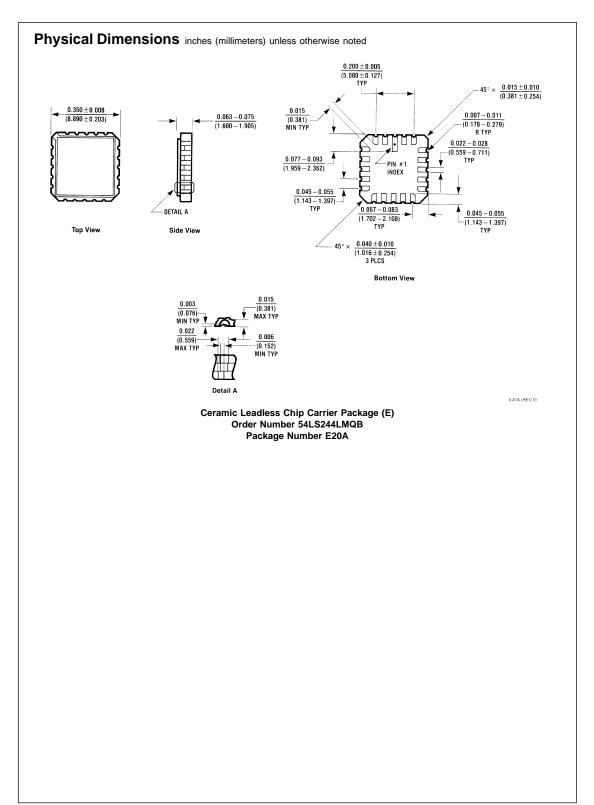
Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.

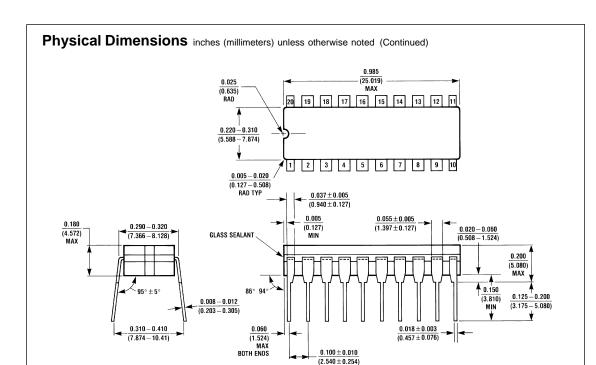
# Switching Characteristics at $V_{CC}$ = 5V, $T_A$ = 25°C

Symbol	Parameter	Conditions	54LS Max	DM74LS Max	Units
t <sub>PLH</sub>	Propagation Delay Time	C <sub>L</sub> = 45 pF	18	18	ns
	Low to High Level Output	$R_L = 667\Omega$			
t <sub>PHL</sub>	Propagation Delay Time	C <sub>L</sub> = 45 pF	18	18	ns
	High to Low Level Output	$R_L = 667\Omega$			
t <sub>PZL</sub>	Output Enable Time to	C <sub>L</sub> = 45 pF	30	30	ns
	Low Level	$R_L = 667\Omega$			
t <sub>PZH</sub>	Output Enable Time to	C <sub>L</sub> = 45 pF	23	23	ns
	High Level	$R_L = 667\Omega$			
t <sub>PLZ</sub>	Output Disable Time	C <sub>L</sub> = 5 pF	25	25	ns
	from Low Level	$R_L = 667\Omega$			
t <sub>PHZ</sub>	Output Disable Time	C <sub>L</sub> = 5 pF	18	18	ns
	from High Level	$R_L = 667\Omega$			
t <sub>PLH</sub>	Propagation Delay Time	C <sub>L</sub> = 150 pF		21	ns
	Low to High Level Output	$R_L = 667\Omega$			
t <sub>PHL</sub>	Propagation Delay Time	C <sub>L</sub> = 150 pF		22	ns
	High to Low Level Output	$R_L = 667\Omega$			
t <sub>PZL</sub>	Output Enable Time to	C <sub>L</sub> = 150 pF		33	ns
	Low Level	$R_L = 667\Omega$			
t <sub>PZH</sub>	Output Enable Time to	C <sub>L</sub> = 150 pF		26	ns
	High Level	$R_L = 667\Omega$			

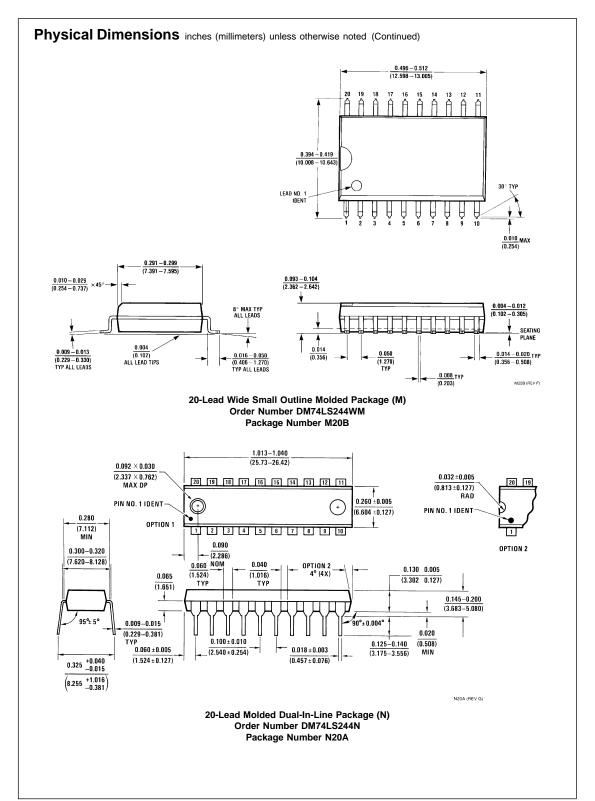
Note 4: 54LS Output Load is  $C_L$  = 50 pF for  $t_{PLH}$ ,  $t_{PHL}$ ,  $t_{PZL}$  and  $t_{PZH}$ .



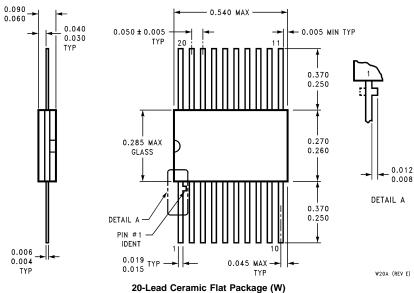




20-Lead Ceramic Dual-In-Line Package (J) Order Number 54LS244DMQB Package Number J20A



## Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



# Order Number 54LS244FMQB Package Number W20A

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