

Class-AB Speaker Amplifiers

5W+5W Stereo Speaker Amplifiers



BA5406,BA5417 No.13077EDT02

Description

The BA5406/BA5417 is a dual OTL monolithic power IC with two built-in, high output speaker amplifier circuits. High output of 5W×2 can be produced when $V_{\rm CC}$ =12 V and $R_{\rm L}$ =3 Ω , and 2.8 W×2 when $V_{\rm CC}$ =9V and $R_{\rm L}$ =3 Ω . The BA5406, which uses a high allowable power dissipation package, has a simple heatsink design. The BA5417 not only exceeds basic characteristics, but also has a built-in soft clip circuit, thermal shutdown and standby circuits.

Features

BA5406

- 1) Good low voltage characteristics (Operation from Vcc=5 V)
- 2) Ripple filter (6pin) also can be used as muting pin (Make 6pin GND potential)
- 3) Small thermal resistance package and simple heatsink design

BA5417

- 1) Small pop noise when standby switches ON/OFF
- 2) Built-in circuit to prevent ripple addition when motor starts
- 3) Built-in thermal shutdown circuit
- 4) Built-in standby switch circuit
- 5) Built-in soft clip circuit

Applications

Stereo radio cassette players, mini-audio systems, LCD TVs, etc.

Line up matrix

Part No.	BA5406	BA5417	Units
Supply voltage	5 ~ 15	6 ~ 15	V
Power dissipation	20	15	W
Quiescent current	40	22	mA
Standby current	_	0	μA
Closed loop voltage gain	46	45	dB
Output noise voltage	0.6	0.3	mVrms
Total harmonic distortion	0.3	0.1	%
Ripple rejection	_	55	dB
Package	SIP-M12	HSIP15	_

● Absolute maximum ratings (Ta=25°C)

Parameter	Cumbal	Rati	Unit		
Faranielei	Symbol	BA5406	BA5417	Offic	
Supply voltage	Vcc	18 ^{*1}	20 *1	V	
Power dissipation	Pd	20 *2	15 ^{*3}	W	
Operating temperature	Topr	-20 ~ +75	-20 ~ +75	οຶ	
Storage temperature	Tstg	-30 ~ +125	-55 ~ +150	°C	

^{*1} When no signal

Operating range (Ta=25°C)

Parameter	Symbol	Ratings		Unit
Farameter		BA5406	BA5417	Offic
Supply voltage	V _{CC}	5.0 ~ 15.0	6.0 ~ 15.0	V

● Electrical characteristics (BA5406 : Unless otherwise noted, Ta=25°C, Vcc=12V) (BA5417: Unless otherwise noted, Ta=25°C, Vcc=9V)

Parameter		Symbol	Symbol Limits			Conditions
		Syllibol	BA5406	BA5417	Unit.	Conditions
Quiescent current		I_{O}	40	22	mA	V _{IN} =0Vms
Rated output power		Pout	5.0	5.0	W	THD=10%,Vcc=12V, RL=3 Ω
Closed loop voltage	gain	G _{VC}	46	45	dB	_
Output noise voltage		V_{NO}	0.6	0.3	mVrms	Rg=10kΩ, DIN-Audio
Total harmonic distor	tion	THD	0.3	0.1	%	P _{OUT} =0.5W, f=1kHz
Ripple rejection		RR	_	55	dB	f _{RR} =100Hz,V _{RR} =-10dBm
Crosstalk		СТ	_	65	dB	V _O =0dBm
Standby current		I _{OFF}	_	0	μA	_
Standby pin input current		I _{SIN}	_	0.15	mA	V _{STBY} =V _{CC}
Standby pin control voltage	Activated	V_{STH}	_	3.5 ~ Vcc	V	_
	Not Activated	V_{STL}		0 ~ 1.2	V	

^{*} Note: This IC is not designed to be radiation-resistant.

^{*2} Back metal temperature 75°C *3 Ta=75°C (Using infinite heatsink)

●Block diagram

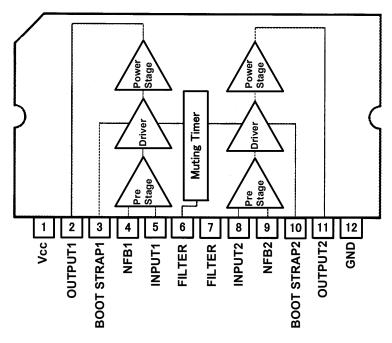


Fig.1 BA5406

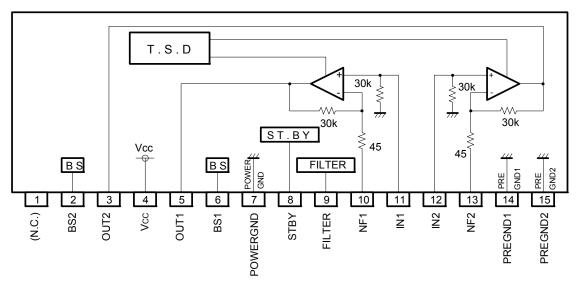


Fig.2 BA5417

●Measurement circuit

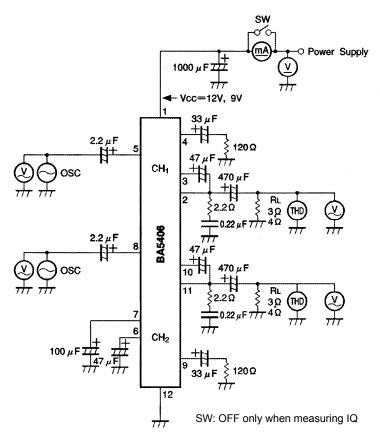
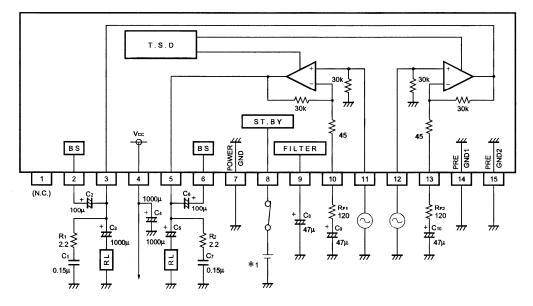


Fig.3 BA5406



*1 V_{STBY}=3.5V-Vcc

Fig.4 BA5417

●Application circuit BA5406

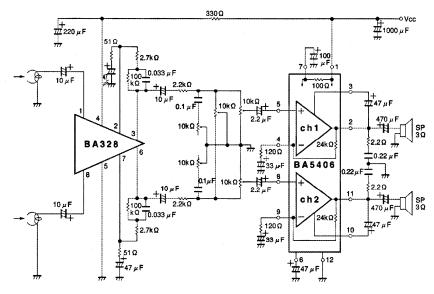
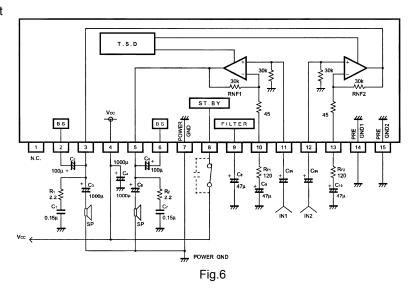


Fig.5

BA5417 OTL mode circuit



BTL mode circuit

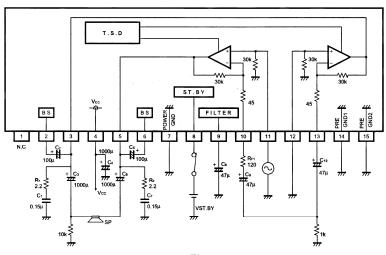
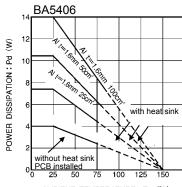


Fig.7

■Reference data



AMBIENT TEMPERATURE : Ta (℃) Fig.8 Thermal derating curve

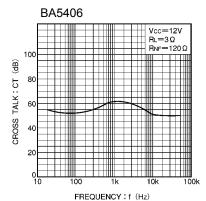
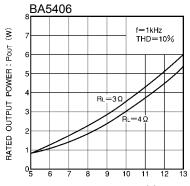


Fig.11Crosstalk vs frequency



SUPPLY VOLTAGE: Vcc (V)
Fig.14 Output power
vs power supply voltage

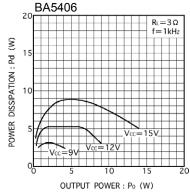


Fig. 17 Power dissipation vs Output power(3)

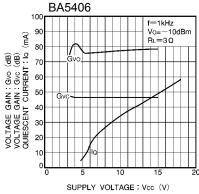


Fig.9 Quiescent current and voltage gain vs Supply voltage

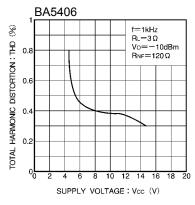


Fig.12 Distortion vs power supply voltage

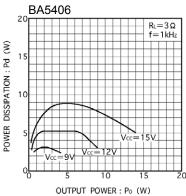


Fig.15 Power dissipation vs Output power(1)

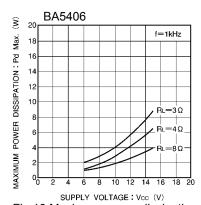


Fig.18 Maximum power dissipation vs Supply voltage

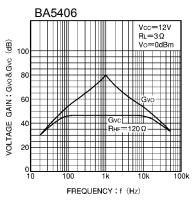


Fig.10 Voltage gain vs frequency

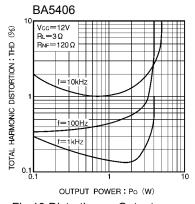


Fig.13 Distortion vs Output power

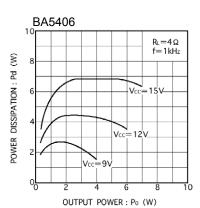


Fig.16 Power dissipation vs Output power(2)

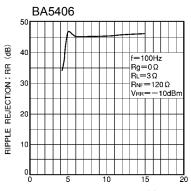


Fig. 19 Ripple rejection ratio vs Supply voltage

BA5417 OTL mode

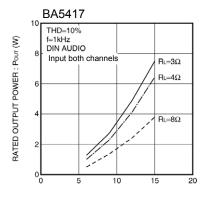
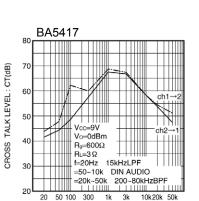
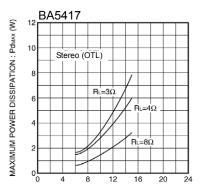


Fig.20 Rated output power vs Supply voltage



FREQUENCY: f (Hz)
Fig.23 Crosstalk vs. Frequency



SUPPLY VOLTAGE: V∞ (V)
Fig.26 Maximum power dissipation vs. Supply voltage

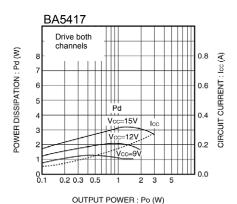


Fig.29 Power dissipation, circuit current vs. Supply Voltage (RL=8Ω)

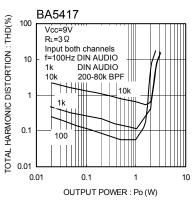


Fig.21 Total harmonic distortion vs Output power

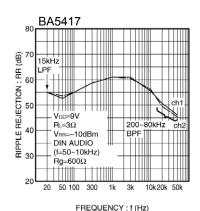
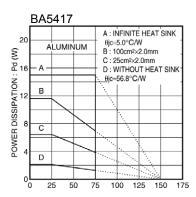
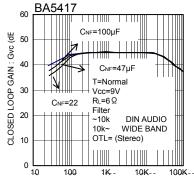


Fig.24 Ripple rejection vs. Frequency



AMBIENT TEMPERATURE : Ta (°C) Fig.27 Thermal derating curve



FREQUENCY: f(Hz)
Fig.30 Closed loop gain vs. Frequency

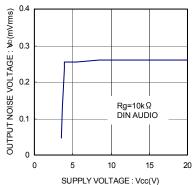


Fig.22 Output noise voltage vs Supply voltage

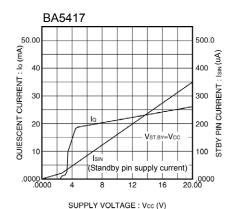


Fig.25 Quiescent, standby pin input current vs. Supply voltage

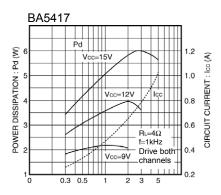


Fig.28 Power dissipation, circuit current vs. Supply Voltage($RL=4\Omega$)

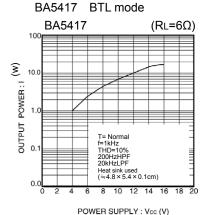


Fig.31 Rated output power vs. Supply Voltage

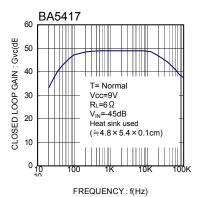
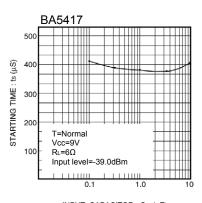
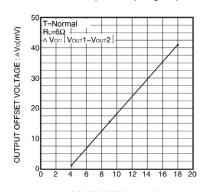


Fig.34 Close loop gain vs. Frequency



INPUT CAPACITOR: Cin (μF)
Fig.37 Starting time
vs. Input coupling capacitor



POWER SUPPLY: Vcc (V)
Fig.40 Output offset voltage
vs. Supply Voltage

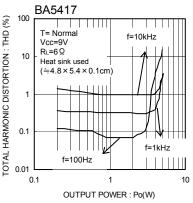


Fig.32 Total harmonic distortion vs. Output power

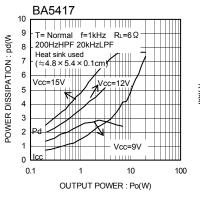


Fig.35 Power dissipation, Supply current vs. Frequency

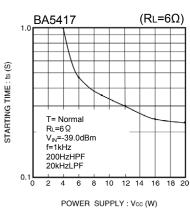
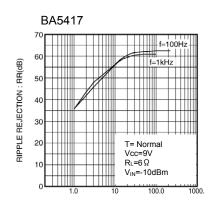


Fig.38 Starting time vs. Supply Voltage



FIPPLE CAPACITOR: CRF (µF)
Fig.41 Ripple rejection
vs. Ripple filter capacitor

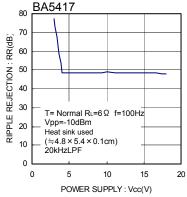


Fig.33 Ripple rejection ratio vs. Supply Voltage

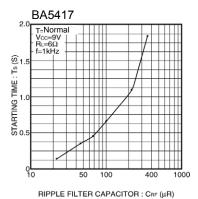


Fig.36 Starting time vs. Ripple filter capacitor

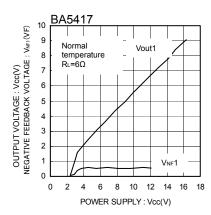


Fig.39 Output voltage, Negative feed back voltage vs. Supply Voltage

Notes for use

- 1) Numbers and data in entries are representative design values and are not guaranteed values of the items.
- 2) Although ROHM is confident that the example application circuit reflects the best possible recommendations, be sure to verify circuit characteristics for your particular application. Modification of constants for other externally connected circuits may cause variations in both static and transient characteristics for external components as well as this Rohm IC. Allow for sufficient margins when determining circuit constants.
- 3) Absolute maximum ratings

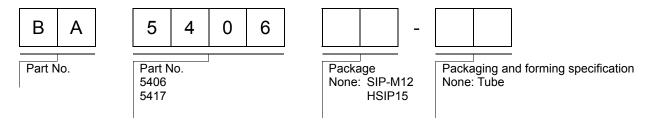
Use of the IC in excess of absolute maximum ratings, such as the applied voltage or operating temperature range (Topr), may result in IC damage. Assumptions should not be made regarding the state of the IC (short mode or open mode) when such damage is suffered. A physical safety measure, such as a fuse, should be implemented when using the IC at times where the absolute maximum ratings may be exceeded.

4) GND potential

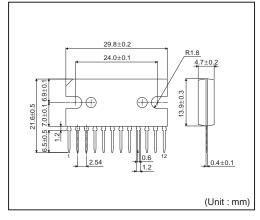
Ensure a minimum GND pin potential in all operating conditions. Make sure that no pins are at a voltage below the GND at any time, regardless of whether it is a transient signal or not.

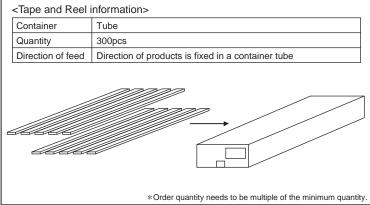
- 5) Thermal design
 - Perform thermal design, in which there are adequate margins, by taking into account the permissible dissipation (Pd) in actual states of use.
- 6) Short circuit between terminals and erroneous mounting
 Pay attention to the assembly direction of the ICs. Wrong mounting direction or shorts between terminals, GND, or other
 components on the circuits, can damage the IC.
- Operation in strong electromagnetic field Using the ICs in a strong electromagnetic field can cause operation malfunction.

Ordering part number

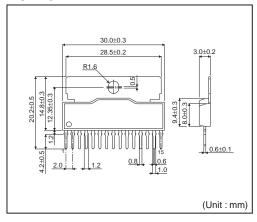


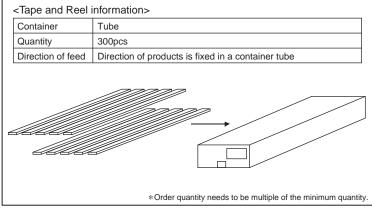
SIP-M12





HSIP15





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(Note1) Medical Equipment Classification of the Specific Applications

JÁPAN	USA	EU	CHINA
CLASSI	СГУССШ	CLASSIIb	СГУССШ
CLASSIV	CLASSII	CLASSIII	— CLASSⅢ

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
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- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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