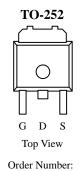


# N-Channel 30-V (D-S), 175°C MOSFET

# **Product Summary**

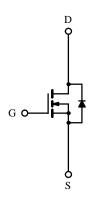
V <sub>DS</sub> (V)	$\mathbf{r_{DS(on)}}(\Omega)$	I <sub>D</sub> (A)
30	$0.010 @ V_{GS} = 10 V$	±15
	$0.019 @ V_{GS} = 4.5 V$	±12





SUD50N03-10

Drain Connected to Tab



N-Channel MOSFET

# Absolute Maximum Ratings ( $T_A = 25^{\circ}C$ Unless Otherwise Noted)

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		$V_{\mathrm{DS}}$	30	17	
Gate-Source Voltage		$V_{ m GS}$	±20	V	
Continuous Drain Current <sup>a</sup>	$T_A = 25^{\circ}C$	I <sub>D</sub>	±15		
Continuous Drain Current	$T_A = 100^{\circ}C$		±10		
Pulsed Drain Current		$I_{\mathrm{DM}}$	± 100	A	
Continuous Source Current (Diode Conduction) <sup>a</sup>		$I_S$	15		
Maximum Power Dissipation	$T_C = 25^{\circ}C$	D_	83	W	
Maximum 1 ower Dissipation	$T_A = 25^{\circ}C$	P <sub>D</sub>	4 <sup>a</sup>		
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 175	°C	

# **Thermal Resistance Ratings**

Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$		30	00/84	
Maximum Junction-to-Case	R <sub>thJC</sub>		1.8	°C/W	

#### Notes

Updates to this data sheet may be obtained via facsimile by calling Siliconix FaxBack, 1-408-970-5600. Please request FaxBack document #70265.

a. Surface Mounted on FR4 Board,  $t \le 10$  sec.

# Siliconix



# Specifications ( $T_J = 25^{\circ}C$ Unless Otherwise Noted)

Parameter	Symbol	<b>Test Condition</b>	Min	Typa	Max	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30				
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.0	2.0		V	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
7 C. W. D. C.	,	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125^{\circ}\text{C}$			50	μA	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			A	
		$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$			0.010	Ω	
Drain-Source On-State Resistance <sup>b</sup>	r <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}, T_J = 125^{\circ}\text{C}$			0.018		
		$V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A}$			0.019		
Forward Transconductance <sup>b</sup>	gfs	$V_{DS} = 15 \text{ V}, I_D = 15 \text{ A}$	20			S	
Dynamic <sup>a</sup>	•		•	•	•		
Input Capacitance	C <sub>iss</sub>			3200	6000	pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, F = 1 \text{ MHz}$		800			
Reverse Transfer Capacitance	C <sub>rss</sub>			150			
Total Gate Charge <sup>c</sup>	$Q_{g}$			55	100	пС	
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 15 \text{ V}, \ V_{GS} = 10 \text{ V}, \ I_D = 50 \text{ A}$		10			
Gate-Drain Charge <sup>c</sup>	Qgd			9			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			16	30	ns	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 15 \text{ V}, R_L = 0.3 \Omega$		8	20		
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$	$I_D \cong 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 2.5 \Omega$		33	60		
Fall Time <sup>c</sup>	$t_{\mathrm{f}}$			20	40		
Source-Drain Diode Ratings and C	haracteristic (	$T_C = 25^{\circ}C$	-	-	-		
Pulsed Current	$I_{SM}$				100	A	
Diode Forward Voltage <sup>b</sup>	$V_{\mathrm{SD}}$	$I_F = 100 \text{ A}, V_{GS} = 0 \text{ V}$		1.2	1.5	V	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	$I_F = 50 \text{ A},  di/dt = 100  A/\mu s$		55	100	ns	

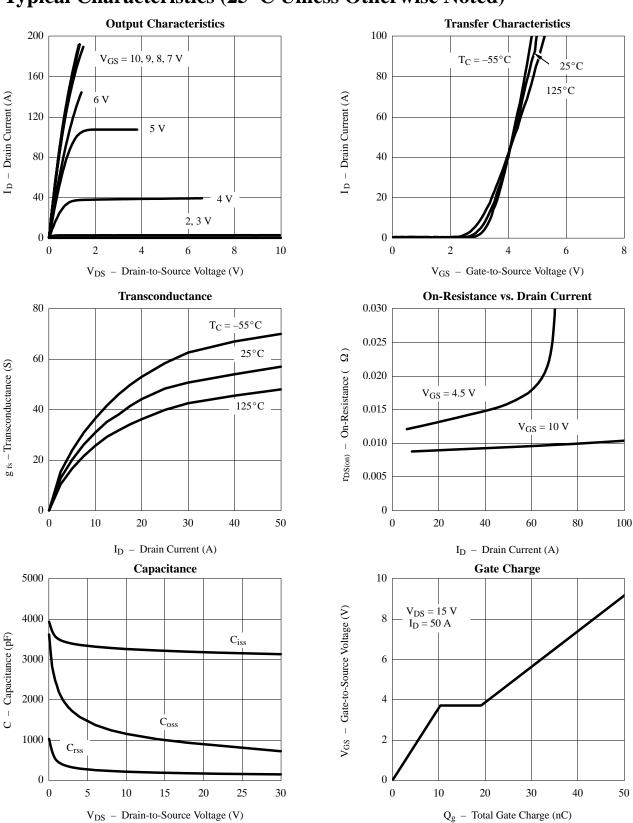
#### Notes

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width  $\leq 300 \,\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- c. Independent of operating temperature.





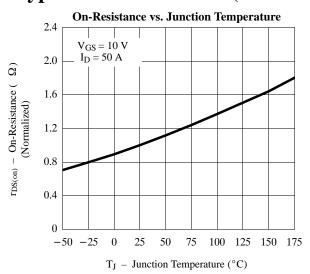
# **Typical Characteristics (25°C Unless Otherwise Noted)**

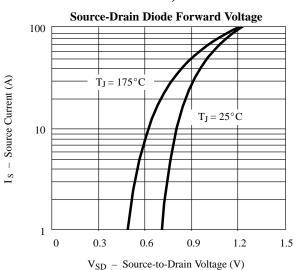


#### **Siliconix**

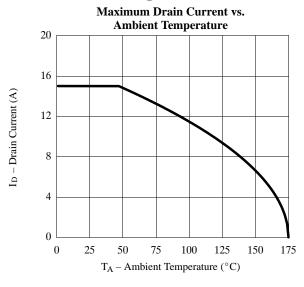


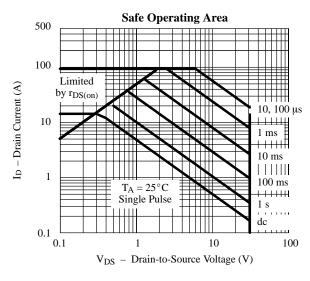
# **Typical Characteristics (25°C Unless Otherwise Noted)**

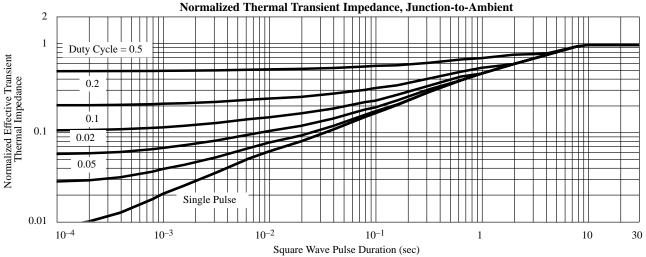




### **Thermal Ratings**







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Document Number: 91000 www.vishay.com Revision: 08-Apr-05



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Document Number: 91000 Revision: 18-Jul-08