

SOT-26



Pin Definition:

- | | |
|----------|-----------|
| 1. Drain | 6. Drain |
| 2. Drain | 5. Drain |
| 3. Gate | 4. Source |

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (m Ω)	I_D (A)
20	60 @ $V_{GS} = -4.5V$	-4.7
	100 @ $V_{GS} = -2.5V$	-3.8

Features

- Advance Trench Process Technology
- High Density Cell Design fPor Ultra Low On-resistance

Application

- Load Switch
- PA Switch

Ordering Information

Part No.	Package	Packing
TSM3443CX6 RF	SOT-26	3Kpcs / 7" Reel
TSM3443CX6 RFG	SOT-26	3Kpcs / 7" Reel

Note: "G" denote for Halogen Free Product

Absolute Maximum Rating ($T_A=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current, V_{GS} @4.5V.	I_D	-4.7	A
Pulsed Drain Current, V_{GS} @4.5V	I_{DM}	-20	A
Continuous Source Current (Diode Conduction) ^{a,b}	I_S	-1.7	A
Maximum Power Dissipation	P_D	2	W
		1.3	
Operating Junction Temperature	T_J	+150	$^{\circ}C$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	- 55 to +150	$^{\circ}C$

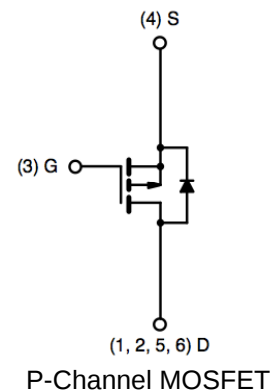
Thermal Performance

Parameter	Symbol	Limit	Unit
Junction to Case Thermal Resistance	$R_{\theta JC}$	30	$^{\circ}C/W$
Junction to Ambient Thermal Resistance (PCB mounted)	$R_{\theta JA}$	80	$^{\circ}C/W$

Note 1: Pulse width limited by the Maximum junction temperature

Note 2: Surface Mounted on FR4 Board, $t \leq 5$ sec

Block Diagram



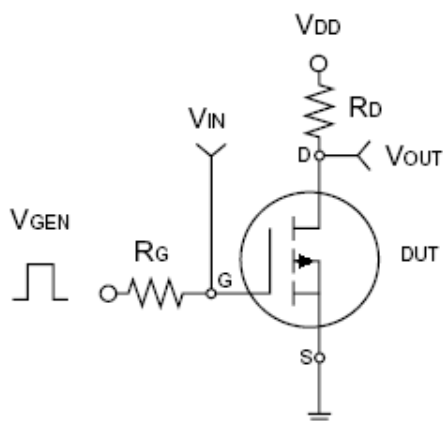
Electrical Specifications (Ta = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = -250uA	BV _{DSS}	-20	--	--	V
Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250μA	V _{GS(TH)}	-0.5	--	-1.4	V
Gate Body Leakage	V _{GS} = ±12V, V _{DS} = 0V	I _{GSS}	--	--	±100	nA
Zero Gate Voltage Drain Current	V _{DS} = -20V, V _{GS} = 0V	I _{DSS}	--	--	-1.0	μA
On-State Drain Current ^a	V _{DS} = -5V, V _{GS} = -4.5V	I _{D(ON)}	-15	--	--	A
Drain-Source On-State Resistance ^a	V _{GS} = -4.5V, I _D = -4.7A	R _{DS(ON)}	--	48	60	mΩ
	V _{GS} = -2.5V, I _D = -3.8A		--	80	100	
Forward Transconductance ^a	V _{DS} = -10V, I _D = -4.7A	g _{fs}	--	11	--	S
Diode Forward Voltage	I _S = -1.7A, V _{GS} = 0V	V _{SD}	--	-0.8	-1.2	V
Dynamic ²						
Total Gate Charge	V _{DS} = -10V, I _D = -4.7A, V _{GS} = -4.5V	Q _g	--	6	9	nC
Gate-Source Charge		Q _{gs}	--	1.4	--	
Gate-Drain Charge		Q _{gd}	--	1.9	--	
Input Capacitance	V _{DS} = -10V, V _{GS} = 0V, f = 1.0MHz	C _{iSS}	--	640	--	pF
Output Capacitance		C _{oSS}	--	180	--	
Reverse Transfer Capacitance		C _{rSS}	--	90	--	
Switching ³						
Turn-On Delay Time	V _{DD} = -10V, R _L = 10Ω, I _D = -1A, V _{GEN} = -4.5V, R _G = 6Ω	t _{d(on)}	--	22	35	nS
Turn-On Rise Time		t _r	--	35	55	
Turn-Off Delay Time		t _{d(off)}	--	45	70	
Turn-Off Fall Time		t _f	--	25	50	

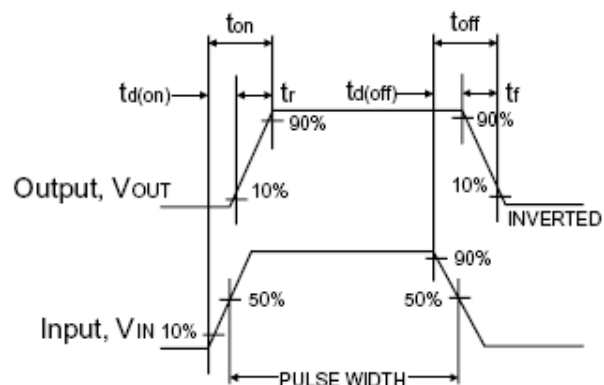
Note 1: Pulse test: P_W ≤ 300μs, duty cycle ≤ 2%

Note 2: For DESIGN AID ONLY, not subject to production testing.

Note 3: Switching time is essentially independent of operating temperature.



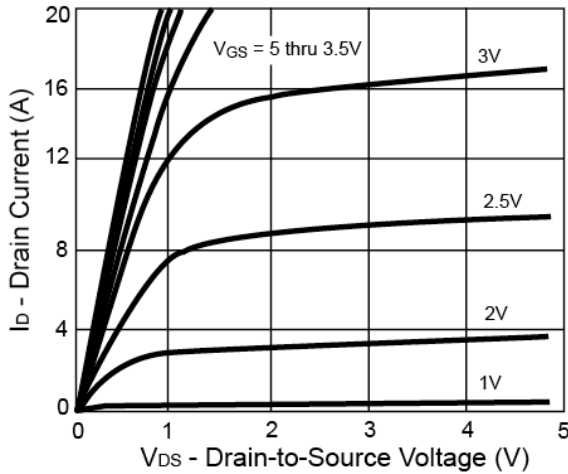
Switching Test Circuit



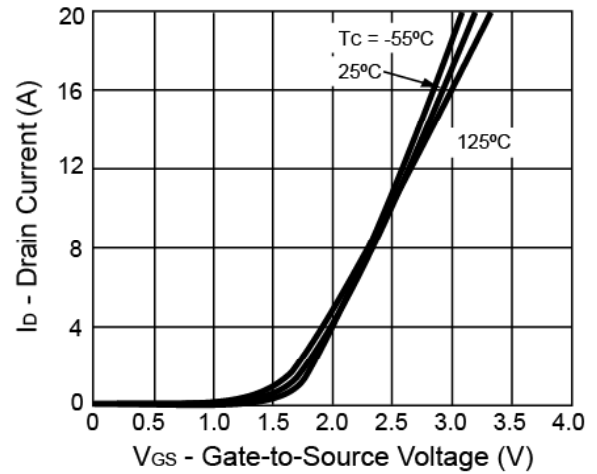
Switchin Waveforms

Electrical Characteristics Curve ($T_a = 25^\circ\text{C}$, unless otherwise noted)

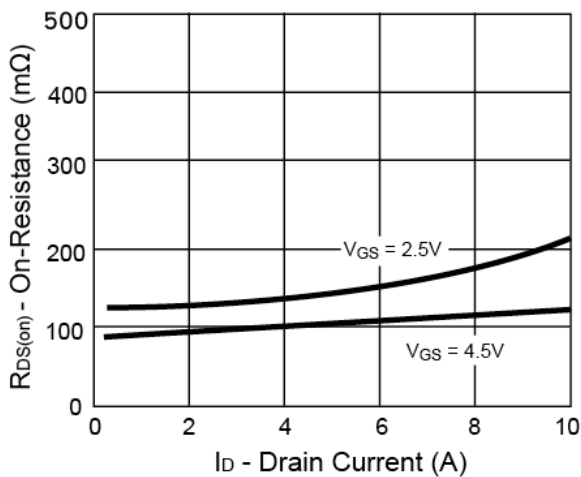
Output Characteristics



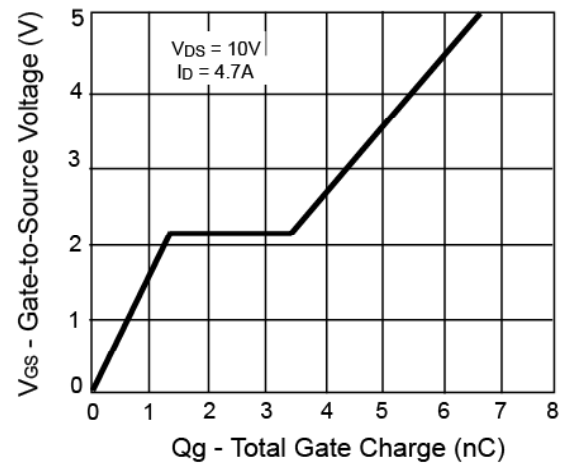
Transfer Characteristics



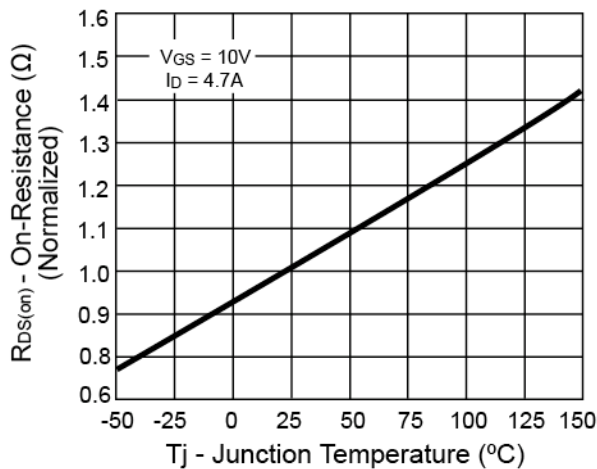
On-Resistance vs. Drain Current



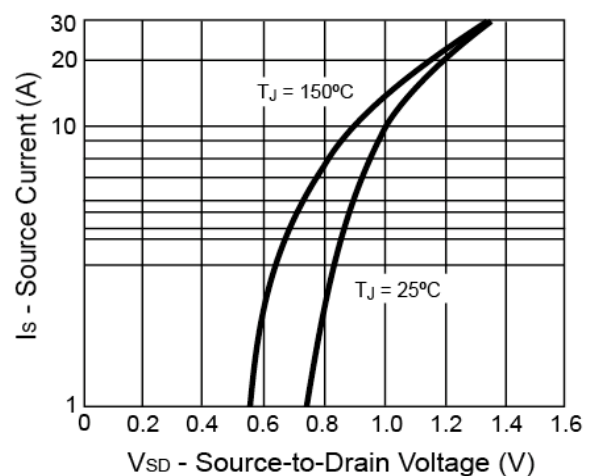
Gate Charge



On-Resistance vs. Junction Temperature

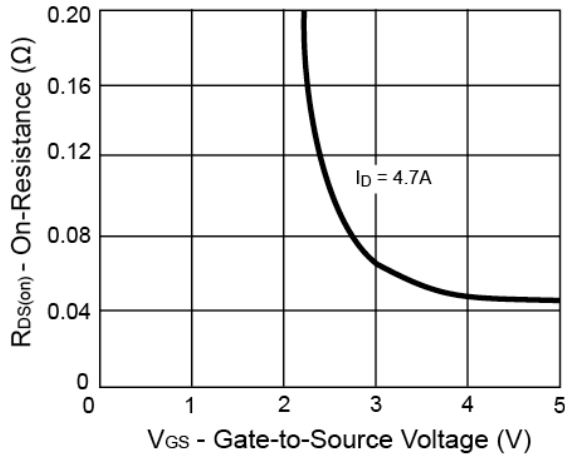


Source-Drain Diode Forward Voltage

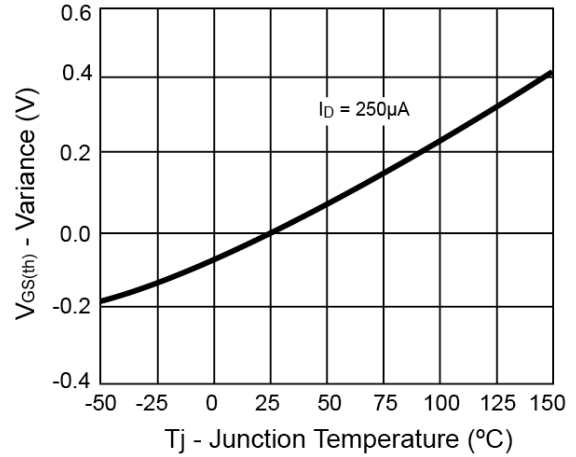


Electrical Characteristics Curve ($T_a = 25^\circ\text{C}$, unless otherwise noted)

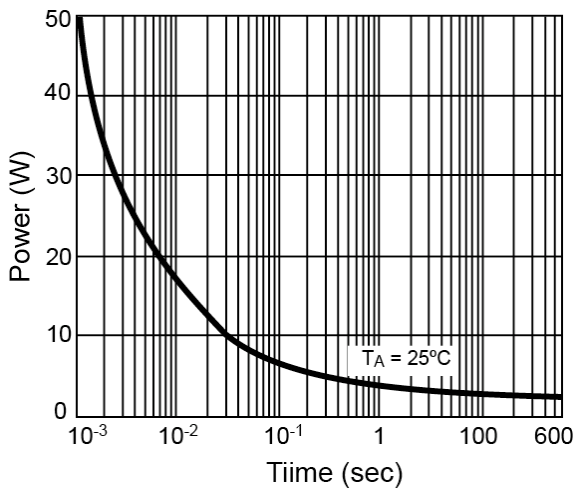
On-Resistance vs. Gate-Source Voltage



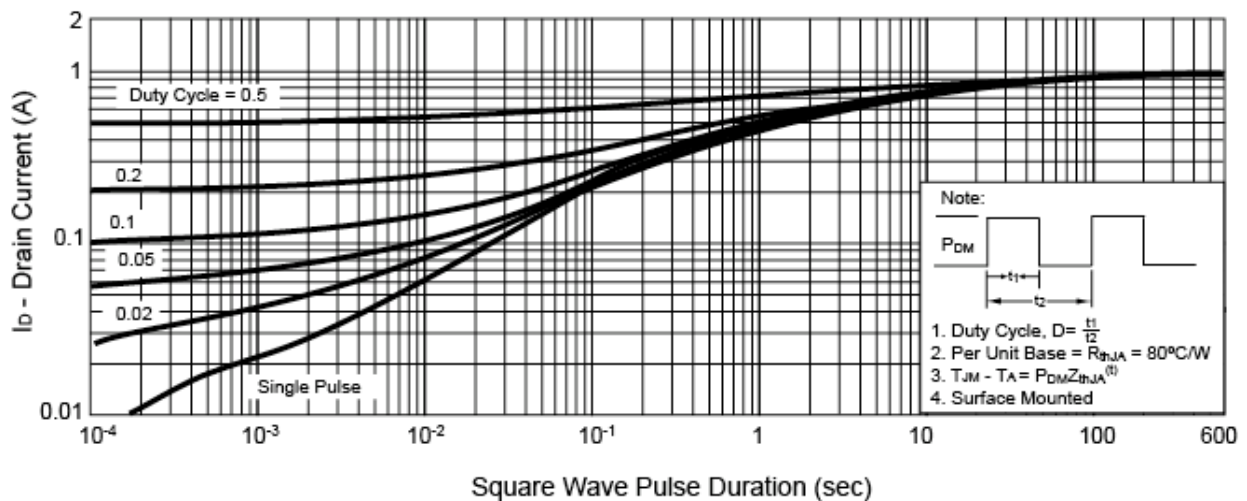
Threshold Voltage



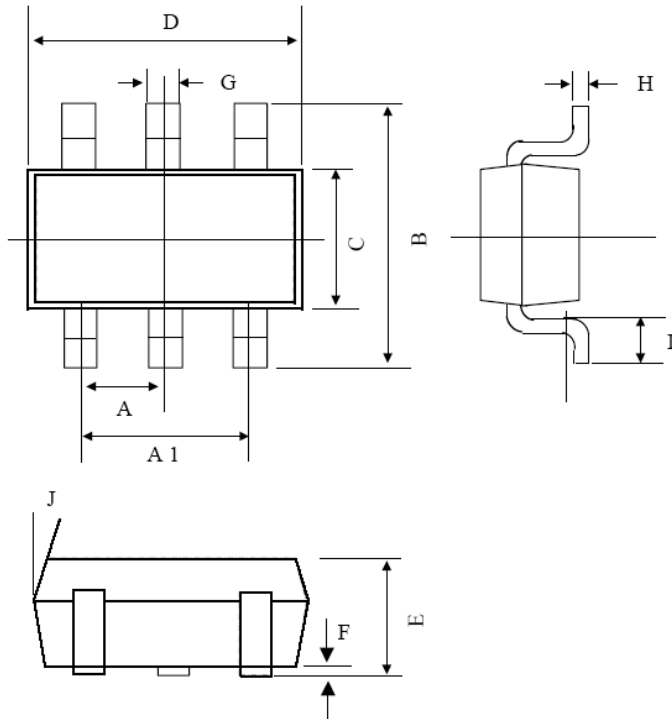
Single Pulse Power



Normalized Thermal Transient Impedance, Junction-to-Ambient

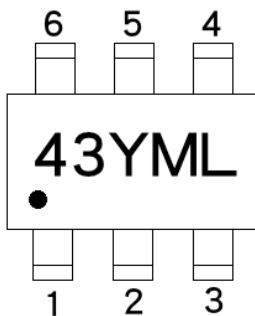


SOT-26 Mechanical Drawing



SOT-26 DIMENSION						
DIM	MILLIMETERS			INCHES		
	MIN	TYP	MAX	MIN	TYP	MAX
A	0.95 BSC			0.0374 BSC		
A1	1.9 BSC			0.0748 BSC		
B	2.60	2.80	3.00	0.1024	0.1102	0.1181
C	1.40	1.50	1.70	0.0551	0.0591	0.0669
D	2.80	2.90	3.10	0.1101	0.1142	0.1220
E	1.00	1.10	1.20	0.0394	0.0433	0.0472
F	0.00	--	0.10	0.00		0.0039
G	0.35	0.40	0.50	0.0138	0.0157	0.0197
H	0.10	0.15	0.20	0.0039	0.0059	0.0079
I	0.30	--	0.60	0.0118	--	0.0236
J	5°	--	10°	5°	--	10°

Marking Diagram



43 = Device Code

Y = Year Code

M = Month Code

(**A**=Jan, **B**=Feb, **C**=Mar, **D**=Apr, **E**=May, **F**=Jun, **G**=Jul, **H**=Aug, **I**=Sep, **J**=Oct, **K**=Nov, **L**=Dec)

= Month Code for Halogen Free Product

(**O**=Jan, **P**=Feb, **Q**=Mar, **R**=Apr, **S**=May, **T**=Jun, **U**=Jul, **V**=Aug, **W**=Sep, **X**=Oct, **Y**=Nov, **Z**=Dec)

L = Lot Code

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