

# Errata Sheet

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**Device:** **C501G-1E**

**Stepping Code / Marking:** **BA**

**Package:** **P-MQFP-44, P-LCC-44, P-DIP-40**

This Errata Sheet describes the deviations from the current user documentation. The classification and numbering system is module oriented in a continual ascending sequence over several derivatives, as well already solved deviations are included. So gaps inside this enumeration could occur.

The current documentation is: Data Sheet 09.99  
User's Manual 04.97  
Instruction Set Manual 05.98

**Note:** *Devices marked with EES- or ES are engineering samples which may not be completely tested in all functional and electrical characteristics, therefore they should be used for evaluation only.*

The specific test conditions for EES and ES are documented in a separate Status Sheet.

## Functional Problems:

The following malfunctions are known in this step:

None.

## Application Hint:

### Port2.1: Port 2 behavior during MOVX @Ri instructions

(If required, refer to Problem 4 in previous errata sheet for step AA, aar12.pdf)

During such instructions (and also for @dptr), whether code is executed from external memory or internal memory, Port 2 will always output the high-address byte. During such outputs, if Port 2 pin outputs an address bit that is a '1', strong pullups are used internally. Port 2 lines should therefore never be used for general purpose I/O. The last sentence in User's Manual 04.97 section 4.1.3 is applicable when code is executed out of internal program memory, too.

## Workaround:

None.

## Deviation from Electrical- and Timing Specification:

The following deviations of electrical and timing parameters from the specification are known in this step:

None.

## History List (since last CPU Step AA)

### Functional Problems/Application Hints

Functional Problem/Hint	Short Description	Fixed
CPU.1	Wrong destination address at AJMP and ACALL instructions	Step BA
Port2.1	Port 2 behavior during MOVX @Ri instructions	Hint
T2.1	Timer 2 - Concurrent Access on T2CON	Step BA
Mfr-Code.1	Different manufacturer code	Step BA
OTP.1	OTP protection, the EA#-pin and MOVC instructions	DS 09.99

### AC/DC Deviations

AC/DC Deviation	Short Description	Fixed
DC.1	Test condition for $I_{PD}$ (power down mode)	Step BA

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