

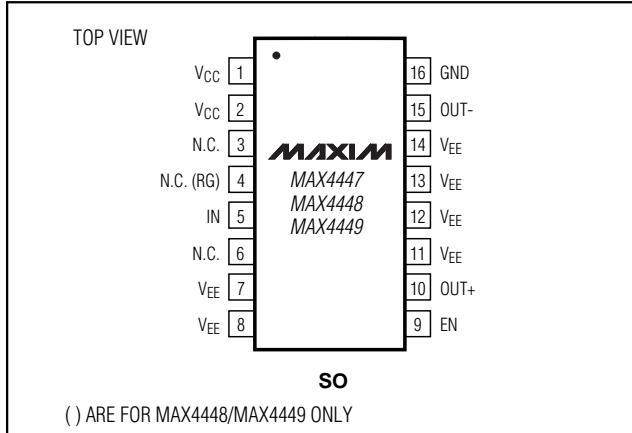
**MAXIM****6500V/μs, Wideband, High-Output-Current, Single-Ended-to-Differential Line Drivers with Enable****General Description**

The MAX4447/MAX4448/MAX4449 single-ended-to-differential line drivers are designed for high-speed communications. Using current feedback for greater bandwidth, these devices deliver full-power bandwidths up to 405MHz and feature slew rates as high as 6500V/μs. The MAX4447 has a fixed gain of +2V/V and a small-signal bandwidth of 430MHz. The MAX4448/MAX4449 have small-signal bandwidths of 330MHz and 400MHz, respectively, and are internally compensated for minimum gain configurations of +2V/V and +5V/V, respectively. For greater design flexibility, the MAX4448/MAX4449 allow for variable gain selection using external gain-setting resistors. A low-power enable mode reduces current consumption below 5.5mA and places the outputs in a high-impedance state.

The MAX4447/MAX4448/MAX4449 can deliver differential output swings of  $\pm 6.2V$  from  $\pm 5V$  supplies with a  $50\Omega$  load. Excellent differential gain/phase and noise specifications make these amplifiers ideal for a wide variety of video and RF signal-processing and transmission applications.

**Applications**

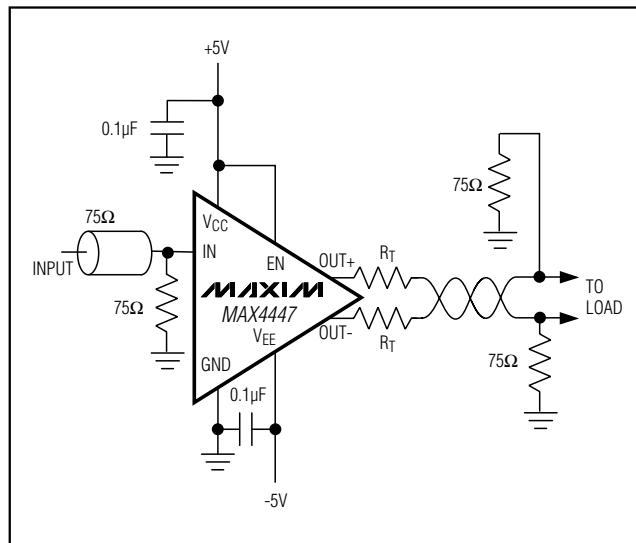
- Differential Line Driver
- Single-Ended-to-Differential Conversion
- High-Speed Differential Transmitter
- Coaxial to Twisted-Pair Converter
- Differential Pulse Amplifier
- Differential ADC Driver
- xDSL Applications
- Video and RF Signal Processing and Transmission

**Pin Configuration****Features**

- ♦ 6500V/μs Slew Rate (MAX4449)
- ♦ Small-Signal Bandwidth
  - 430MHz (MAX4447)
  - 330MHz (MAX4448)
  - 400MHz (MAX4449)
- ♦ 200MHz 0.1dB Gain Flatness (MAX4447)
- ♦ 130mA Output Drive Current
- ♦ +2V/V Internally Fixed Gain (MAX4447)
- ♦ External Gain Selection
  - $\geq +2V/V$  (MAX4448)
  - $\geq +5V/V$  (MAX4449)
- ♦ -78dB SFDR at 100kHz
- ♦ Low Differential Gain/Phase: 0.01%/0.02°
- ♦ Ultra-Low Noise:  $23nV/\sqrt{Hz}$  at  $f_{IN} = 1MHz$
- ♦ 8ns Settling Time to 0.1%

**Ordering Information**

PART	TEMP. RANGE	PIN-PACKAGE
<b>MAX4447ESE</b>	-40°C to +85°C	16 Narrow SO
<b>MAX4448ESE</b>	-40°C to +85°C	16 Narrow SO
<b>MAX4449ESE</b>	-40°C to +85°C	16 Narrow SO

**Typical Operating Circuit****MAXIM**

Maxim Integrated Products 1

For free samples & the latest literature: <http://www.maxim-ic.com>, or phone 1-800-998-8800.  
For small orders, phone 1-800-835-8769.

**MAX4447/MAX4448/MAX4449**

# 6500V/μs, Wideband, High-Output-Current, Single-Ended-to-Differential Line Drivers with Enable

## ABSOLUTE MAXIMUM RATINGS

V<sub>CC</sub> to V<sub>EE</sub> ..... +12V  
 Voltage on IN, EN, OUT+, OUT-, RG ....(V<sub>EE</sub> - 0.3V) to (V<sub>CC</sub> + 0.3V)  
 Output Short-Circuit Duration to GND ..... Indefinite  
 Continuous Power Dissipation (T<sub>A</sub> = +70°C)  
 16-Pin Narrow SO (derate 20mW/°C above +70°C) ... 1600mW

Operating Temperature Range ..... -40°C to +85°C  
 Storage Temperature Range ..... -65°C to +150°C  
 Lead Temperature (soldering, 10sec) ..... +300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## DC ELECTRICAL CHARACTERISTICS

(V<sub>CC</sub> = +5V, V<sub>EE</sub> = -5V, V<sub>EN</sub> ≥ 2V, V<sub>OUT</sub> = V<sub>OUT+</sub> - V<sub>OUT-</sub>, R<sub>L</sub> = ∞, T<sub>A</sub> = T<sub>MIN</sub> to T<sub>MAX</sub>, unless otherwise noted. Typical values are at T<sub>A</sub> = +25°C.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Operating Supply Voltage Range	V <sub>CC</sub>	V <sub>CC</sub> guaranteed by PSRR test		4.5	5.5		V
	V <sub>EE</sub>	V <sub>EE</sub> guaranteed by PSRR test		-5.5	-4.5		
Input Voltage Range	V <sub>IN</sub>	Guaranteed by gain-error test		-6/A <sub>V</sub>		+6/A <sub>V</sub>	V
Input Offset Voltage	V <sub>OS</sub>	V <sub>IN</sub> = 0			1.3	50	mV
Input Offset Voltage Temperature Coefficient	T <sub>CVOS</sub>	V <sub>IN</sub> = 0			25		μV/°C
Input Bias Current	I <sub>B</sub>	V <sub>IN</sub> = 0			7	45	μA
Input Resistance	R <sub>IN</sub>	-3.0V ≤ V <sub>IN</sub> ≤ 3.0V			50		kΩ
Gain	A <sub>V</sub>	-6V ≤ V <sub>OUT</sub> ≤ 6V	MAX4447		2		V/V
			MAX4448/MAX4449 (Note 1)		2 • (1+300/R <sub>G</sub> )		
Gain Error		-6V ≤ V <sub>OUT</sub> ≤ 6V	MAX4447	0.1	2		%
			MAX4448/MAX4449	-0.3	5		
Gain Drift		V <sub>OUT</sub> = 0	MAX4447		-0.002		%/°C
			MAX4448/MAX4449		0.01		
Output Voltage Swing	V <sub>OUT</sub>	R <sub>L</sub> = 100Ω between OUT+ and OUT-		±6.3	±7.4		V
			R <sub>L</sub> = 50Ω between OUT+ and OUT-	±5.2	±6.2		
Output Current Drive	I <sub>OUT</sub>	R <sub>L</sub> = 20Ω between OUT+ and OUT-		90	130		mA
Output Short-Circuit Current	I <sub>SC</sub>	Short circuit to GND			140		mA
Power-Supply Rejection Ratio	PSRR	V <sub>S</sub> = ±4.5V to ±5.5V		53	75		dB
Output Leakage Current	I <sub>OUT(OFF)</sub>	V <sub>EN</sub> = 0, V <sub>OUT+</sub> = V <sub>OUT-</sub> = 3.15V or -3.15V		4	30		μA
EN Logic Low Threshold	V <sub>IL</sub>					0.8	V
EN Logic High Threshold	V <sub>IH</sub>			2			V
EN Logic Input Low Current	I <sub>IL</sub>	V <sub>EN</sub> = 0			-2.5	10	μA
EN Logic Input High Current	I <sub>IH</sub>	V <sub>EN</sub> = 5V			0.8	10	μA
Quiescent Current	I <sub>Q</sub>	V <sub>IN</sub> = 0, V <sub>EN</sub> ≥ V <sub>IH</sub>		46	55		mA
		V <sub>IN</sub> = 0, V <sub>EN</sub> ≤ V <sub>IL</sub>		3.2	5.5		

# 6500V/μs, Wideband, High-Output-Current, Single-Ended-to-Differential Line Drivers with Enable

## AC ELECTRICAL CHARACTERISTICS

(V<sub>CC</sub> = +5V, V<sub>EE</sub> = -5V, R<sub>L</sub> = 100Ω between OUT+ and OUT-, AvCL = +2V/V for MAX4447/MAX4448, AvCL = +5V/V for MAX4449, V<sub>OUT</sub> = V<sub>OUT</sub>+ - V<sub>OUT</sub>-, TA = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Small-Signal -3dB Bandwidth	BW <sub>SS</sub>	V <sub>OUT</sub> = 100mVp-p	MAX4447	430		MHz
			MAX4448	330		
			MAX4449	400		
Large-Signal -3dB Bandwidth	BW <sub>LS</sub>	V <sub>OUT</sub> = 8Vp-p	MAX4449	250		MHz
		V <sub>OUT</sub> = 4Vp-p	MAX4447	250		
			MAX4448	260		
			MAX4449	320		
		V <sub>OUT</sub> = 2Vp-p	MAX4447	285		
			MAX4448	310		
			MAX4449	405		
0.1dB Gain Flatness		V <sub>OUT</sub> = 100mVp-p	MAX4447	200		MHz
			MAX4448	40		
			MAX4449	140		
Slew Rate (Note 2)	SR	V <sub>OUT</sub> = 8V step	MAX4447	5700		V/μs
			MAX4448	4300		
			MAX4449	6500		
		V <sub>OUT</sub> = 4V step	MAX4447	3000		
			MAX4448	3000		
			MAX4449	3700		
		V <sub>OUT</sub> = 2V step	MAX4447	1700		
			MAX4448	1900		
			MAX4449	1800		
Rise Time (Note 2)	t <sub>RISE</sub>	V <sub>OUT</sub> = 8V step	MAX4447	670		ps
			MAX4448	1030		
			MAX4449	850		
		V <sub>OUT</sub> = 4V step	MAX4447	720		
			MAX4448	820		
			MAX4449	660		
		V <sub>OUT</sub> = 2V step	MAX4447	720		
			MAX4448	520		
			MAX4449	740		

# 6500V/μs, Wideband, High-Output-Current, Single-Ended-to-Differential Line Drivers with Enable

## AC ELECTRICAL CHARACTERISTICS (continued)

(V<sub>CC</sub> = +5V, V<sub>EE</sub> = -5V, R<sub>L</sub> = 100Ω between OUT+ and OUT-, AvCL = +2V/V for MAX4447/MAX4448, AvCL = +5V/V for MAX4449, V<sub>OUT</sub> = V<sub>OUT</sub>+ - V<sub>OUT</sub>-, TA = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Fall Time (Note 2)	t <sub>FALL</sub>	V <sub>OUT</sub> = 8V step	MAX4447	1100		ps
			MAX4448	900		
			MAX4449	900		
		V <sub>OUT</sub> = 4V step	MAX4447	900		
			MAX4448	810		
			MAX4449	780		
		V <sub>OUT</sub> = 2V step	MAX4447	800		
			MAX4448	770		
			MAX4449	660		
Settling Time				8		ns
Spurious-Free Dynamic Range	SFDR	V <sub>OUT</sub> = 2Vp-p	f <sub>C</sub> = 100kHz	-78		dBc
			f <sub>C</sub> = 5MHz	-78		
			f <sub>C</sub> = 20MHz	-62		
			f <sub>C</sub> = 100MHz	-46		
2nd Harmonic Distortion		V <sub>OUT</sub> = 2Vp-p	f <sub>C</sub> = 100kHz	-78		dBc
			f <sub>C</sub> = 5MHz	-78		
			f <sub>C</sub> = 20MHz	-62		
			f <sub>C</sub> = 100MHz	-46		
3rd Harmonic Distortion		V <sub>OUT</sub> = 2Vp-p	f <sub>C</sub> = 100kHz	-86		dBc
			f <sub>C</sub> = 5MHz	-86		
			f <sub>C</sub> = 20MHz	-71		
			f <sub>C</sub> = 100MHz	-54		
Differential Phase Error	DP	NTSC, R <sub>L</sub> = 150Ω		0.02		degrees
Differential Gain Error	DG	NTSC, R <sub>L</sub> = 150Ω		0.01		%
Input Noise Voltage Density	e <sub>N</sub>	f = 1MHz (Note 3)		24		nV/√Hz
Input Noise Current Density	i <sub>N</sub>	f = 1MHz		1.8		pA/√Hz
Output Impedance	Z <sub>OUT</sub> ±	f = 10MHz, each output to ground		1.0		Ω
Enable Time		V <sub>IN</sub> = 1V, V <sub>OUT</sub> settle to within 1%		55		ns
Disable Time		V <sub>IN</sub> = 1V, V <sub>OUT</sub> settle to within 1%		0.4		μs
Power-Up Time	t <sub>ON</sub>	V <sub>IN</sub> = 1V, V <sub>OUT</sub> settle to within 1%		0.08		μs
Power-Down Time	t <sub>OFF</sub>	V <sub>IN</sub> = 1V, V <sub>OUT</sub> settle to within 1%		0.5		μs

**Note 1:** R<sub>G</sub> is the gain resistor. See Figure 1.

**Note 2:** Input step voltage has <100ps rise (fall) time. Measured at the output from 10% to 90% (90% to 10%) levels.

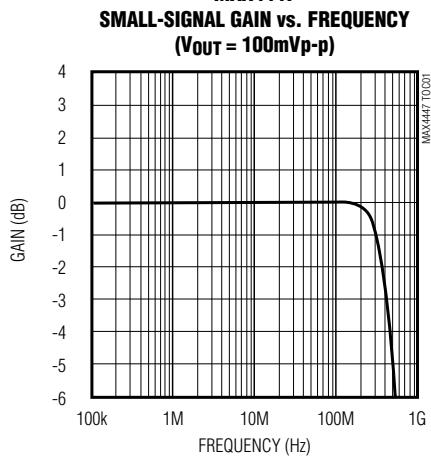
**Note 3:** Includes the current noise contribution through the on-die feedback resistor.

# 6500V/ $\mu$ s, Wideband, High-Output-Current, Single-Ended-to-Differential Line Drivers with Enable

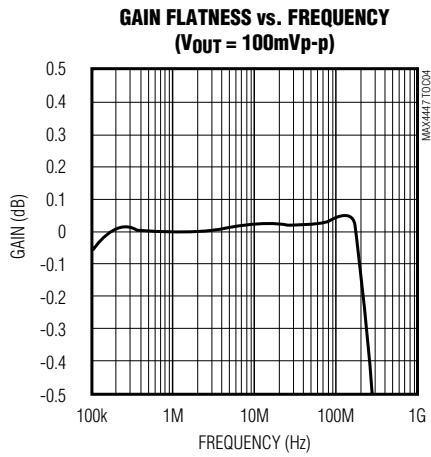
## Typical Operating Characteristics

( $V_{CC} = +5V$ ,  $V_{EE} = -5V$ ,  $V_{EN} = +5V$ ,  $V_{OUT} = V_{OUT+} - V_{OUT-}$ ,  $R_L = 100\Omega$  between  $OUT+$  and  $OUT-$ ,  $A_V = +2V/V$  for MAX4447/MAX4448,  $A_V = +5V/V$  for MAX4449,  $T_A = +25^\circ C$ , unless otherwise noted.)

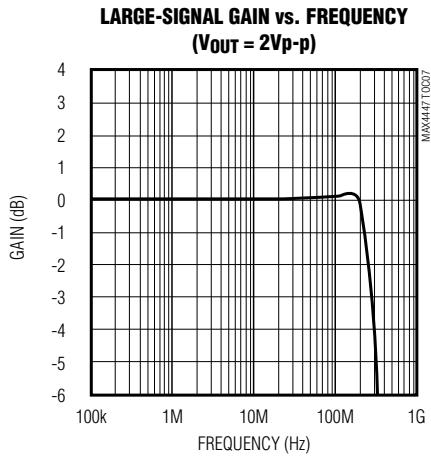
MAX4447



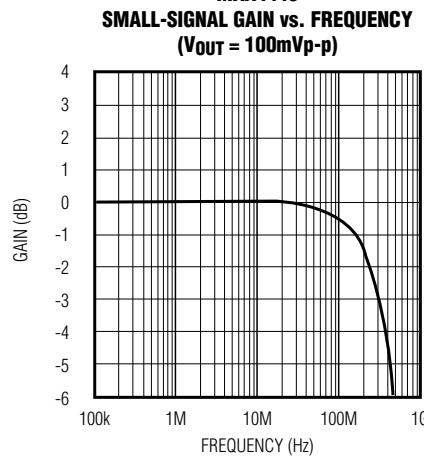
MAX4447



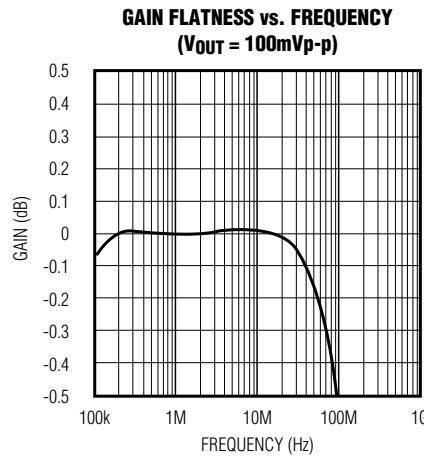
MAX4447



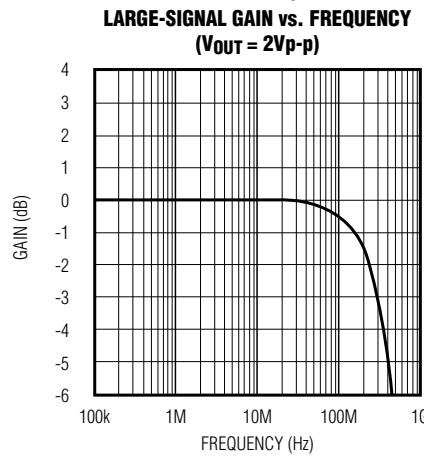
MAX4448



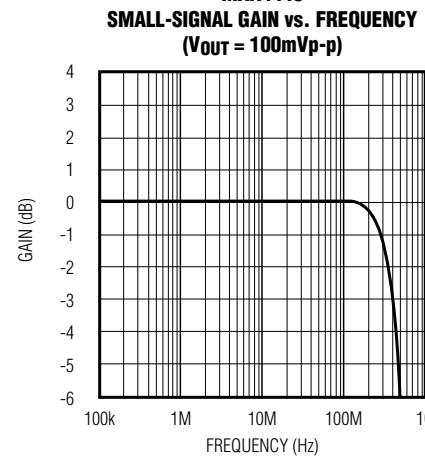
MAX4448



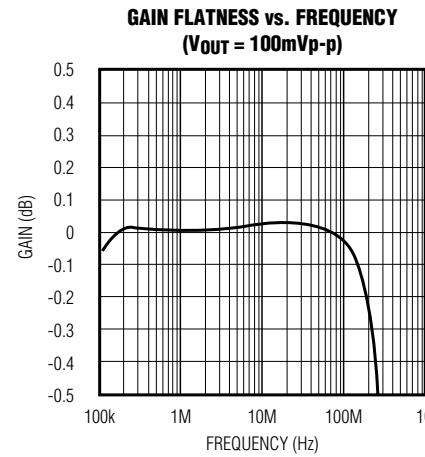
MAX4448



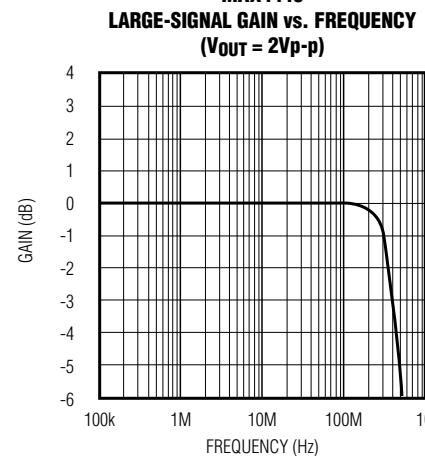
MAX4449



MAX4449



MAX4449



MAX4447/MAX4448/MAX4449

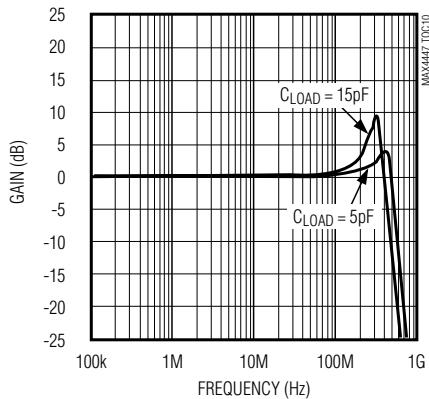
## 6500V/μs, Wideband, High-Output-Current, Single-Ended-to-Differential Line Drivers with Enable

### Typical Operating Characteristics (continued)

( $V_{CC} = +5V$ ,  $V_{EE} = -5V$ ,  $V_{EN} = +5V$ ,  $V_{OUT} = V_{OUT+} - V_{OUT-}$ ,  $R_L = 100\Omega$  between  $OUT+$  and  $OUT-$ ,  $A_V = +2V/V$  for MAX4447/MAX4448,  $A_V = +5V/V$  for MAX4449,  $T_A = +25^\circ C$ , unless otherwise noted.)

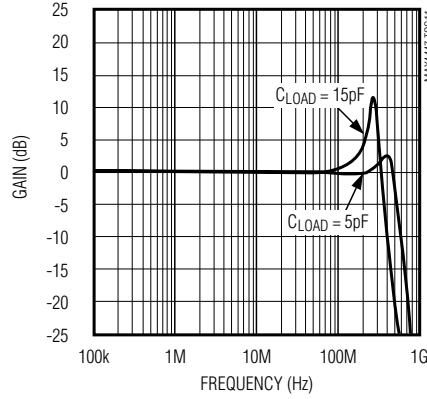
**MAX4447**

**SMALL-SIGNAL GAIN vs. FREQUENCY**  
( $V_{OUT} = 100mVp-p$ )



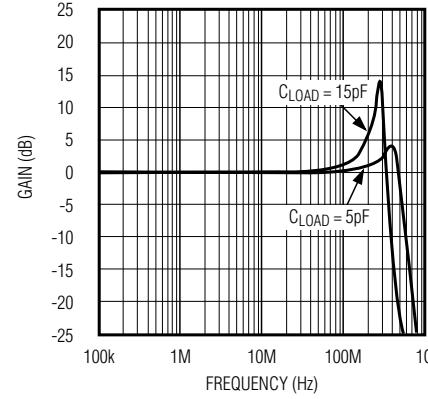
**MAX4448**

**SMALL-SIGNAL GAIN vs. FREQUENCY**  
( $V_{OUT} = 100mVp-p$ )



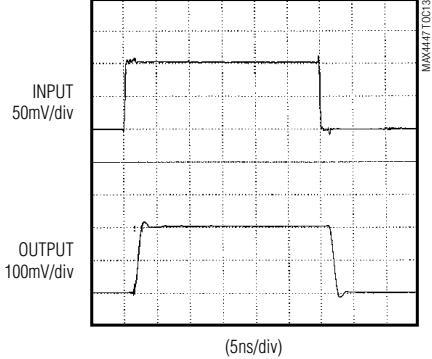
**MAX4449**

**SMALL-SIGNAL GAIN vs. FREQUENCY**  
( $V_{OUT} = 100mVp-p$ )



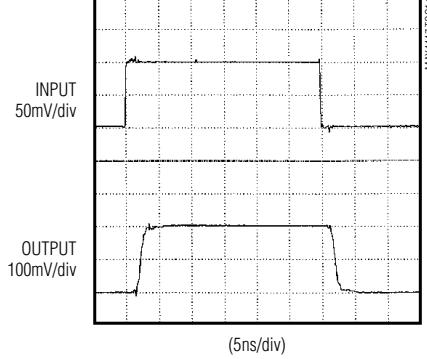
**MAX4447**

**SMALL-SIGNAL PULSE RESPONSE**



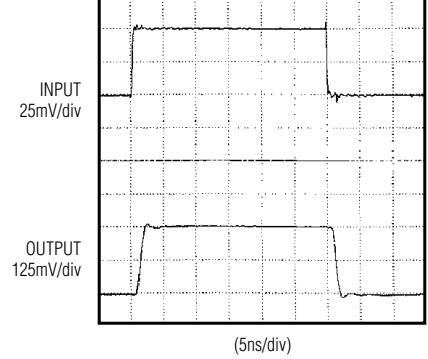
**MAX4448**

**SMALL-SIGNAL PULSE RESPONSE**



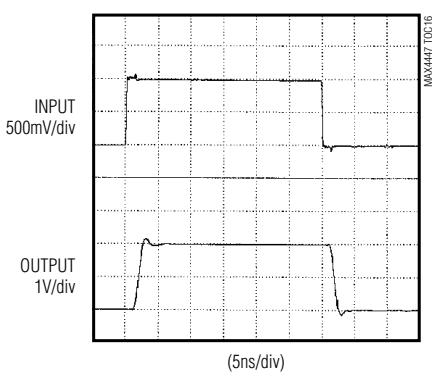
**MAX4449**

**SMALL-SIGNAL PULSE RESPONSE**



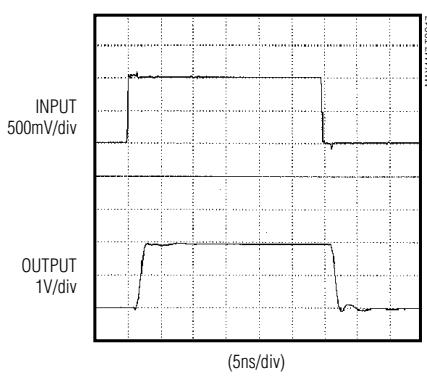
**MAX4447**

**LARGE-SIGNAL PULSE RESPONSE**



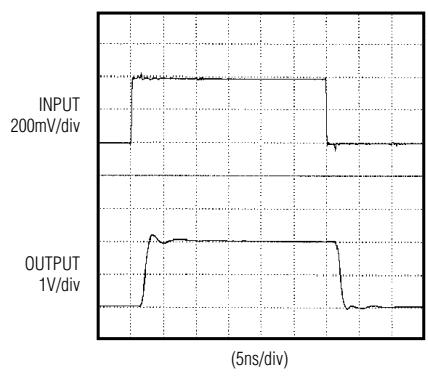
**MAX4448**

**LARGE-SIGNAL PULSE RESPONSE**



**MAX4449**

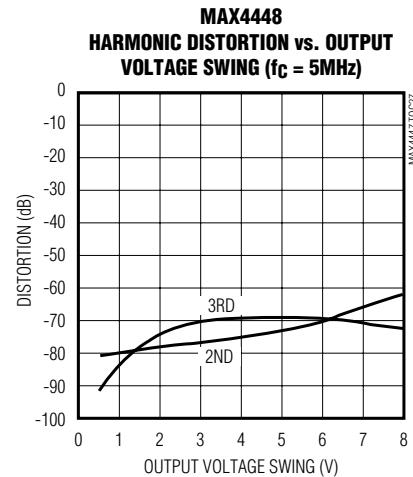
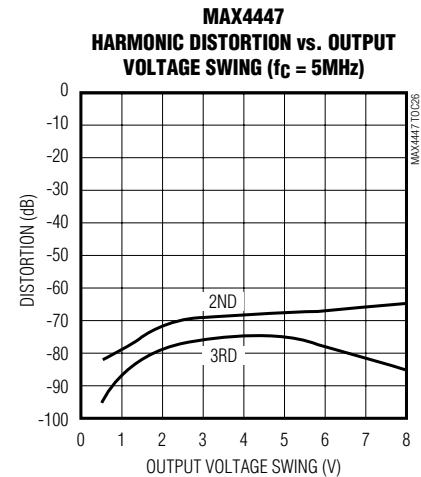
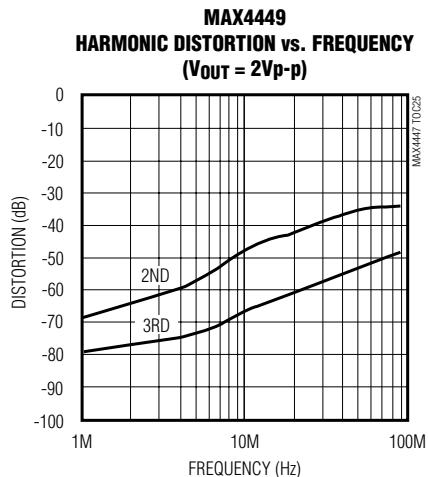
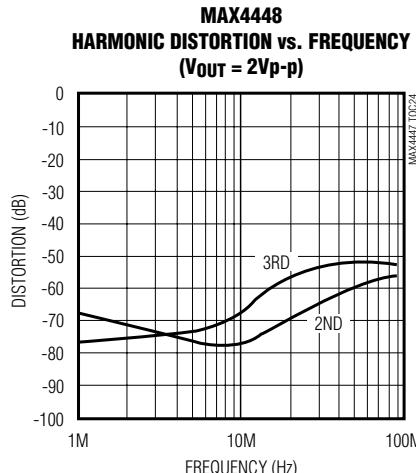
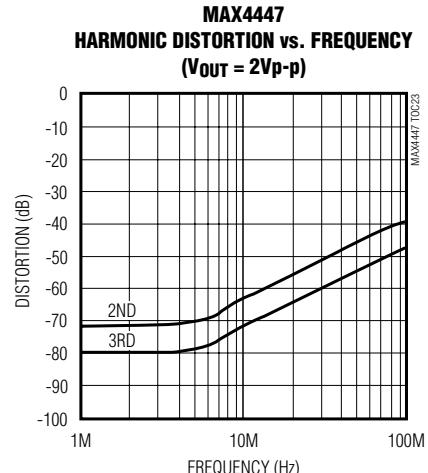
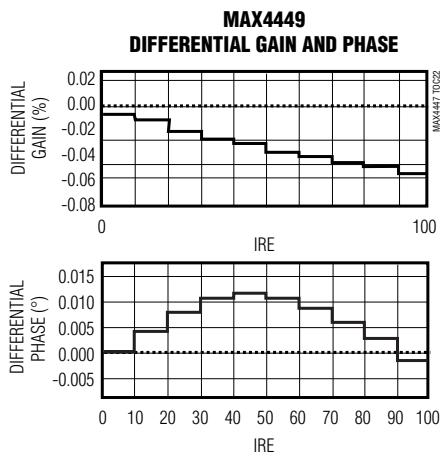
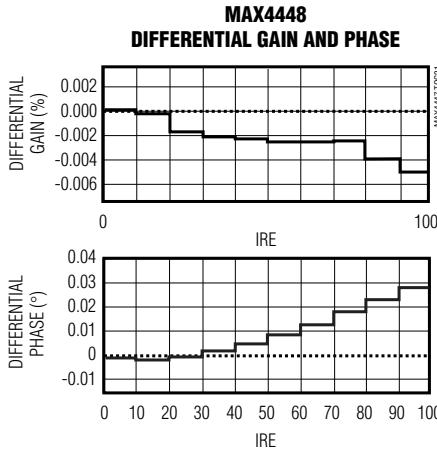
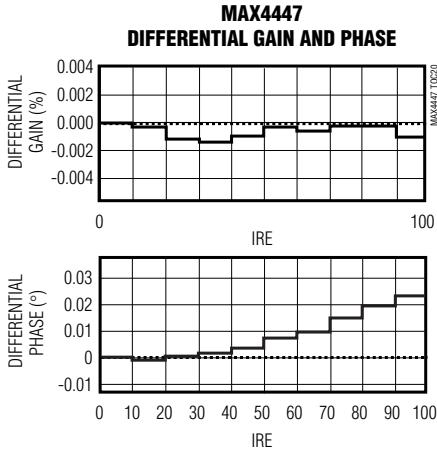
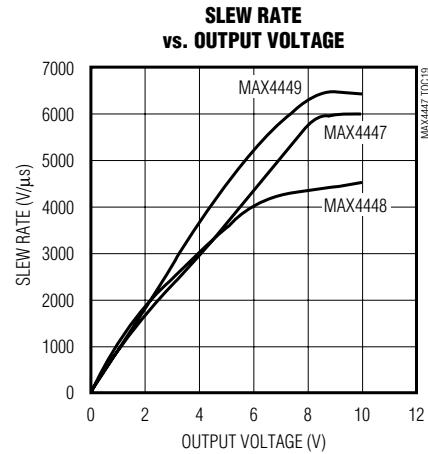
**LARGE-SIGNAL PULSE RESPONSE**



# 6500V/μs, Wideband, High-Output-Current, Single-Ended-to-Differential Line Drivers with Enable

## Typical Operating Characteristics (continued)

( $V_{CC} = +5V$ ,  $V_{EE} = -5V$ ,  $V_{EN} = +5V$ ,  $V_{OUT} = V_{OUT+} - V_{OUT-}$ ,  $R_L = 100\Omega$  between OUT+ and OUT-,  $Av = +2V/V$  for MAX4447/MAX4448,  $Av = +5V/V$  for MAX4449,  $T_A = +25^\circ C$ , unless otherwise noted.)

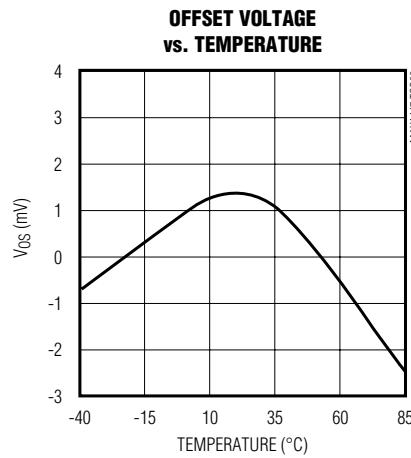
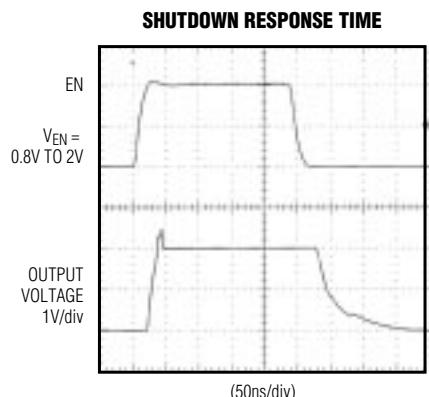
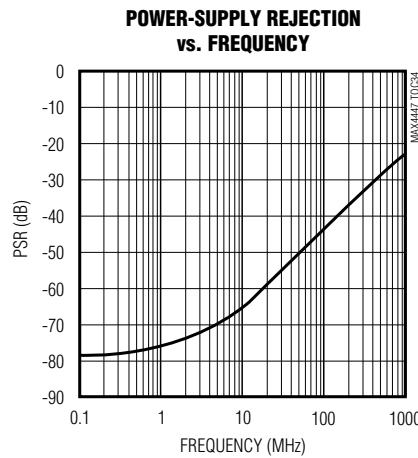
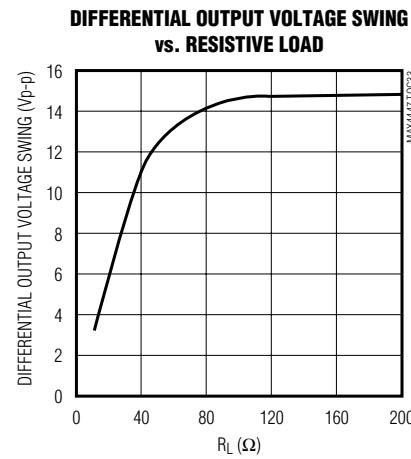
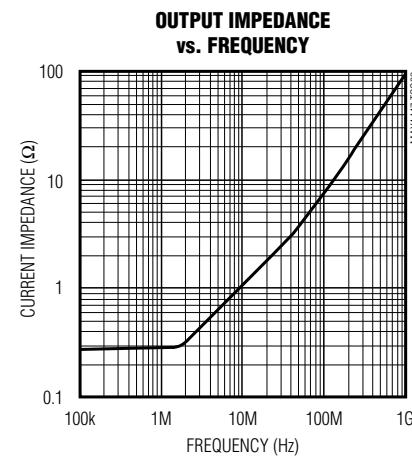
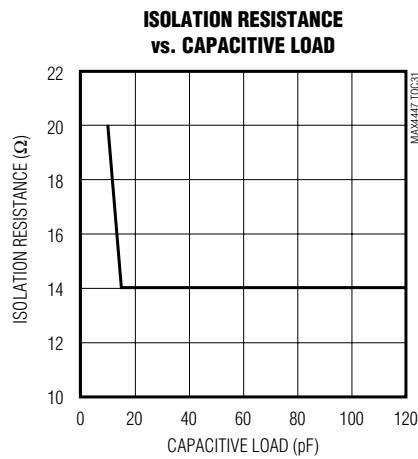
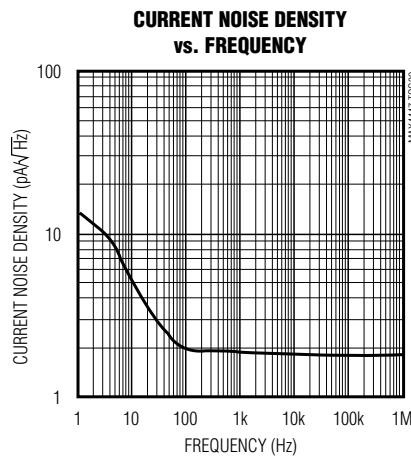
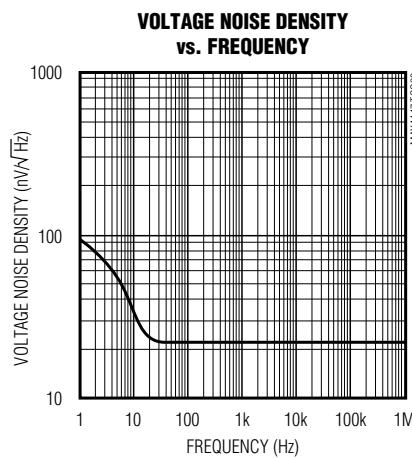
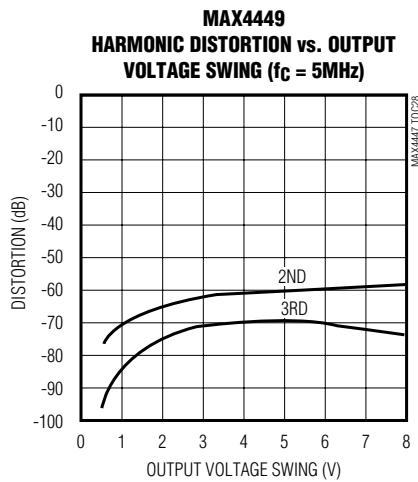


**MAX4447/MAX4448/MAX4449**

## 6500V/μs, Wideband, High-Output-Current, Single-Ended-to-Differential Line Drivers with Enable

### Typical Operating Characteristics (continued)

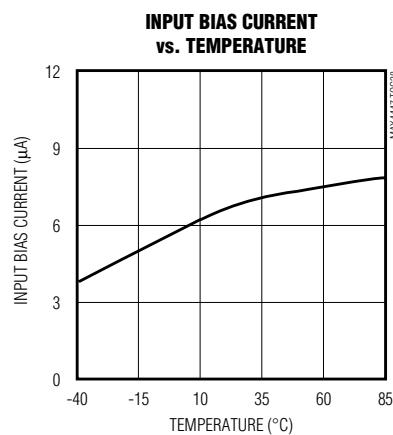
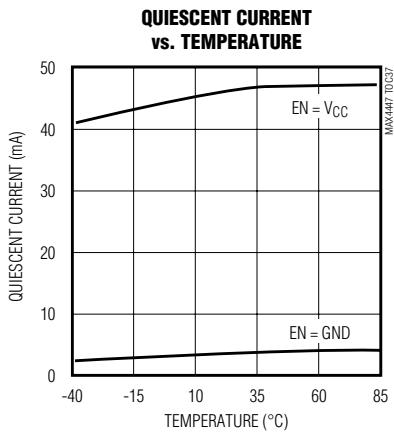
( $V_{CC} = +5V$ ,  $V_{EE} = -5V$ ,  $V_{EN} = +5V$ ,  $V_{OUT} = V_{OUT+} - V_{OUT-}$ ,  $R_L = 100\Omega$  between OUT+ and OUT-,  $A_v = +2V/V$  for MAX4447/MAX4448,  $A_v = +5V/V$  for MAX4449,  $T_A = +25^\circ C$ , unless otherwise noted.)



# 6500V/μs, Wideband, High-Output-Current, Single-Ended-to-Differential Line Drivers with Enable

## Typical Operating Characteristics (continued)

( $V_{CC} = +5V$ ,  $V_{EE} = -5V$ ,  $V_{EN} = +5V$ ,  $V_{OUT} = V_{OUT+} - V_{OUT-}$ ,  $R_L = 100\Omega$  between  $OUT+$  and  $OUT-$ ,  $Av = +2V/V$  for MAX4447/MAX4448,  $Av = +5V/V$  for MAX4449,  $T_A = +25^\circ C$ , unless otherwise noted.)



## Pin Description

PIN		NAME	FUNCTION
MAX4447	MAX4448 MAX4449		
1, 2	1, 2	V <sub>CC</sub>	Positive Power Supply. Bypass with a 0.1μF capacitor to GND.
3, 4, 6	3, 6	N.C.	No Connection. Not internally connected. Connect to GND for best AC performance.
—	4	RG	Gain-Set Resistor. Connect gain-setting resistor from RG to GND.
5	5	IN	Amplifier Noninverting Input
7, 8, 11, 12, 13, 14	7, 8, 11, 12, 13, 14	V <sub>EE</sub>	Negative Power-Supply Input. Bypass with a 0.1μF capacitor to GND.
9	9	EN	Active-High, TTL-Compatible, Enable Input. Connect to V <sub>CC</sub> for normal operation. Connect to GND for low-power operation.
10	10	OUT+	Positive Polarity Output
15	15	OUT-	Negative Polarity Output
16	16	GND	Ground

# 6500V/μs, Wideband, High-Output-Current, Single-Ended-to-Differential Line Drivers with Enable

## Detailed Description

The MAX4447/MAX4448/MAX4449 single-ended-to-differential converters are capable of transmitting high-speed signals such as T1 or xDSL over twisted-pair cable. Excellent gain and phase characteristics, along with low distortion, make these devices suitable for video and RF signal processing and transmission. These converters can be interfaced directly to some of Maxim's wireless products, such as the MAX2450/MAX2451.

The MAX4447/MAX4448/MAX4449 offer wide small-signal bandwidths of 430MHz, 330MHz, and 400MHz, respectively. Internally trimmed resistors minimize gain errors to under 2% over the full output range. Other features include a high slew rate up to 6500V/μs and high output current (130mA), which allow these amplifiers to be used in numerous high-speed communications applications.

## Applications Information

### Grounding and Bypassing

Use high-frequency design techniques when designing the PC board for the MAX4447/MAX4448/MAX4449:

- Use a multilayer board with one layer dedicated as the ground plane.
- Do not wire-wrap or use breadboards, due to high inductance.
- Avoid IC sockets, due to high parasitic capacitance and inductance.
- Bypass supplies with 0.1μF. Use surface-mount capacitors to minimize lead inductance.
- Keep signal lines as short and straight as possible. Do not make 90° turns; round all corners. Do not cross signals if possible.
- Ensure that the ground plane is free from voids.

### Output Short-Circuit Protection

Output short-circuit protection typically limits the current to 140mA when shorted to GND, thereby keeping the power dissipation under the absolute maximum power dissipating rating. However, when shorted to either supply, the short-circuit current can be significantly higher and cause damage to the device.

### Low-Power Enable Mode

The MAX4447/MAX4448/MAX4449 are disabled when EN goes low. This reduces supply current to only 3.2mA and places the outputs into a higher impedance.

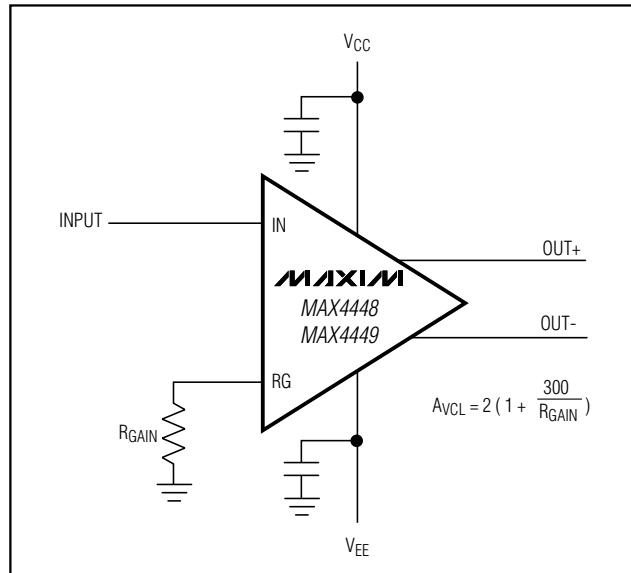


Figure 1. Setting the Amplifier Gain

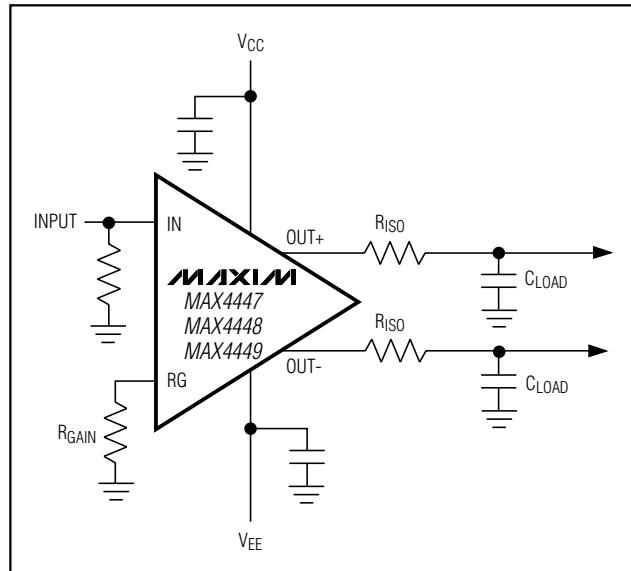


Figure 2. Using an Isolation Resistor for High Capacitive Loads

# 6500V/μs, Wideband, High-Output-Current, Single-Ended-to-Differential Line Drivers with Enable

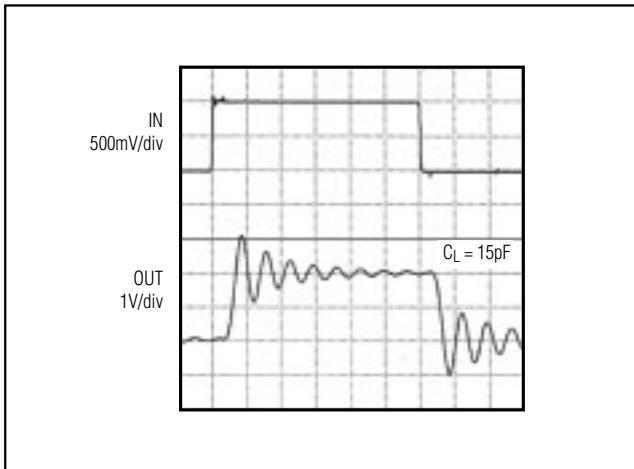


Figure 3. Capacitive-Loaded Output Step Response Without Isolation Resistor

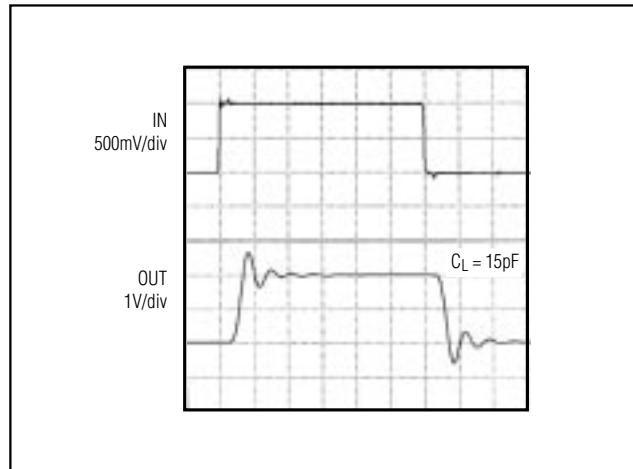


Figure 4. Capacitive-Loaded Output Step Response with 14Ω Isolation Resistor

## Setting Gain

The MAX4448/MAX4449 are stable with minimum gain of +2V/V and +5V/V, respectively. An external resistor, RGAIN, connected between RG and GND sets the gain of these devices. Calculate the gain as follows:

$$\text{Gain} = 2 (1 + 300 / \text{RGAIN})$$

RGAIN for the MAX4449 must be  $\leq 200\Omega$ .

## Driving Capacitive Loads

The MAX4447/MAX4448/MAX4449 are designed to drive capacitive loads. However, excessive capacitive loads may cause ringing or instability at the output as phase margin is reduced. Adding a small series isolation resistor at the output helps reduce the ringing but slightly increases gain error.

## Twisted-Pair Line Driver

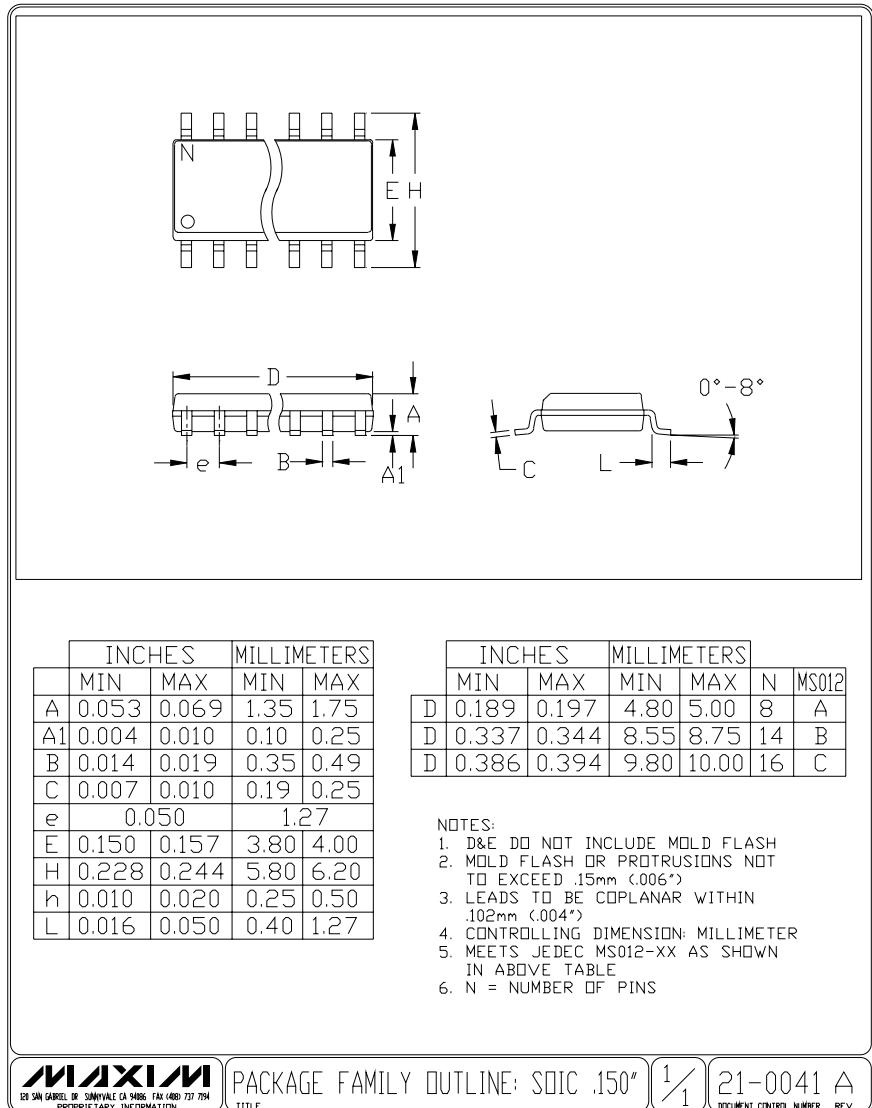
The MAX4447/MAX4448/MAX4449 are well-suited to drive twisted-pair cables. The 24AWG telephone wire widely used produces losses at the higher frequencies. Compensate for these losses by increasing the gain slightly.

## Chip Information

TRANSISTOR COUNT: 291

# 6500V/ $\mu$ s, Wideband, High-Output-Current, Single-Ended-to-Differential Line Drivers with Enable

## Package Information



Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

12 **Maxim Integrated Products, 120 San Gabriel Drive, Sunnyvale, CA 94086 408-737-7600**