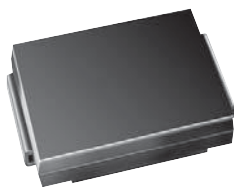


Surface Mount Glass Passivated Rectifier


DO-214AB (SMC)

FEATURES

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- Low forward voltage drop
- Low leakage current
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT

TYPICAL APPLICATIONS

For use in general purpose rectification of power supplies, inverters, converters and freewheeling diodes for consumer, automotive and telecommunication.

MECHANICAL DATA

Case: DO-214AB (SMC)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-E3 - RoHS compliant, commercial grade

Base P/NHE3 - RoHS compliant, AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	5.0 A
V_{RRM}	50 V to 1000 V
I_{FSM}	100 A
I_R	10 μ A
V_F	1.15 V
T_J max.	150 °C

MAXIMUM RATINGS ($T_A = 25\text{ °C}$ unless otherwise noted)									
PARAMETER	SYMBOL	S5A	S5B	S5D	S5G	S5J	S5K	S5M	UNIT
Device marking code		5A	5B	5D	5G	5J	5K	5M	
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current at $T_L = 75\text{ °C}$	$I_{F(AV)}$	5.0							A
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I_{FSM}	100							A
Operating junction and storage temperature range	T_J, T_{STG}	- 55 to + 150							°C

**ELECTRICAL CHARACTERISTICS** ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	TEST CONDITIONS		SYMBOL	S5A	S5B	S5D	S5G	S5J	S5K	S5M	UNIT
Maximum instantaneous forward voltage	5.0 A		V _F	1.15							V
Maximum DC reverse current at rated DC blocking voltage		T _A = 25 °C	I _R	10							μA
		T _A = 125 °C		250							
Typical reverse recovery time	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A		t _{rr}	2.5							μs
Typical junction capacitance	4.0 V, 1 MHz		C _J	40							pF

THERMAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	S5A	S5B	S5D	S5G	S5J	S5K	S5M	UNIT
Typical thermal resistance ⁽¹⁾	$R_{\theta JL}$	10							$^{\circ}\text{C/W}$

Note

⁽¹⁾ Thermal resistance from junction to lead mounted on PCB with 0.3" x 0.3" (8.0 mm x 8.0 mm) copper pad area

ORDERING INFORMATION (Example)

PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
S5J-E3/57T	0.211	57T	850	7" diameter plastic tape and reel
S5J-E3/9AT	0.211	9AT	3500	13" diameter plastic tape and reel
S5JHE3/57T ⁽¹⁾	0.211	57T	850	7" diameter plastic tape and reel
S5JHE3/9AT ⁽¹⁾	0.211	9AT	3500	13" diameter plastic tape and reel

Note

⁽¹⁾ AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES

($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)

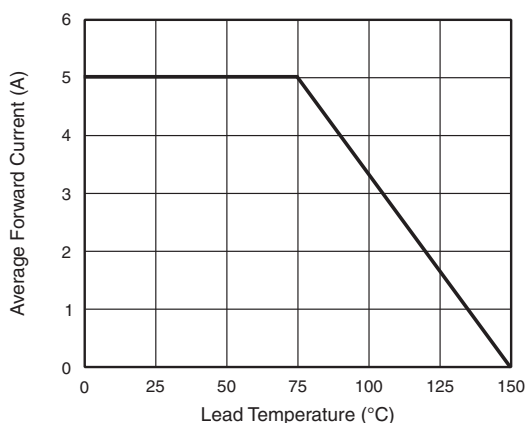


Fig. 1 - Forward Current Derating Curve

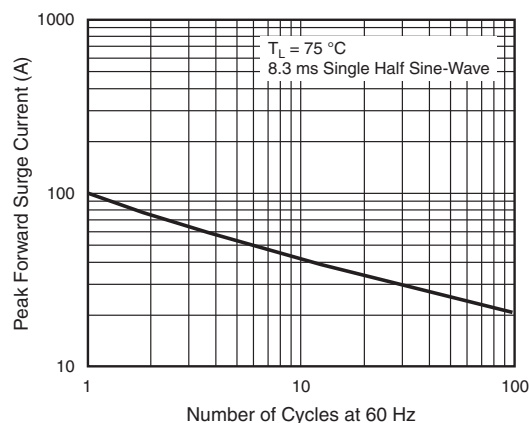


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

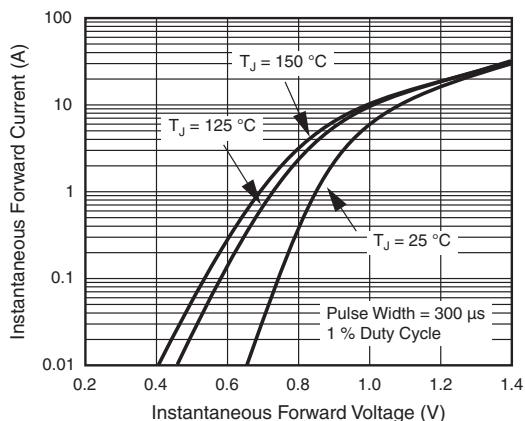


Fig. 3 - Typical Instantaneous Forward Characteristics

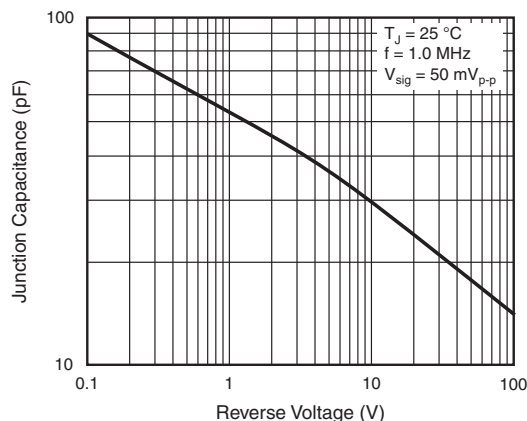


Fig. 5 - Typical Junction Capacitance

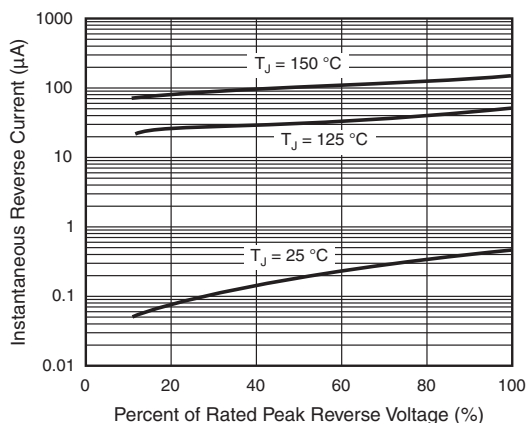
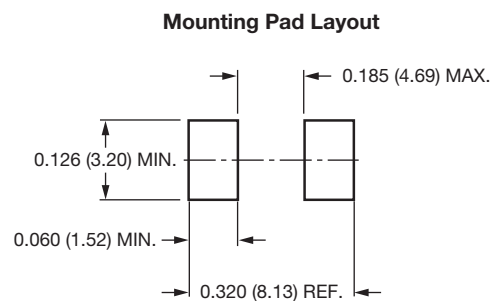
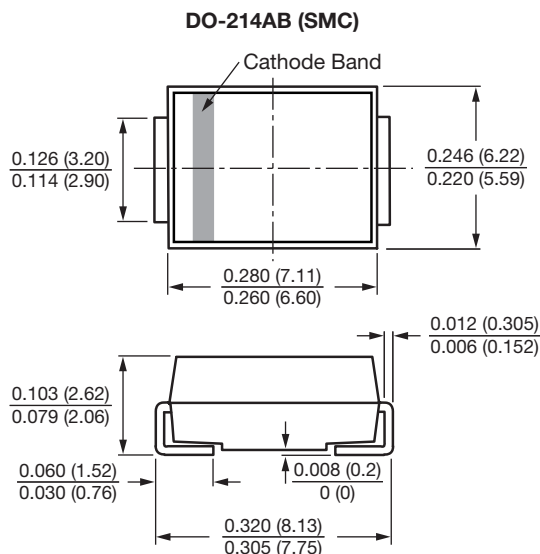


Fig. 4 - Typical Reverse Characteristics

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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