

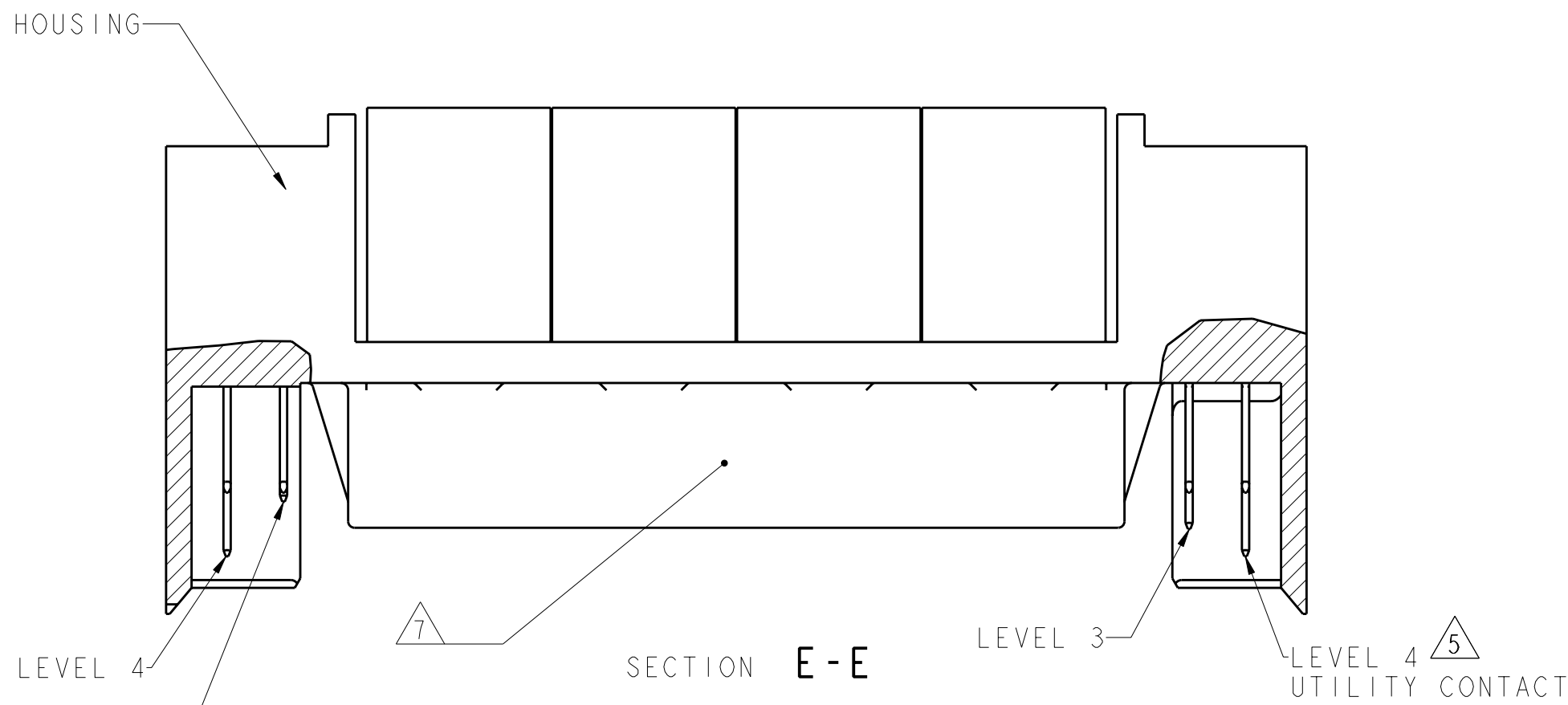
LOC		DIST		REVISIONS						
GP	00	P	LTR	DESCRIPTION			DATE	DWN	APVD	
			B	REVISED PER ECO-10-012356			14DEC2010	CJV	EJB	

D

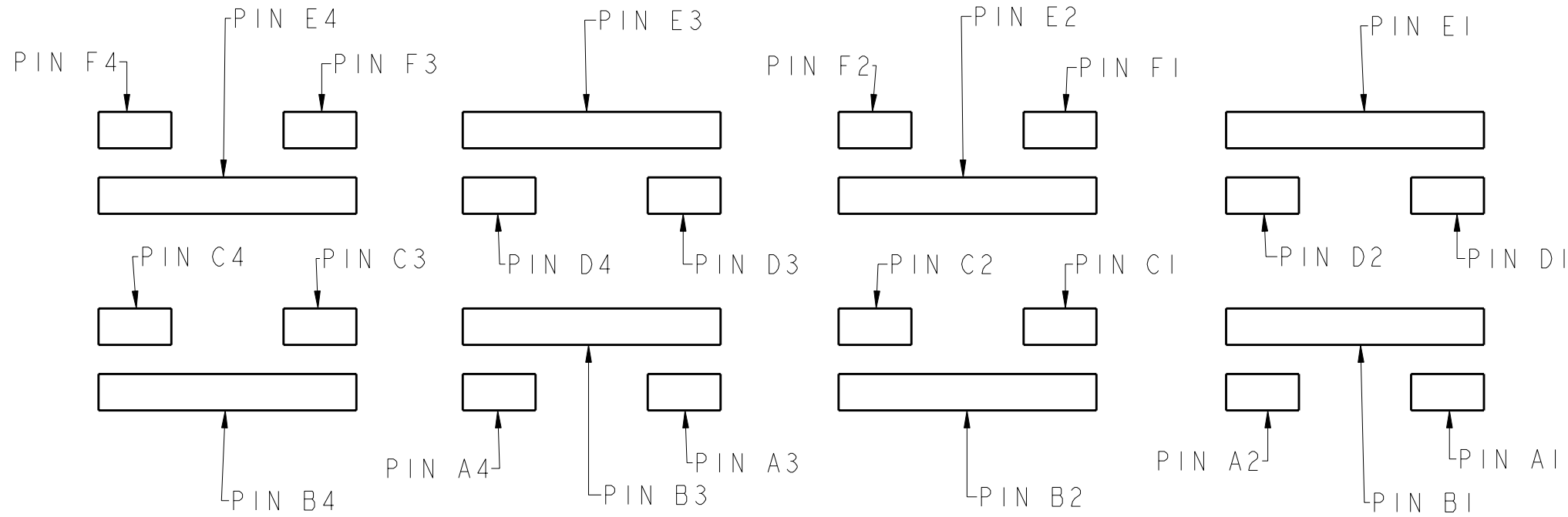
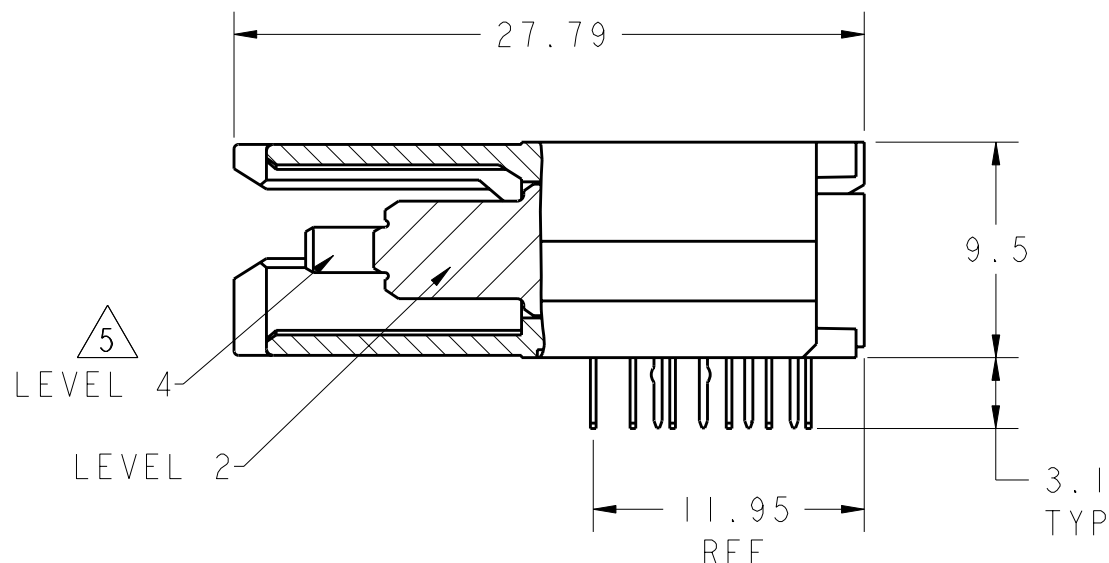
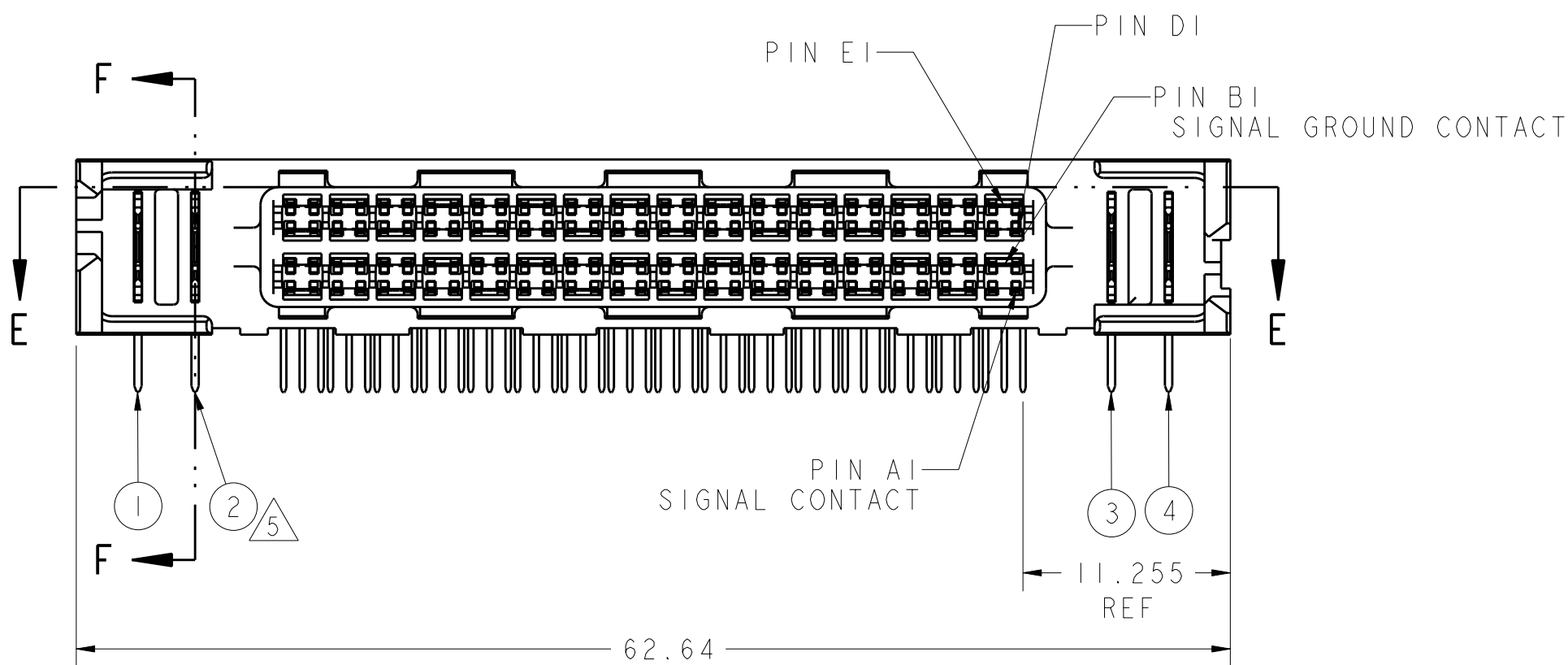
C

B

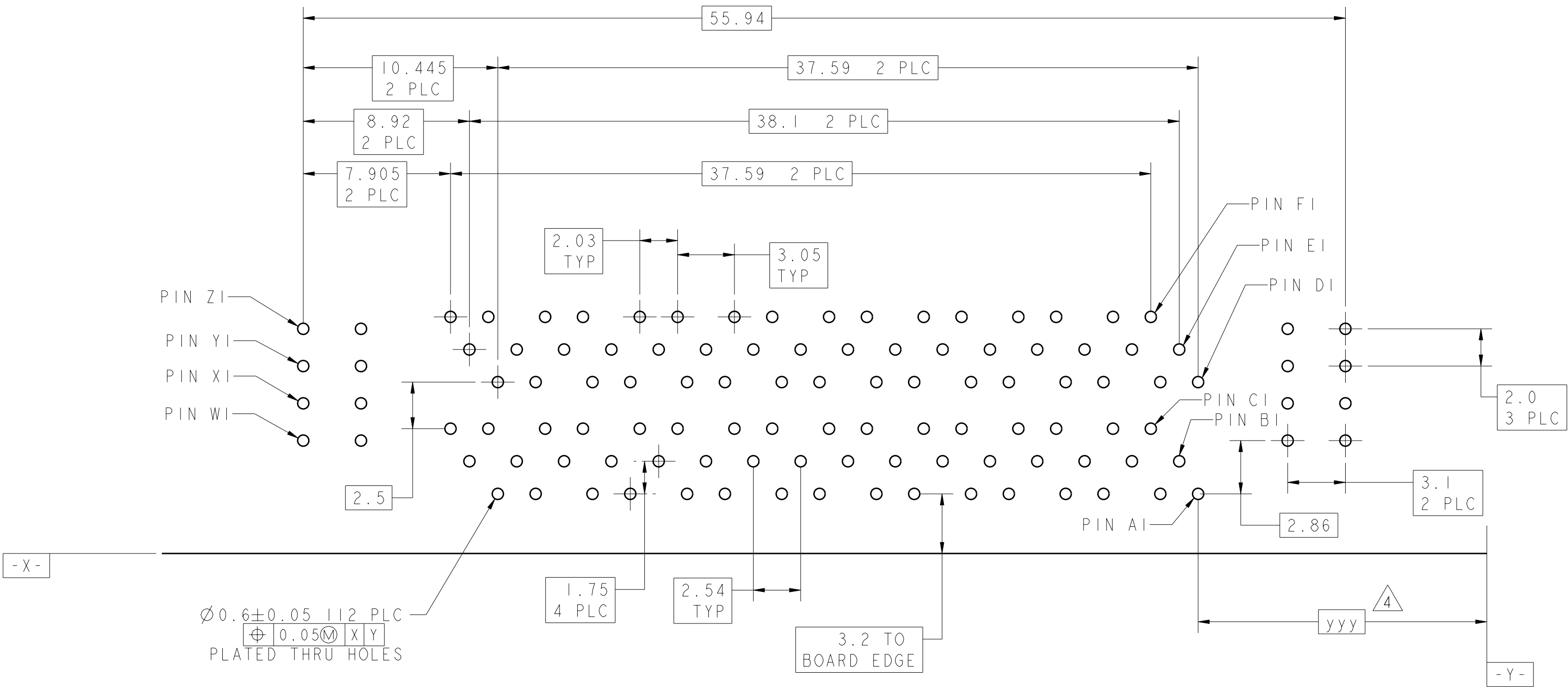
A



- 1 HOUSINGS: POLYESTER, UL 94V-0 RATED, NATURAL
SIGNAL AND SIGNAL GROUND CONTACTS: COPPER ALLOY
UTILITY CONTACTS: PHOSPHOR BRONZE
- 2 UTILITY CONTACTS: 0.76µm MIN GOLD IN CONTACT AREA,
2.54µm MIN TIN-LEAD ON PCB TAILS, OVER 1.27µm MIN NICKEL OVER ALL.
SIGNAL AND SIGNAL GROUND CONTACTS: 0.76µm MIN GOLD IN CONTACT AREA,
2.54µm MIN TIN-LEAD ON PCB TAILS, OVER 1.27µm MIN NICKEL OVER ALL.
3. ROWS A, C, D, AND F ARE SIGNAL CONTACTS. ROWS B AND E ARE SIGNAL GROUND CONTACTS.
- 4 DIMENSIONS PER CUSTOMER BOARD LAYOUT.
- 5 SEE UTILITY CONTACT SEQUENCE TABLE FOR LOCATION AND LEVEL/LENGTH OF UTILITY CONTACTS
FOR EACH PRODUCT PART NUMBER. UTILITY LEVEL 1 CAN BE USED FOR SENSING. UTILITY LEVELS
2, 3, AND 4 CAN BE USED FOR POWER, GROUND, OR ESD. SEQUENCING SHOWN IN SECTION E-E
SHOWS THREE LEVELS FOR COMPARISON. UTILITY LEVEL 2 EQUALS THE SIGNAL GROUND CONTACT LEVEL.
SIGNAL LEVEL IS BETWEEN UTILITY LEVELS 1 AND 2.
6. BLOCK DIAGRAM AND CONTACT IDENTIFICATION APPLY TO COPLANAR NON-INVERTED APPLICATION ONLY.
CONTACT IDENTIFICATION REVERSES FOR INVERTED APPLICATIONS, I.E COPLANAR OR MID-BOARD INVERTED.
- 7 COMPANY LOGO IN APPROXIMATE AREA SHOWN.



BLOCK DIAGRAM
MATING FACE CONFIGURATION



RECOMMENDED PC BOARD FOOTPRINT
COMPONENT SIDE SHOWN
SCALE 5:1

UTILITY CONTACT SEQUENCE TABLE				
5	UTILITY CONTACT LEVEL 1, 2, 3, OR 4			
2	2	3	3	6367584-4
3	2	2	3	6367584-3
4	1	3	4	6367584-2
4	3	3	4	6367584-1
1	2	3	4	PART NUMBER
UTILITY CONTACT LOCATION				

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DIMENSIONS: mm		CHK P. RECCE 14DEC2010	NAME	
TOLERANCES UNLESS OTHERWISE SPECIFIED: 9 PLC ±.3 2 PLC ±0.25 3 PLC ±.25 4 PLC ±.25 ANGLES ±.25		APVD P. RECCE 14DEC2010	PRODUCT SPEC 108-1985 APPLICATION SPEC 114-13068	
MATERIAL		FINISH	WEIGHT	SIZE
CUSTOMER DRAWING				00779
				C=6367584
				RESTRICTED TO
				SCALE 3:1 SHEET 1 OF 1 REV B