

RB095B-40

General rectification
(Common cathode dual chip)

- 1) Power mold (CPD3)
- 2) High reliability
- 3) Low V_F & Low I_R

[illegible]

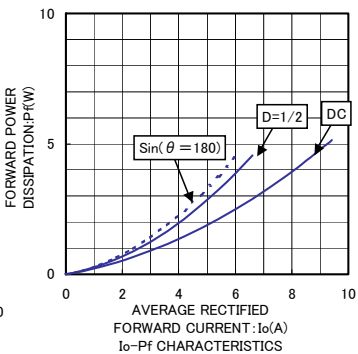
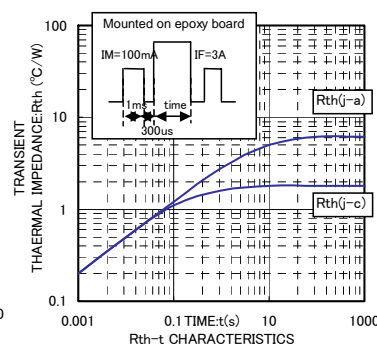
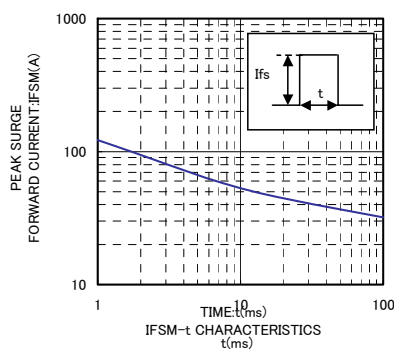
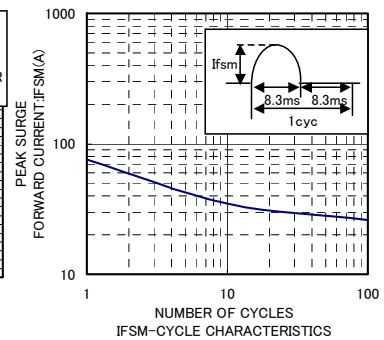
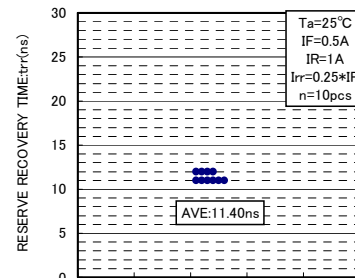
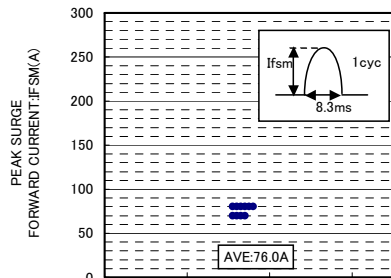
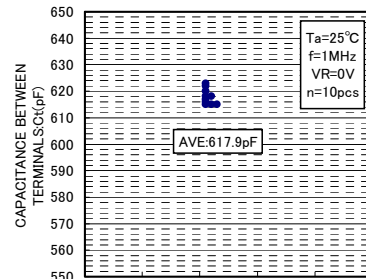
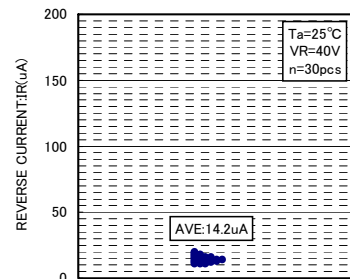
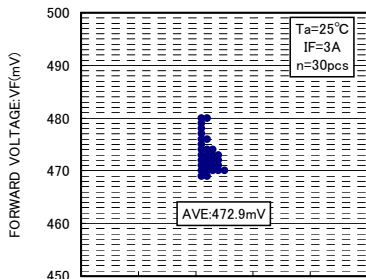
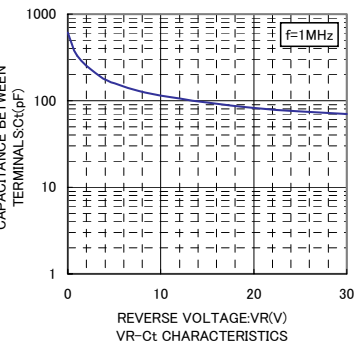
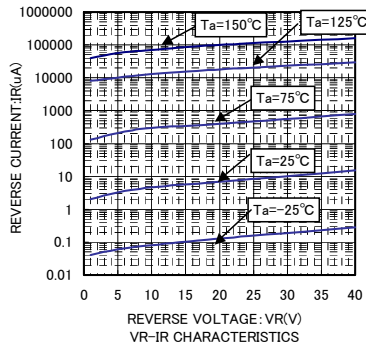
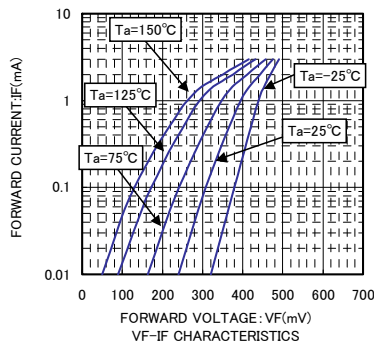
Technical drawing of the top view of a 4-pin connector. The drawing shows a rectangular body with four pins on the left. Dimensions include a total width of 16.0 ± 0.2, a pin pitch of 4.0 ± 0.1, and a body width of 8.0 ± 0.1. Pin diameters are specified as φ1.55 ± 0.1 and φ3.0 ± 0.1. Mounting hole diameters are φ0.4 ± 0.1 and φ0.3 ± 0.1. A detail view of the mounting hole is shown on the right.

Parameter	Symbol	Limits	Unit
Reverse voltage (repetitive peak)	V _{RM}	45	V
Reverse voltage (DC)	V _R	40	V
Average rectified forward current *	I _O	6	A
Forward current surge peak (60Hz • 1cyc.) *	I _{FSM}	45	A
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	−40 to +150	°C

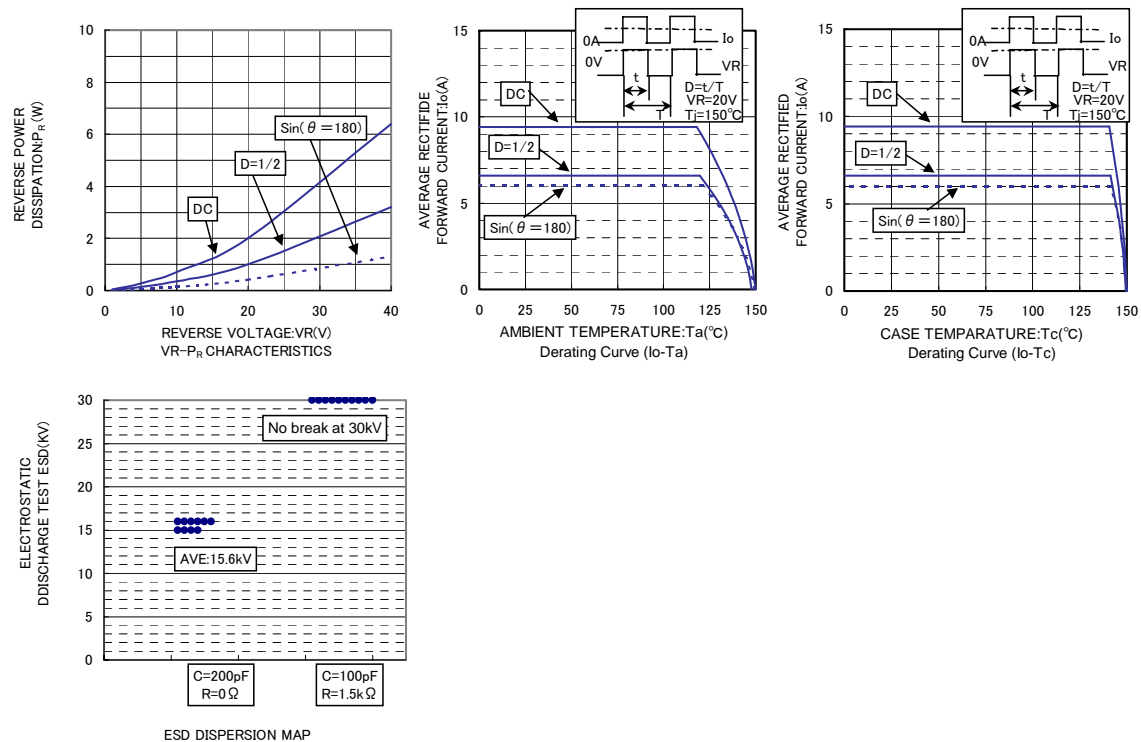
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V_F	—	—	0.55	V	$I_F=3.0A$
Reverse current	I_R	—	—	0.1	mA	$V_R=40V$
Thermal impedance	θ_{JC}	—	—	6.0	°C/W	junction to case

Diodes

●Electrical characteristic curves



Diodes



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