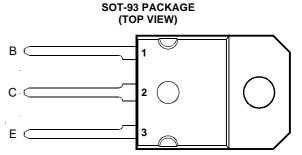
- Designed for Complementary Use with the BD250 Series
- 125 W at 25°C Case Temperature
- 25 A Continuous Collector Current
- 40 A Peak Collector Current
- Customer-Specified Selections Available



Pin 2 is in electrical contact with the mounting base.

MDTRAA

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING			VALUE	UNIT
	BD249		55	
Collector-emitter voltage (R_{BE} = 100 Ω)	BD249A	V	70	V
	BD249B	V _{CER}	90	V
	BD249C		115	
	BD249		45	
Collector-emitter voltage (I _C = 30 mA)	BD249A	V	60	V
	BD249B	V _{CEO}	80	
	BD249C		100	
Emitter-base voltage	V _{EBO}	5	V	
Continuous collector current			25	Α
Peak collector current (see Note 1)			40	Α
Continuous base current			5	Α
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			125	W
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)			3	W
Unclamped inductive load energy (see Note 4)			90	mJ
Operating junction temperature range	Tj	-65 to +150	°C	
Storage temperature range	T _{stg}	-65 to +150	°C	
Lead temperature 3.2 mm from case for 10 seconds	T _L	250	°C	

NOTES: 1. This value applies for $t_p \le 0.3$ ms, duty cycle $\le 10\%$.

- 2. Derate linearly to 150°C case temperature at the rate of 1 W/°C.
- 3. Derate linearly to 150°C free air temperature at the rate of 24 mW/°C.
- 4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH, $I_{B(on)}$ = 0.4 A, R_{BE} = 100 Ω , $V_{BE(off)}$ = 0, R_{S} = 0.1 Ω , V_{CC} = 20 V.



BD249, BD249A, BD249B, BD249C NPN SILICON POWER TRANSISTORS

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electrical characteristics at 25°C case temperature

PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT	
V _{(BR)CEO}	Collector-emitter breakdown voltage	1 - 20 mA	1 -0	BD249 BD249A	45 60			V	
		breakdown voltage	$I_C = 30 \text{ mA}$ $I_B = 0$ BD249B (see Note 5) BD249C		80 100			V	
	V _{CE}	V _{CE} = 55 V	$V_{BE} = 0$	BD249			0.7		
1	Collector-emitter	$V_{CE} = 70 V$	$V_{BE} = 0$	BD249A			0.7	mA	
ICES	cut-off current	$V_{CE} = 90 V$	$V_{BE} = 0$	BD249B			0.7		
		V _{CE} = 115 V	$V_{BE} = 0$	BD249C			0.7		
1	Collector cut-off	V _{CE} = 30 V	I _B = 0	BD249/249A			1	mA	
I _{CEO}	current	$V_{CE} = 60 \text{ V}$	$I_B = 0$	BD249B/249C			1	ША	
I _{EBO}	Emitter cut-off current	V _{EB} = 5 V	I _C = 0				1	mA	
	Forward current transfer ratio	V _{CE} = 4 V	I _C = 1.5 A	$I_C = 1.5 A$	25				
h _{FE}		transfer ratio $V_{CE} = 4$	-	$I_C = 15 A$	(see Notes 5 and 6)	10			
		$V_{CE} = 4 V$	$I_C = 25 A$		5				
V _{CE(sat)}	Collector-emitter	$I_B = 1.5 A$	$I_C = 15 A$	(see Notes 5 and 6)			1.8	V	
*CE(sat)	saturation voltage	$I_B = 5 A$	$I_C = 25 A$				4	•	
V _{BE}	Base-emitter	$V_{CE} = 4 V$	$I_C = 15 A$	(see Notes 5 and 6)			2	V	
*BE	voltage	$V_{CE} = 4 V$	$I_C = 25 A$				4	•	
h _{fe}	Small signal forward current transfer ratio	V _{CE} = 10 V	I _C = 1 A	f = 1 kHz	25				
h _{fe}	Small signal forward current transfer ratio	V _{CE} = 10 V	I _C = 1 A	f = 1 MHz	3				

NOTES: 5. These parameters must be measured using pulse techniques, t_p = 300 μs , duty cycle \leq 2%.

thermal characteristics

	PARAMETER			MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			1	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance			42	°C/W

resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t _{on}	Turn-on time	I _C = 5 A	$I_{B(on)} = 0.5 A$	$I_{B(off)} = -0.5 A$		0.3		μs
t _{off}	Turn-off time	$V_{BE(off)} = -5 V$	$R_L = 5 \Omega$	$t_p = 20 \ \mu s, \ dc \le 2\%$		0.9		μs

[†] Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

^{6.} These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

TYPICAL CHARACTERISTICS

TYPICAL DC CURRENT VS COLLECTOR CURRENT $T_{CS635AD}$ $T_{C} = 25^{\circ}C$ $T_{C} = 25^{\circ}C$ $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2% $T_{D} = 300 \,\mu s$, duty cycle < 2%

Figure 1.

COLLECTOR-EMITTER SATURATION VOLTAGE

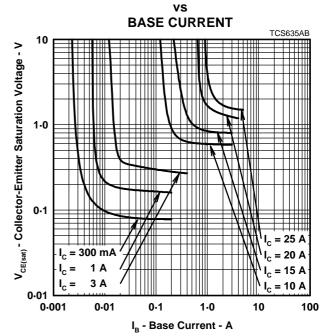
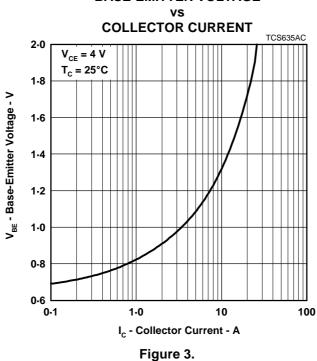


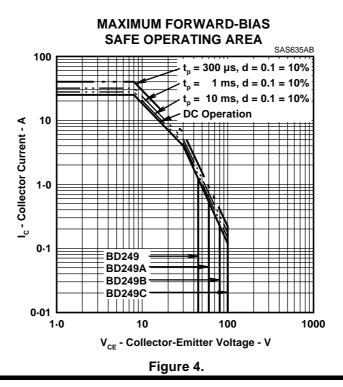
Figure 2.

BASE-EMITTER VOLTAGE



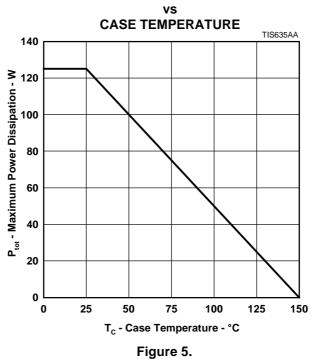
Power

MAXIMUM SAFE OPERATING REGIONS



THERMAL INFORMATION

MAXIMUM POWER DISSIPATION

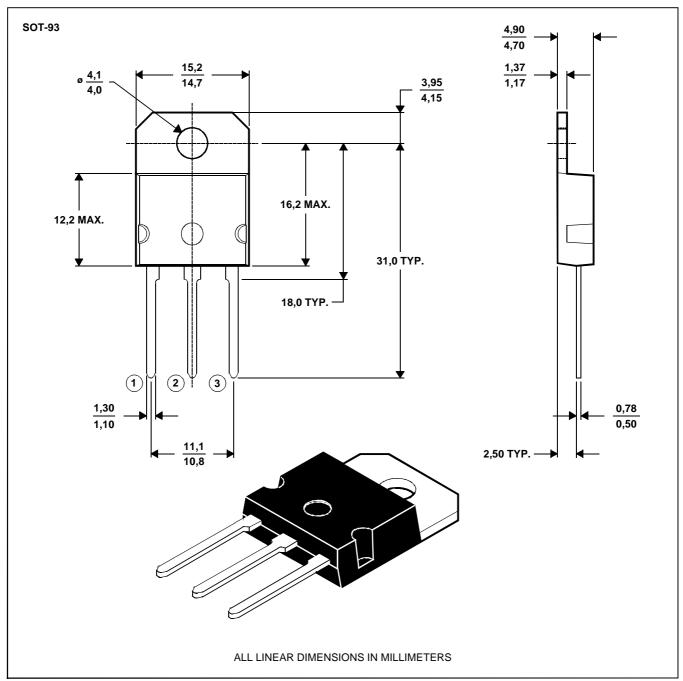


MECHANICAL DATA

SOT-93

3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTE A: The centre pin is in electrical contact with the mounting tab.

MDXXAW



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JUNE 1973 - REVISED MARCH 1997

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