

Mixed Signal Division

DK86064-1

February 2004
Version 1.0

FME/MS/DAC80/FL_2/5084

Dual 14-bit 1GSa/s DAC Development Kit

Features

- Modular development kit for MB86064
 - General Purpose Motherboard
 - PC USB Interface lead
 - Plug-on DAC Module
 - User Manual
 - SMA data adaptors (optional)
- PC control software supplied on CD
 - Win98/2000 compatible
- Provides easy access to on-chip waveform memories to perform initial performance tests, avoiding need for high performance data generating equipment
- Evaluate multiple devices by swapping DAC modules (optional)

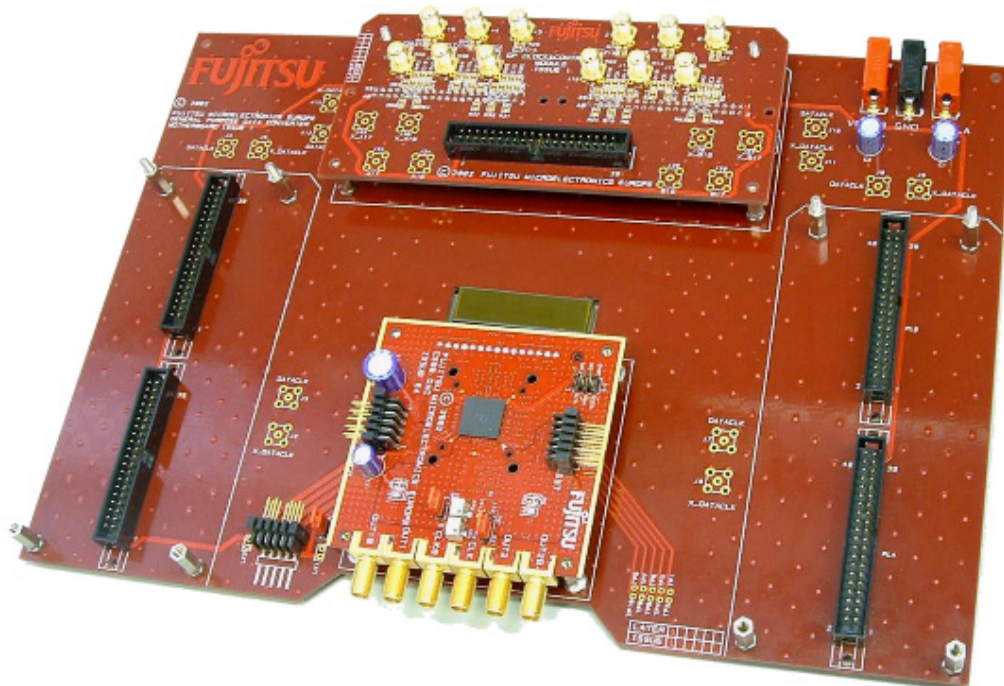
Description

Fujitsu's DK86064-1 Dual 14-bit 1GSa/s DAC Development Kit provides a simple and effective means of evaluating the MB86064 Digital to Analog Converter (DAC).

A user manual provides a step-by-step guide from configuring the board, connecting test equipment, through to evaluating the MB86064's performance. Schematics, PCB overlays and connector pin-outs are included. The evaluation platform requires two DC power supplies, 1.8V & 3.3V, each capable of providing 1A.

A PC USB interface lead and control software are provided to configure, control and download test vectors to the device.

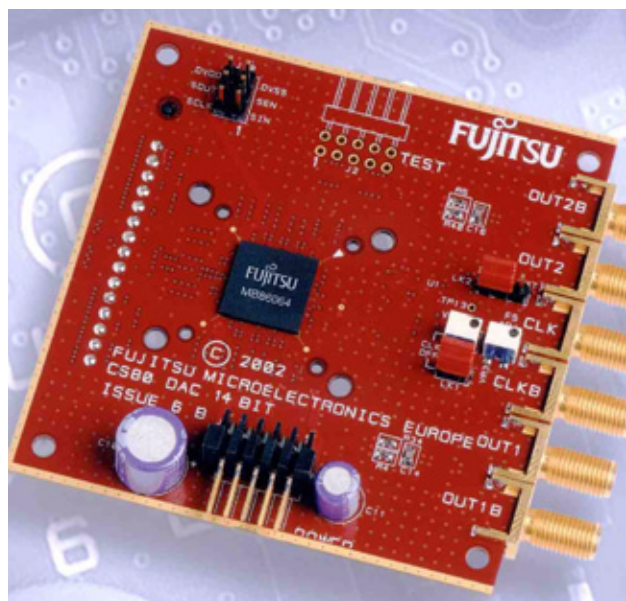
DK86064-1 Evaluation Platform



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Essential Equipment

Apart from the power supplies, equipment vital to conducting an evaluation of the MB86064 is a high quality RF clock and spectrum analyser. The phase & spurious performance of the clock should be such as to not limit the DAC performance (e.g. HP8664A). However, performance of even the best spectrum analysers available is inferior to that of the converter. To overcome this, filtering techniques and careful attention to analyser settings, e.g. RF Attenuation, is essential during the course of the evaluation.



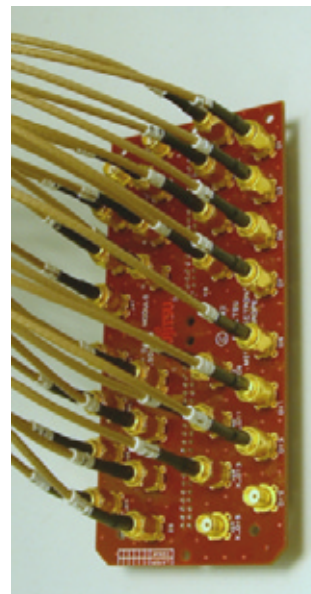
Driving the DAC

As with any DAC evaluation, an appropriate stimulus [test vector] is required. Unfortunately at data rates above 300MSa/s this requires digital pattern generation capabilities beyond most standard test equipment. The DK86064-1 Development Kit has been designed to help overcome this difficulty in a number of ways. Initially, simple unmodulated or pseudo-modulated single and multi-tone tests can be conducted using waveforms downloaded to the device's on-chip waveform memories.

Test waveforms are easily loaded into the waveform memories, via a USB interface, using the PC software supplied with the main Motherboard. Even if high speed digital pattern

generating equipment is available, initial testing using the waveform memories serves as a useful setup check.

Pattern generators can be connected to the General Purpose Motherboard using either the on-board 2-row 0.1" data headers, or the optional SMA Adaptors. When using the 0.1" data headers it is assumed that a custom wiring harness will be required. This would be made according to the connector type and pinout of the generator's output. The optional SMA



Adaptors provide a convenient conversion from SMA to the Motherboard's 0.1" headers. This simplifies the simultaneous removal of 28 SMAs (14-bit differential LVDS) when required. One advantage of this is the ability to swap the data generator easily between DAC data ports if insufficient channels are available to drive both ports simultaneously.

Rather than using general purpose test equipment, customers may wish to use parts of the development kit to construct an evaluation platform more representative of their end application. This might, for example, involve an FPGA to implement a variety of pre-processing and/or waveform generation. At the simplest level, a setup similar to that described for the digital pattern generator could be used, where a custom wiring harness interfaces a standard or existing FPGA evaluation platform to the Motherboard. Control of the DAC from the PC software can be maintained to minimise effort to get up and running.

Alternatively, an FPGA board could be designed with a DAC interface connector compatible with the Fujitsu DAC Module's 114-pin Mictor

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connector, thereby removing the Motherboard. Having done this, it is still possible to continue using the PC USB interface lead and software if this functionality is not incorporated in the FPGA.

Clock Inputs & Analog Outputs

The DAC module is designed to accommodate up to six SMA connectors. Two are used for the differential clock in and four for the two differential analog outputs. Modules are supplied with transformers on-board to perform single ended-to-differential and differential-to-single ended conversions. As such, only 3 SMAs are required. This facilitates easier connection to standard test equipment during evaluation.

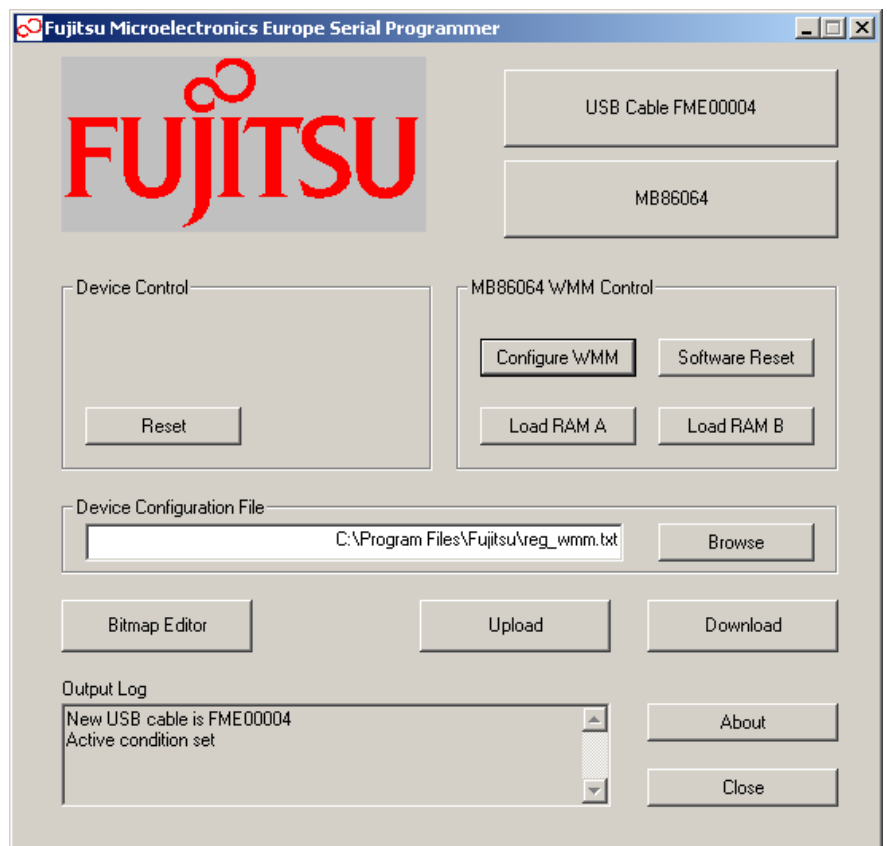
PC Control Software

To simplify control & configuration of the MB86064 during evaluation the development kit includes a PC USB interface lead and a software utility. The USB interface lead interfaces between a host PC's USB port and the 4-wire serial interface implemented on the device. Software is supplied on CD.



Ordering Information

Part	Order Reference
DK86064-1 Development Kit (includes General Purpose Motherboard, DAC module, PC USB Interface Lead & Control Software and User Manual)	DK86064-1
Additional DAC Module	DK86064-1-DAC
SMA Adaptors (optional)	DK86064-1-SMA
PC USB Interface Lead & Control Software	DKUSB-1
MB86064 Device	MB86064PB-G
DK86064-1 User Manual	Contact Fujitsu
MB86064 Datasheet	Contact Fujitsu



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