

TOSHIBA Transistor Silicon NPN Epitaxial Planar Type

MT3S07FS

VHF~UHF Band Low-Noise Amplifier Applications
VHF~UHF Band Buffer Applications

- Superior performance in buffer applications
- Superior noise characteristics
: NF = 1.6 dB, $|S_{21e}|^2 = 8 \text{ dB}$ (f = 2 GHz)

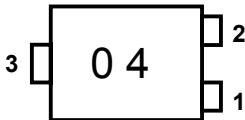
Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	10	V
Collector-emitter voltage	V _{CEO}	5	V
Emitter-base voltage	V _{EBO}	1.5	V
Collector current	I _C	25	mA
Base current	I _B	10	mW
Collector power dissipation	P _C (Note)	85	mW
Junction temperature	T _j	125	°C
Storage temperature range	T _{stg}	−55~125	°C

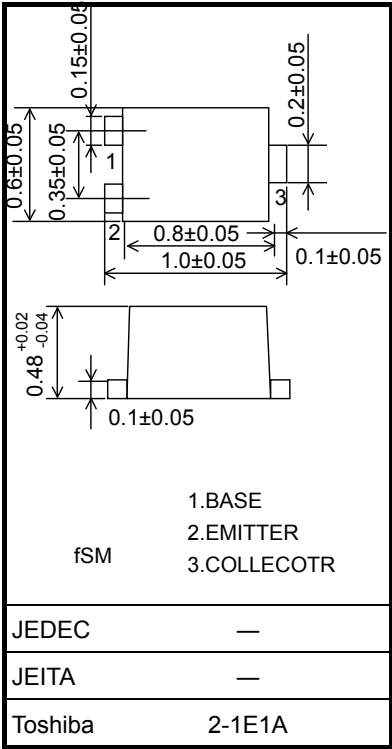
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note: 10 mm² × 1.0 mm(t), mounted on a glass-epoxy printed circuit board.

Marking



Unit: mm



Weight: 0.0006 g (typ.)

Microwave Characteristics (Ta = 25°C)

Characteristic	Symbol	Condition	Min	Typ.	Max	Unit
Transition frequency	f_T	$V_{CE} = 3\text{ V}$, $I_C = 10\text{ mA}$	10	12	—	GHz
Insertion gain	$ S_{21e} ^2 (1)$	$V_{CE} = 1\text{ V}$, $I_C = 5\text{ mA}$, $f = 2\text{ GHz}$	—	8	—	dB
	$ S_{21e} ^2 (2)$	$V_{CE} = 3\text{ V}$, $I_C = 15\text{ mA}$, $f = 2\text{ GHz}$	7.5	10	—	
Noise figure	NF (1)	$V_{CE} = 1\text{ V}$, $I_C = 5\text{ mA}$, $f = 2\text{ GHz}$	—	1.6	3	dB

Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Condition	Min	Typ.	Max	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = 5\text{ V}$, $I_E = 0$	—	—	0.1	μA
Emitter cutoff current	I_{EBO}	$V_{EB} = 1\text{ V}$, $I_C = 0$	—	—	1	μA
DC current gain	h_{FE}	$V_{CE} = 1\text{ V}$, $I_C = 5\text{ mA}$	70	—	140	
Reverse transfer capacitance	C_{re}	$V_{CB} = 1\text{ V}$, $I_E = 0$, $f = 1\text{ MHz}$ (Note)	—	0.4	0.65	pF

Note: C_{re} is measured with a three-terminal method using a capacitance bridge.

Caution

This device is sensitive to electrostatic discharge. Ensure that tools and equipment are sufficiently grounded before handling. When handling individual devices (which are not yet mounted on a circuit board), ensure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

- The information contained herein is subject to change without notice.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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