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DAC1208D650W1

DAC1208D650W1

IDT's DAC1208D650W1 demo board is suitable for dynamic performance evaluations from low to high outputs frequency configuration. The FPGA makes it easy to evaluate and analyze the DAC's dynamic performance and enables use of the full JESD204A feature set (e.g. Multiple DAC Synchronization)

Features

- SW interface to easily program DAC parameters (NCO, offset, interpolation factor, PLL, etc.) On-board AQM+ LO synthesizer SMA connector for DAC and AQM external clock input signals On-board clock generator for DAC and FPGA
- On-board Virtex 5 FPGA for data generation: 2 DACs on same PCB to easily evaluate Multi-Devices Synchronization



Product Options

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Documents

	Title	Type▲	Format	File Size	Date
	Demo Board Software DAC1x08DW1	Manual - Software	ZIP	2.26 MB	07/18/2012

Related Products

Product ID	Title
DAC1208D650HN	Dual 12-bit DAC; up to 650 Msps; 2x, 4x or 8x interpolating with JESD204A interface

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