

SD103A - SD103C

Features

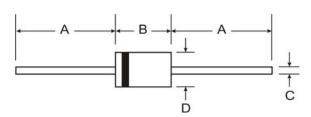
- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- Low Reverse Recovery Time
- Low Reverse Capacitance
- Lead Free Finish, RoHS Compliant (Note 2)

Mechanical Data

Case: DO-35

Case Material: Glass

- Moisture Sensitivity: Level 1 per J-STD-020C
- Leads: Solderable per MIL-STD-202, Method
- Terminals: Finish Sn96.5Ag3.5. Solderable per MIL-STD-202, Method 208 @3
- Polarity: Cathode Band
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.13 grams (approximate)



DO-35				
Dim	Min	Max		
Α	25.40	_		
В	_	4.00		
C	_	0.60		
D	_	2.00		
All Dimensions in mm				

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	SD103A	SD103B	SD103C	Unit
Peak Repetitive Reverse Voltage	V_{RRM}				
Working Peak Reverse Voltage	V_{RWM}	40	30	20	V
DC Blocking Voltage	V_R				
RMS Reverse Voltage	$V_{R(RMS)}$	28	21	14	V
Forward Continuous Current	I_{FM}		350		mA
Repetitive Peak Forward Current (Note 1) @ t ≤ 1.0s	I _{FRM}		1.0		Α
Non-Repetitive Peak Forward Surge Current 8.3 ms Half Sine Wave	I _{FSM}		15		А
Power Dissipation (Note 1)	P_d		400		mW
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$		300		°C/W
Operating Junction Temperature	T _i		125		°C
Storage Temperature Range	T _{STG}		-55 to +150		°C

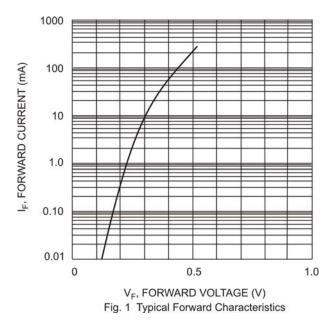
Electrical Characteristics @T_A = 25°C unless otherwise specified

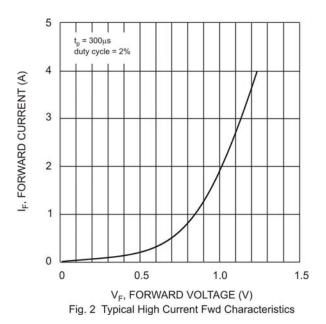
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 3)	SD103A SD103B SD103C	$V_{(BR)R}$	40 30 20	_	_	V	$I_R = 100 \mu A$
Maximum Forward Voltage Drop		V_{FM}	_	_	0.37 0.60	V	$I_F = 20 \text{mA}$ $I_F = 200 \text{mA}$
Maximum Peak Reverse Current (Note 3)	SD103A SD103B SD103C	I _{RM}			5.0	μА	$V_R = 30V$ $V_R = 20V$ $V_R = 10V$
Total Capacitance		C _T		50	_	pF	$V_R = 0V, f = 1.0MHz$
Reverse Recovery Time		t _{rr}		10	_	ns	$I_F = I_R = 50 \text{mA} \text{ to } 200 \text{mA},$ $I_{rr} = 0.1 \text{ x } I_R, R_L = 100 \Omega$

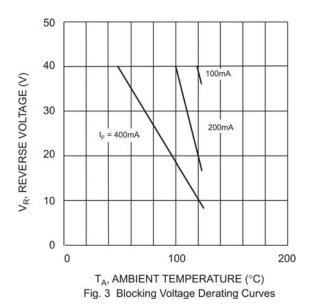
Notes:

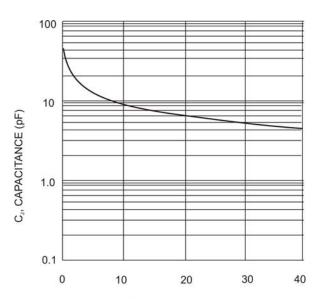
- 1. Valid provided that device terminals are kept at ambient temperature.
- 2. EC Directive 2002/95/EC (RoHS) revision 13.2.2003. Glass and high temperature solder exemptions applied where applicable, see EU Directive Annex Notes 5 and 7.
- 3. Short duration test pulse used to minimize self-heating effect.











V_R, REVERSE VOLTAGE (V) Fig. 4 Typ. Junction Capacitance vs Reverse Voltage

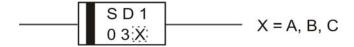


Ordering Information (Note 4)

Device	Packaging	Shipping
SD103A-A	DO-35	10,000 / Ammo Pak
SD103A-T	DO-35	10,000 / Tape & Reel
SD103B-A	DO-35	10,000 / Ammo Pak
SD103B-T	DO-35	10,000 / Tape & Reel
SD103C-A	DO-35	10,000 / Ammo Pak
SD103C-T	DO-35	10,000 / Tape & Reel

4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



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