

SSD II

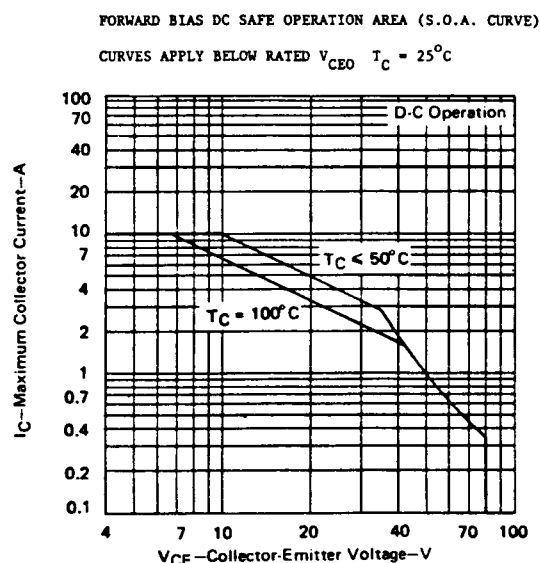
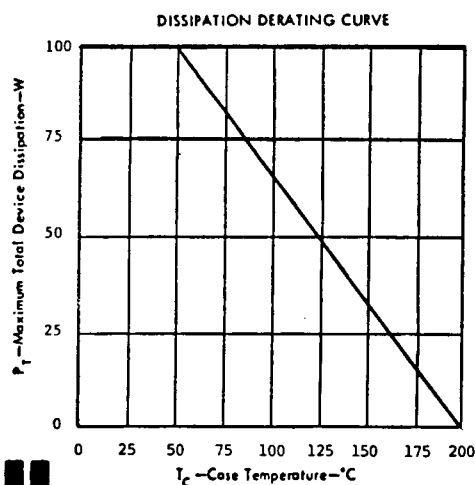
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ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Min.	Max.	Unit
Collector Cutoff Current ($V_{CE} = 40 \text{ Vdc}$)	I_{CEO}		50	μAdc
($V_{CE} = 60 \text{ Vdc}$)	I_{CES}		1.0	μAdc
Collector Cutoff Current ($V_{CE} = 100 \text{ Vdc}$)	I_{CEX}		1.0	mAdc
($V_{CE} = 60 \text{ Vdc}$, $V_{BE} = 2 \text{ Vdc}$, $T_C = 150^\circ\text{C}$)	I_{CEX}		500	μAdc
Emitter Cutoff Current ($V_{EB} = 4 \text{ Vdc}$)	I_{EBO}		1.0	μAdc
($V_{EB} = 5.5 \text{ Vdc}$)			1.0	mAdc
DC Current Gain*				
($I_C = 100 \text{ mAdc}$, $V_{CE} = 5 \text{ Vdc}$)	h_{FE}	20		
($I_C = 5 \text{ Adc}$, $V_{CE} = 5 \text{ Vdc}$)		50		
($I_C = 10 \text{ Adc}$, $V_{CE} = 5 \text{ Vdc}$)		30	90	
		70	200	
		20		
		45		
Collector - Emitter Saturation Voltage*				
($I_C = 5 \text{ Adc}$, $I_B = 500 \text{ mAdc}$)	$V_{CE(SAT)}$		0.9	Vdc
($I_C = 10 \text{ Adc}$, $I_B = 500 \text{ mAdc}$)			1.5	
Base - Emitter Saturation Voltage*				
($I_C = 5 \text{ Adc}$, $I_B = 500 \text{ mAdc}$)	$V_{BE(SAT)}$		1.8	Vdc
($I_C = 10 \text{ Adc}$, $I_B = 1 \text{ Adc}$)			2.2	
Current - Gain - Bandwidth Product				
($I_C = 500 \text{ mAdc}$, $V_{CE} = 5 \text{ Vdc}$, $f = 20 \text{ MHz}$)	f_T	35		MHz
		40		
Output Capacitance				
($V_{CB} = 10 \text{ Vdc}$, $I_E = 0$, $f = 1 \text{ MHz}$)	C_{ob}		500	pf
Base - Emitter Voltage*				
($V_{CE} = 5 \text{ Vdc}$, $I_C = 5 \text{ Adc}$)	$V_{BE(ON)}$		1.8	Vdc
Delay Time	t_d		100	ns
Rise Time	t_r		100	ns
Storage Time	t_s		2.0	μs
Fall Time	t_f		200	ns

*Pulse Test: Pulse width = 300 μs , DutyCycle = 2%

TYPICAL OPERATING CURVES



SSDI SOLID STATE DEVICES, INC.