

**GaAs SP4T Switch, Absorptive, Single Supply
DC - 4.0 GHz**

**MASWCC0010
V2**

Features

- Operates DC - 4 GHz on Single Supply
- ASIC TTL / CMOS Driver
- Leadless 4 x 7 mm Chip Scale Plastic Package
- Low DC Power Consumption
- 50 Ohm Nominal Impedance
- Test Boards are Available
- Tape and Reel is Available
- Lead-Free CSP-2 Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- 260°C Reflow Compatible
- RoHS* Compliant Version of SW90-0003

Description

M/A-COM's MASWCC0010 is a SP4T absorptive pHEMT switch with integral TTL driver. This device is in an MLP plastic surface mount package. This switch offers excellent broadband performance and repeatability from DC to 4 GHz, while maintaining low DC power dissipation. The MASWCC0010 is ideally suited for wireless infrastructure applications.

Pin Configuration^{2, 3, 4}

Pin No.	Function	Pin No.	Function
1	CP2	19	GND
2	Vee	20	NC ¹
3	NC ¹	21	GND
4	C4	22	RFC
5	C3	23	GND
6	C2	24	NC ¹
7	C1	25	RF3
8	NC ¹	26	GND
9	NC ¹	27	NC ¹
10	NC ¹	28	GND
11	NC ¹	29	RF4
12	NC ¹	30	GND
13	GND	31	NC ¹
14	RF1	32	Vee
15	GND	33	Vcc
16	NC ¹	34	NC ¹
17	GND	35	Vcc
18	RF2	36	CP1

Ordering Information

Part Number	Package
MASWCC0010	Bulk Packaging
MASWCC0010TR	1000 piece reel
MASWCC0010-TB	Sample Test Board

Note: Reference Application Note M513 for reel size information.

Note: Die quantity varies.

1. NC = No Connection
2. For single supply operation VEE is internally generated and must remain isolated from external power supplies. Generated noise is typical of switching DC-DC converters.
3. Connections and external components shown in functional schematic are required. 0.1μF Capacitors need to be located near pins 32 & 33.
4. The exposed pad centered on the package bottom must be connected to RF and DC ground. (For PQFN Packages)

* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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Electrical Specifications: T_A = 25°C

Parameter	Test Conditions	Frequency	Units	Min.	Typ.	Max.
Insertion Loss	RFC-RF1, 2, 3, 4	DC - 4.0 GHz	dB	—	—	2.3
Isolation	—	DC - 4.0 GHz	dB	38	—	—
VSWR	On (RFC, RF1-RF4) Logic per Truth Table	DC - 4.0 GHz	Ratio	—	—	2.0:1
	Off (RF1-RF4) Logic per Truth Table	DC - 4.0 GHz	Ratio	—	—	2.0:1
1 dB Compression	—	50 MHz	dBm	—	+15	—
	—	0.5 - 4.0 GHz	dBm	—	+27	—
Input IP ₃	Two-tone inputs up to +5 dBm	50 MHz	dBm	—	30	—
		0.5-4.0 GHz	dBm	—	40	—
Switching Speed	Ton (50% Control to 90% RF)		ns	—	35	—
	Toff (50% Control to 10% RF)		ns	—	20	—
	Trise (10% to 90% RF)		ns	—	12	—
	Tfall (90% to 10% RF)		ns	—	2	—
V _{cc}	—	—	V	4.5	5.0	5.5
Logic "0"	Sink Current is 20 µA max.	—	V	0.0	—	0.8
Logic "1"	Source Current is 20 µA max.	—	V	2.0	—	5.0
I _{cc} ⁵	V _{cc} min to max, Logic "0" or "1"	—	mA	—	5	8
Turn-on Current ⁶	For guaranteed start-up	—	mA	—	—	125
Switching Noise	Generated from DC-DC Converter with recommended capacitors	3.5 MHz	dBm	—	-93	—
Thermal Resistance θ _{jc}	—	—	°C/W	—	15	—

- During turn-on, the device requires an initial start up current (I_{cc}) specified as "Turn-on Current". Once operational, I_{cc} will drop to the specified levels. This is not applicable to dual supply operation.
- The DC-DC converter is guaranteed to start in 100 µs as long as the power supplies have the maximum turn-on current available for start-up.

Absolute Maximum Ratings^{7,8,9}

Parameter	Absolute Maximum
Max. Input Power 0.05 GHz 0.5 - 4.0 GHz	+27 dBm +34 dBm
Bias Voltages V _{cc} Control Voltage ¹⁰	+5.5V -0.5V to V _{cc} +0.5V
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +125°C

Truth Table (Switch)

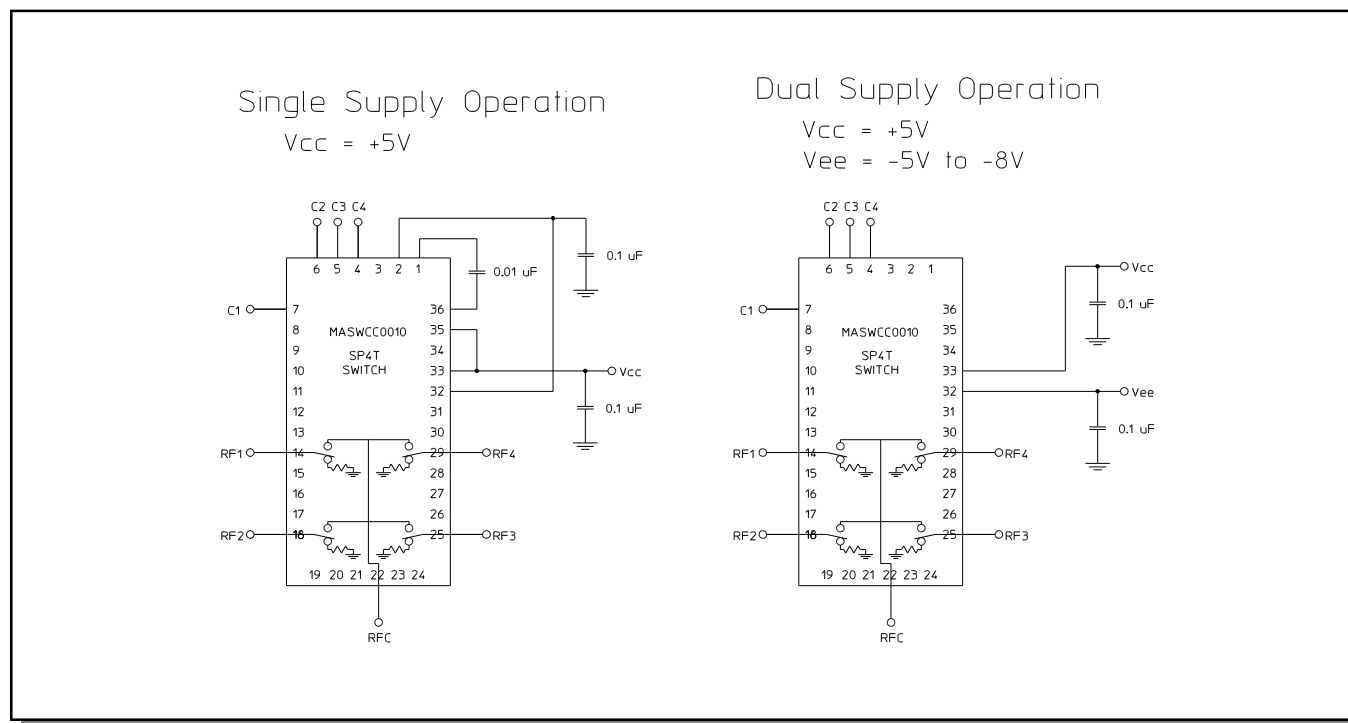
C1	C2	C3	C4	RFC-RF1	RFC-RF2	RFC-RF3	RFC-RF4
1	0	0	0	On	Off	Off	Off
0	1	0	0	Off	On	Off	Off
0	0	1	0	Off	Off	On	Off
0	0	0	1	Off	Off	Off	On

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.
- When the RF input is applied to the terminated port, the absolute maximum power is +30 dBm.
- Standard CMOS TTL interface, latch-up will occur if logic signal is applied prior to power supply.

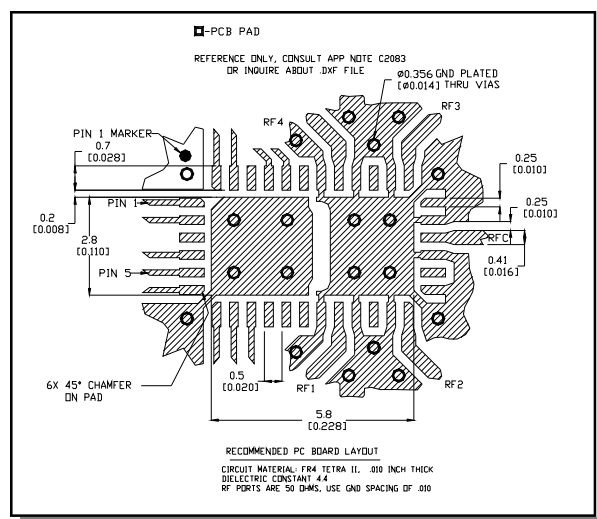
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Functional Schematic



Recommended PCB Configuration¹¹



11. Application Note C2083 is available on line at www.macom.com

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

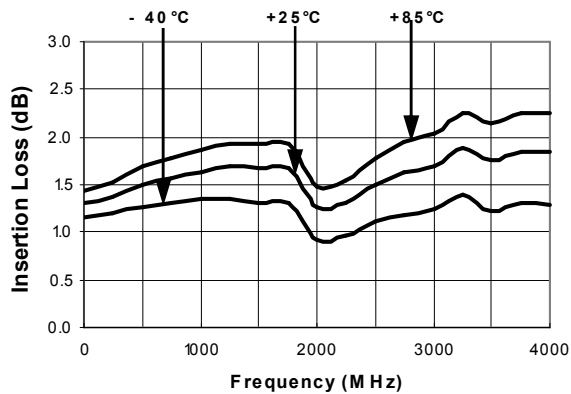
Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

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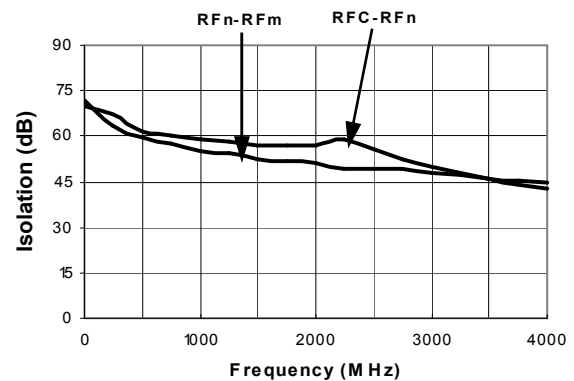
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Typical Performance Curves

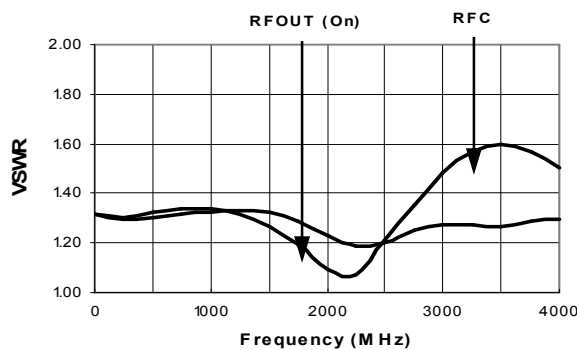
Insertion Loss vs. Frequency



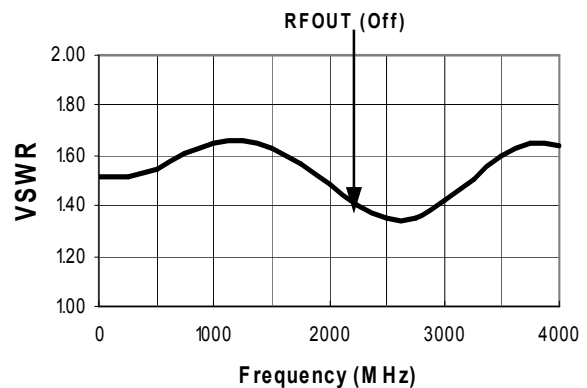
Isolation (dB) vs. Frequency



On VSWR vs. Frequency



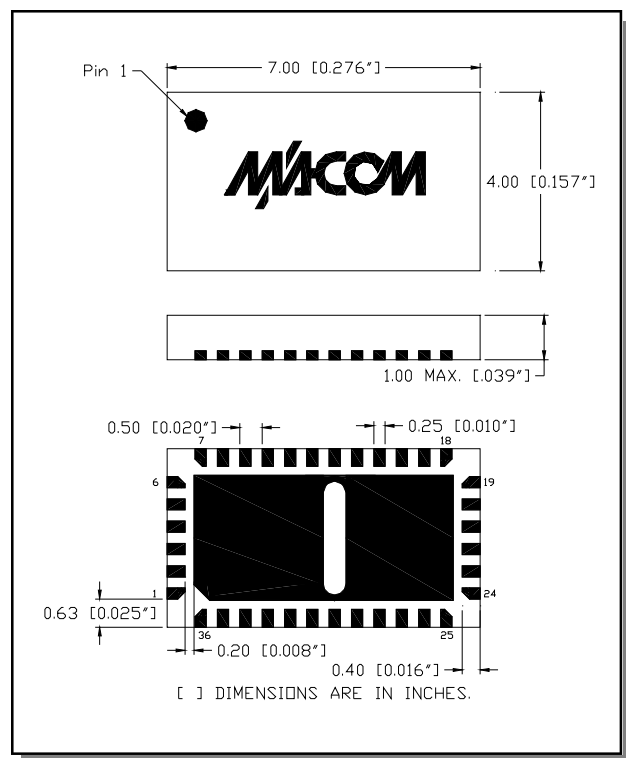
VSWR (Terminations) vs. Frequency



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**CSP-2, Lead-Free, 4 x 7 mm, 36-lead,
PQFN[†]**



[†] Reference Application Note M538 for lead-free solder reflow recommendations.