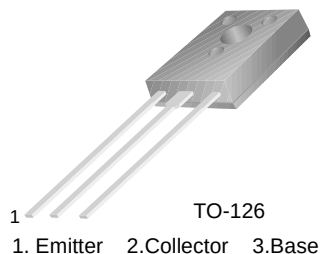


KSD882

KSD882

Audio Frequency Power Amplifier Low Speed Switching

- Complement to KSB772



NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
V_{CBO}	Collector- Base Voltage	40	V
V_{CEO}	Collector-Emitter Voltage	30	V
V_{EBO}	Emitter- Base Voltage	5	V
I_C	Collector Current (DC)	3	A
I_{CP}	*Collector Current (Pulse)	7	A
I_B	Base Current	0.6	A
P_C	Collector Dissipation ($T_C=25^\circ\text{C}$)	10	W
P_C	Collector Dissipation ($T_a=25^\circ\text{C}$)	1	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature	- 55 ~ 150	$^\circ\text{C}$

* $PW \leq 10\text{ms}$, Duty Cycles $\leq 50\%$

Electrical Characteristics $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
I_{CBO}	Collector Cut-off Current	$V_{CB} = 30\text{V}$, $I_E = 0$			1	μA
I_{EBO}	Emitter Cut-off Current	$V_{EB} = 3\text{V}$, $I_C = 0$			1	μA
h_{FE1} h_{FE2}	*DC Current Gain	$V_{CE} = 2\text{V}$, $I_C = 20\text{mA}$ $V_{CE} = 2\text{V}$, $I_C = 1\text{A}$	30 60	150 160	400	
$V_{CE(sat)}$	*Collector-Emitter Saturation Voltage	$I_C = 2\text{A}$, $I_B = 0.2\text{A}$		0.3	0.5	V
$V_{BE(sat)}$	*Base-Emitter Saturation Voltage	$I_C = 2\text{A}$, $I_B = 0.2\text{A}$		1.0	2.0	V
f_T	Current Gain Bandwidth Product	$V_{CE} = 5\text{V}$, $I_E = 0.1\text{A}$		90		MHz
C_{ob}	Output Capacitance	$V_{CB} = 10\text{V}$, $I_E = 0$ $f = 1\text{MHz}$		45		pF

* Pulse Test: $PW \leq 350\mu\text{s}$, Duty Cycle $\leq 2\%$ Pulsed

h_{FE} Classification

Classification	R	O	Y	G
h_{FE2}	60 ~ 120	100 ~ 200	160 ~ 320	200 ~ 400

Typical Characteristics

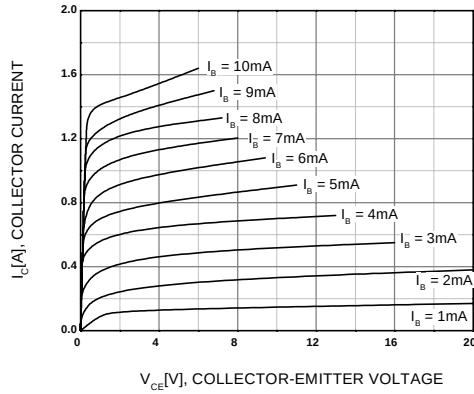


Figure 1. Static Characteristic

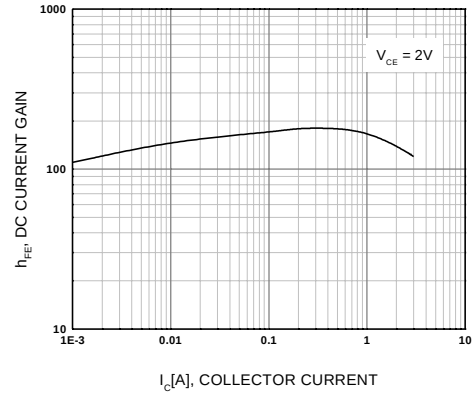


Figure 2. DC current Gain

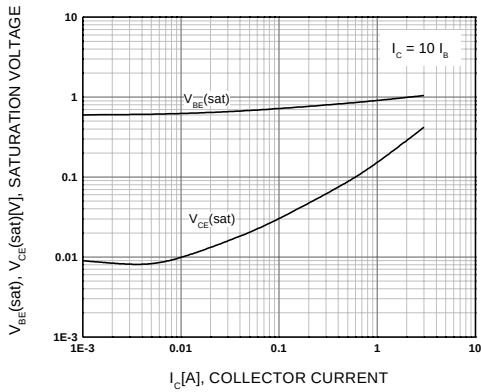


Figure 3. Base-Emitter Saturation Voltage
Collector-Emitter Saturation Voltage

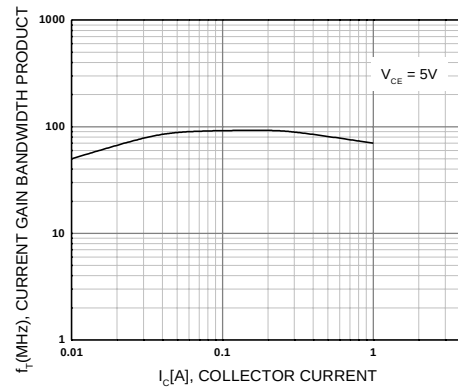


Figure 4. Current Gain Bandwidth Product

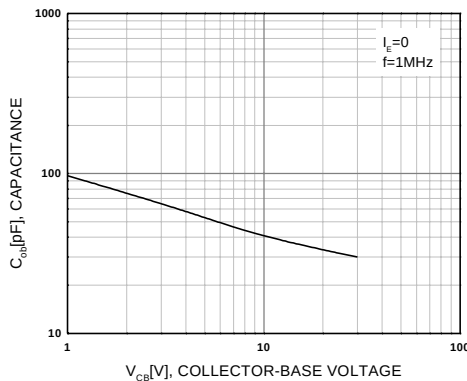


Figure 5. Collector Output Capacitance

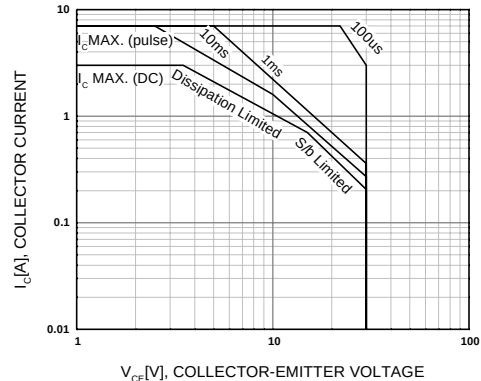


Figure 6. Safe Operating Area

Typical Characteristics (Continued)

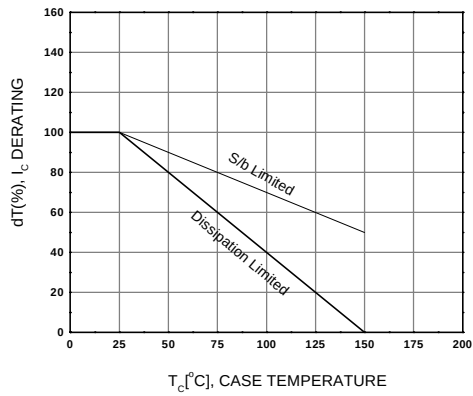


Figure 7. Derating Curve Of Safe Operating Areas

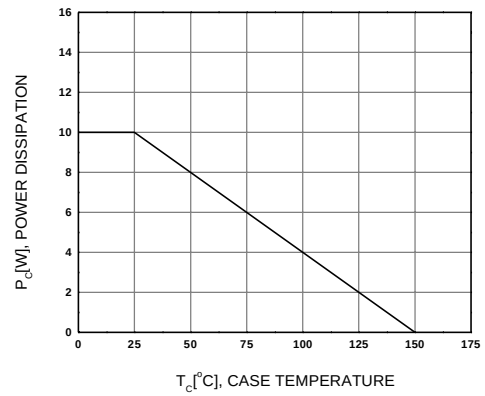


Figure 8. Power Derating

Technical drawing of a 10-pin D-sub connector, showing front and side views with dimensions.

Front View Dimensions:

- Top Flange Height: 3.90 ± 0.10
- Flange Width: 8.00 ± 0.30
- Central Hole Diameter: $\varnothing 3.20 \pm 0.10$
- Overall Height: 14.20 MAX
- Pin Lengths (from top):
 - Pin 1: 0.75 ± 0.10
 - Pin 2: 1.60 ± 0.10
 - Pin 3: 0.75 ± 0.10
- Pin Spacing: 2.28 TYP [2.28 ± 0.20]
- Pin 1 Label: #1
- Pin 2 Label: 2
- Pin 3 Label: 3
- Pin 4 Label: 4
- Pin 5 Label: 5
- Pin 6 Label: 6
- Pin 7 Label: 7
- Pin 8 Label: 8
- Pin 9 Label: 9
- Pin 10 Label: 10

Side View Dimensions:

- Overall Height: 11.00 ± 0.20
- Overall Length: 16.10 ± 0.20
- Pin Lengths (from side):
 - Pin 1: (1.00)
 - Pin 2: (0.50)
 - Pin 3: 1.75 ± 0.20
- Pin Spacing: $0.50^{+0.10}_{-0.05}$

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