

- General information
- Package information
- Environmental and Compliance information
- Manufacturing and Qualification information
- Ordering information
- Operating Characteristics

General Information

Parameter	Value
Part Number	MSC8144SVT800A
Description	800MHZ PACSUN NONECRY
Product Line	GFC8144
PTI	NINA
Material Type	Tested Packaged Device
Life Cycle Description (code)	PRODUCT NEWLY INTRO D/RAMPUP(1)
Status	Active

Package Information

Parameter	Value
Package Description	FCPBGA 783 29SQ3.08P1.0
Pin/Lead/Ball Count	783
Tape & Reel	No

Environmental and Compliance Information

Parameter	Value
Pb-Free	No
RoHS Compliant	
Product Content Report	Product Content Report
RoHS CoA	Contact Us
2nd Level Interconnect	e1
Moisture Sensitivity Level (MSL)	3
Floor Life	168 HOURS
Package Peak Temperature (°C)	245

Manufacturing and Qualification information

Parameter	Value
Micron Size (μm)	.09
Application/Qualification Tier	COMMERCIAL, INDUSTRIAL

Ordering Information

Parameter	Value
Minimum Package Quantity (MPQ)	36
MPQ Container	TRAY
Preferred Order Quantity (POQ)	180
POQ Container	BRICK
Leadtime (weeks)	16
Export Control Classification Number (US)	5A992
Harmonized Tariff (US Disclaimer)	8542.31.0000
Budgetary Price QTY 1000+ (\$US)	-

Operating Characteristics

Parameter	Value
Additional Features	Memory Management Unit (MMU) PLL Watchdog OSC/Timer
Addressable External Memory (Byte)	4000000000
Ambient Operating Temperature (Ta) (Max) (oC)	105
Ambient Operating Temperature (Ta) (Min) (oC)	0
Bus Frequency (Max) (MHz)	66
Bus Interface	DDR Controller PCI RapidIO Serial
Cache (kByte)	320
Communication Protocol	ATM AAL Ethernet TDM UTOPIA UART
Core - Number of Cores (Spec)	4
Core - Operating Frequency (Max) (MHz)	800
Core - Operating Voltage (Spec) (V)	1
Core - Performance MMACS	12800
Core - Type	SC3400
Debug Features	JTAG On-Chip Emulation
Ethernet	10/100/1000 BaseT MAC (FEC) 10/100 Ethernet MII
External Bus Width (bit)	32
External Interrupts	19
External Memory Supported	DDR1 DDR DDR2
I/O Pins	32
Internal RAM (Byte)	10512000000
L2 Cache (Max) (kByte)	128
Serial Interface - Number of Interfaces	8 1 1 1
Serial Interface - Type	TDM I2C UART SPI
Standby Function	STOP WAIT
Timer	Realtime Interrupts PWM Output Compare Input Capture Pulse Accumulator Output Capture Enhanced Capture Referenced Compare
Timers - Channels	16 2 5
Timers - Number of Timers	16 2 5
Timers - Size (bit)	16 32 32
Total Cores (Spec)	4
Total DMA Channels	16