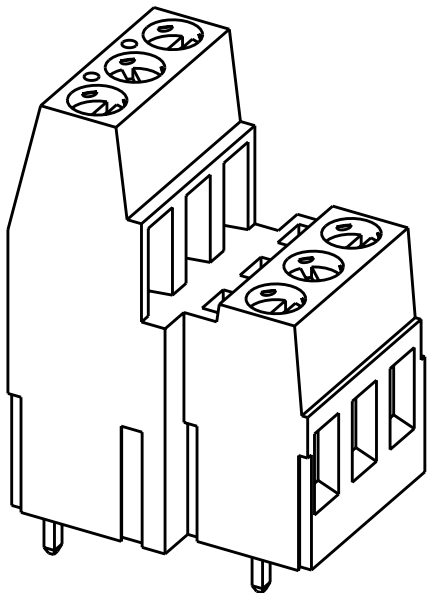
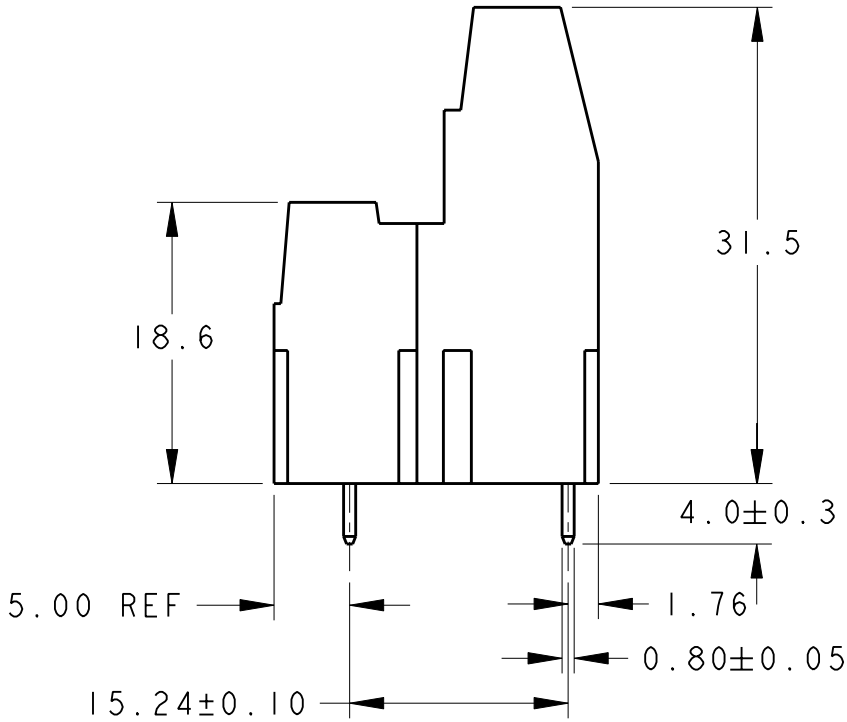
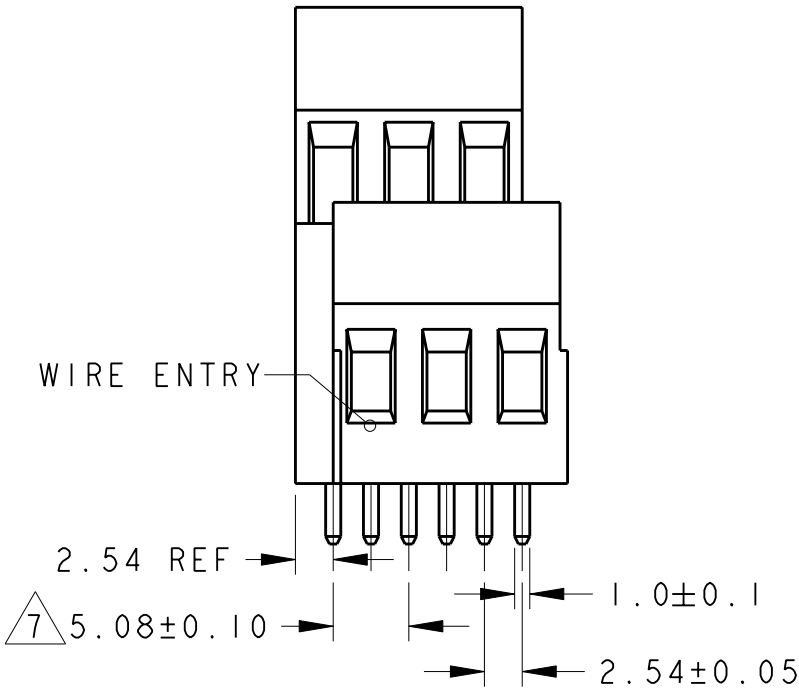
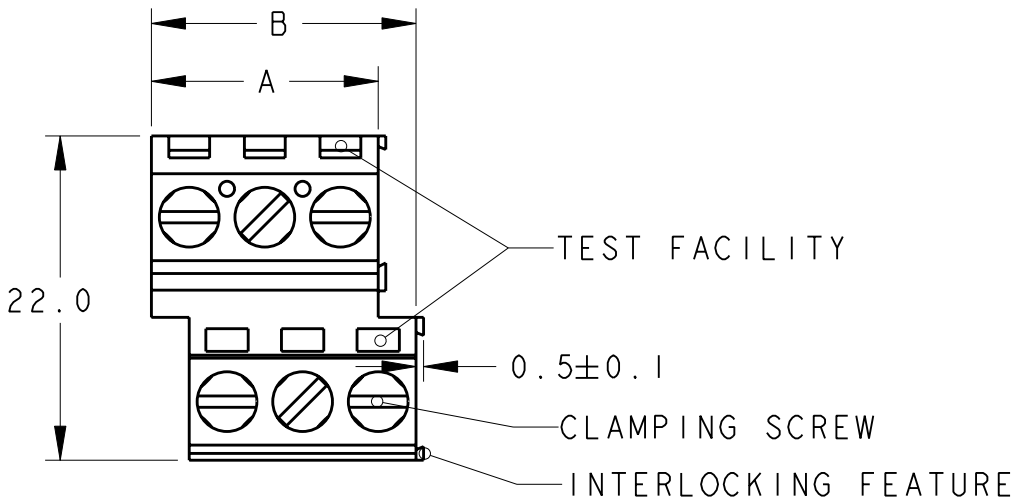
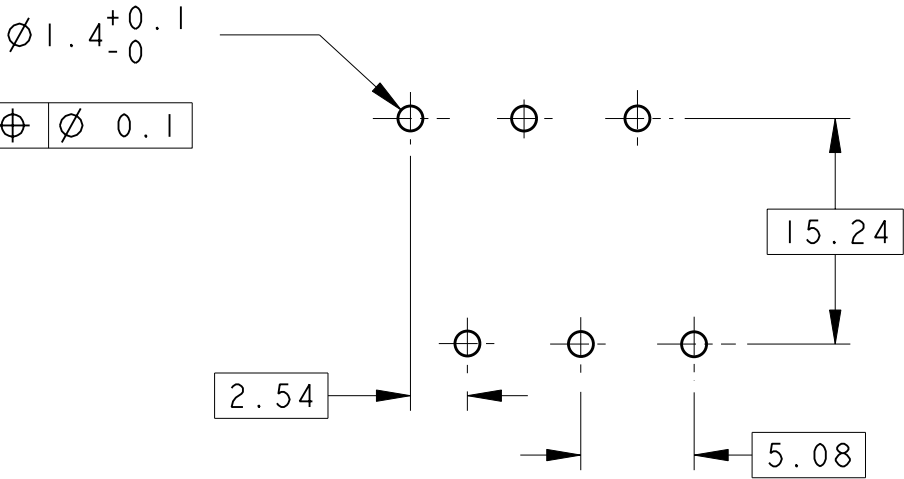


LOC	DIST	REVISIONS					
FT	0	P	LTR	DESCRIPTION	DATE	DWN	APVD
			C	REVISED PER 0G3B-0119-01	19DEC2001	EZ	DB
			D	REVISED EC 0G3B-0875-03	21JAN2004	SW	CR
			DI	REVISED PER ECO-07-018776	29AUG2007	SS	CR



282909-3 AS SHOWN

RECOMMENDED PC BOARD LAYOUT



	8	SEE SHEET 2	63.50	60.96	12	282909-6	9
PRELIMINARY		-	124.46	121.92	24	282909-5	9
PRELIMINARY	8	SEE SHEET 2	124.46	121.92	24	282909-4	9
		-	17.78	15.24	3	282909-3	
		-	12.70	10.16	2	282909-2	
STATUS	MARKING		DIM.B	DIM.A	N° POS.	PART NUMBER	

THIS DRAWING IS A CONTROLLED DOCUMENT FOR TYCO ELECTRONICS CORPORATION. IT IS SUBJECT TO CHANGE AND THE CONTROLLING ENGINEERING ORGANIZATION SHOULD BE CONTACTED FOR THE LATEST REVISION.		DWN E. ZANOLINI 19DEC2001	Tyco Electronics Harrisburg, PA 17105-3608				
		CHK D. BIEVENOUR 19DEC2001					
		APVD D. BIEVENOUR 19DEC2001	NAME TERMINAL BLOCK, PCB MOUNT 1ST AND 2ND LEVEL STACKING W/ INTERLOCK, 5.08mm PITCH				
		PRODUCT SPEC 108-20166					
		APPLICATION SPEC 114-20079	SIZE A3				
		WEIGHT -					
		CUSTOMER DRAWING	SCALE 1:1				
			SHEET 1				
			OF 2				
			REV DI				

