

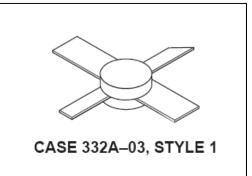
### Microwave Pulse Power Silicon NPN Transistor 150W (peak), 960-1215MHz

M/A-COM Products Released - Rev. 07.07

### Designed for Class B and C common base amplifier applications in short pulse TACAN, IFF, and DME transmitters.

- Guaranteed performance @ 1090 MHz, 50 Vdc Output power = 150 W peak Minimum gain = 7.8 dB
- 100% tested for load mismatch at all phase angles with 10:1 VSWR
- Industry standard package
- Nitride passivated
- Gold metallized, emitter ballasted for long life and resistance to metal migration
- Internal input matching for broadband operation





#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	70	Vdc
Emitter–Base Voltage	V <sub>EBO</sub>	4.0	Vdc
Collector Current — Peak (1)	I <sub>C</sub>	12	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C (1) (2) Derate above 25°C	P <sub>D</sub>	583 3.33	Watts W/°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case (3)		0.3	°C/W

### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•		•	•	•
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 50 mAdc, V <sub>BE</sub> = 0)	V <sub>(BR)CES</sub>	70	_	_	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 50 mAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	70	_	_	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 5.0 mAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	4.0	_	_	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 50 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	_	_	10	mAdc
ON CHARACTERISTICS	·				
DO 0	L	40	20	1	1

NOTES:

DC Current Gain (4) (I <sub>C</sub> = 5.0 Adc, V <sub>CE</sub> = 5.0 Vdc)	h <sub>FE</sub>	10	30	_	_
(10 11111111111111111111111111111111111	1				

#### Pulse Width = 10 μs, Duty Cycle = 1%.

- 2. This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF amplifier.
- 3. Thermal Resistance is determined under specified RF operating conditions by infrared measurement techniques.
- 4. 80 μs Pulse on Tektronix 576 or equivalent.

• North America Tel: 800.366.2266 / Fax: 978.366.2266

- Europe Tel: 44.1908.574.200 / Fax: 44.1908.574.300
- Asia/Pacific Tel: 81.44.844.8296 / Fax: 81.44.844.8298 Visit www.macomtech.com for additional data sheets and product information.

(continued)

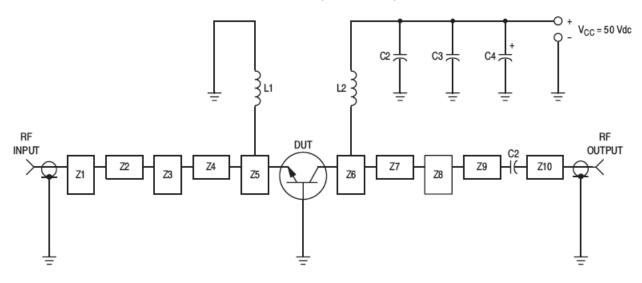


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### ELECTRICAL CHARACTERISTICS — continued (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
DYNAMIC CHARACTERISTICS		•		•	
Output Capacitance (V <sub>CB</sub> = 50 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	_	25	32	pF
FUNCTIONAL TESTS (Pulse Width = 10 μs, Duty Cycle = 1.0%)					
Common–Base Amplifier Power Gain (V <sub>CC</sub> = 50 Vdc, P <sub>out</sub> = 150 W pk, f = 1090 MHz)	G <sub>PB</sub>	7.8	9.8	_	dB
Collector Efficiency (V <sub>CC</sub> = 50 Vdc, P <sub>out</sub> = 150 W pk, f = 1090 MHz)	η	35	40	_	%
Load Mismatch (V <sub>CC</sub> = 50 Vdc, P <sub>out</sub> = 150 W pk, f = 1090 MHz, VSWR = 10:1 All Phase Angles)	Ψ	No Degradation in Power Output			



C1, C2 - 220 pF Chip Capacitor, 100-mil ATC

C3 — 0.1 µF/100 V

C4 — 47 µF/75 V Electrolytic

L1, L2 - 3 Turns #18 AWG, 1/8" ID

Z1-Z10 — Distributed Microstrip Elements — See Photomaster Board Material — 0.031" Thick Teflon-Fiberglass,  $\varepsilon_r$  = 2.5

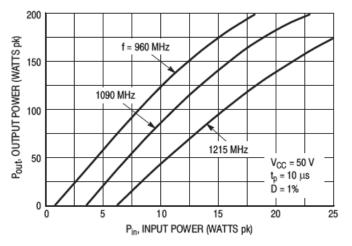
Figure 1. 1090 MHz Test Circuit

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200
Pin = 20 W pk

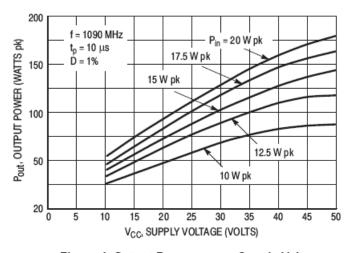
17.5 W pk

17.5 W pk

12.5 W pk

Figure 2. Output Power versus Input Power

Figure 3. Output Power versus Frequency



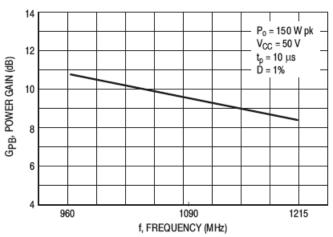


Figure 4. Output Power versus Supply Voltage

Figure 5. Power Gain versus Frequency

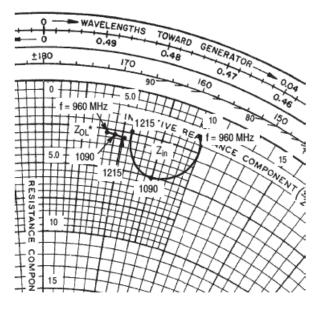
<sup>•</sup> **Europe** Tel: 44.1908.574.200 / Fax: 44.1908.574.300

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 $P_{out} = 150 \text{ W pk} \quad V_{CC} = 50 \text{ V}$  $t_p = 10 \text{ \mu s} \quad D = 1\%$ 

f	Z <sub>in</sub>	Z <sub>OL</sub> *
MHz	Ohms	Ohms
960	1.5 + j9.6	2.6 + j4.1
1090	5.0 + j7.5	2.7 + j4.6
1215	2.4 + j5.6	2.8 + j5.3

Z<sub>OL</sub>\* = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage, and frequency.

Figure 6. Series Equivalent Input/Output Impedance

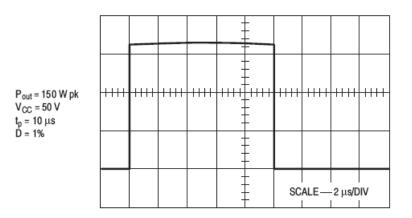


Figure 7. Typical Pulse Performance

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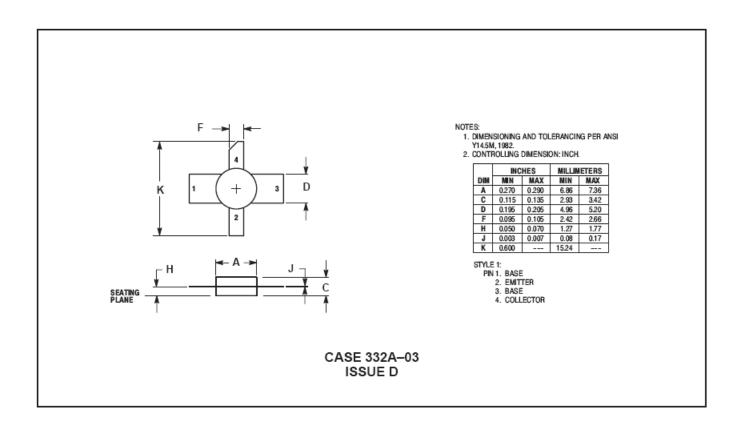
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### PACKAGE DIMENSIONS



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