SKYWORKS

AAT1405

Four-Channel LED Backlight Driver
with Integrated Boost and High Frequency Direct PWM Dimming

General Description

The AAT1405 is a highly integrated, high efficiency LED backlight solution for notebook computers, monitors and portable TVs. The device operates from DC inputs, cigarette lighter adapters, or multi-cell Li-ion batteries over the 4.5V to 26V voltage range.

An integrated boost (step-up) converter provides up to 45V output for driving series LEDs. Four precision current sinks are programmed up to 30mA per string through one external R_{SET} resistor, supporting up to 44[1] white LEDs at 120mA total output current.

LED strings may be disabled or operated in parallel for increased drive capability. The boost output voltage is set by the LED string with the highest voltage requirement, allowing a wide range of LED characteristics.

The PWM dimming range at 100Hz is up to 1,000:1.

The boost switching frequency is selectable (up to 1.3MHz) to allow optimum efficiency and the smallest external L/C filtering components. Alternatively, the device may be synchronized to an external clock.

Boost current mode control provides fast response to line and load transients. Integrated Light-Load mode ensures highest efficiency across the entire load range.

Fault tolerant circuitry extends system life by disabling open LED strings. The unique high voltage current sinks prevent damage resulting from shorted LEDs.

The AAT1405 is available in the Pb-free, thermally enhanced 24-pin 3x4 TQFN package.

Features

- V_{IN} Range: 4.5V to 5.5V / 5.0V to 26.0V
- LX Rated to 50V
- Maximum I_{OUT}: 120mA
- Up to 92% Efficiency
- High Efficiency Light-Load Mode
- 4 LED Current Sinks up to 30mA/each
  - ±2% Accuracy (21mA)
  - ±2% Matching (21mA)
- Flexible Configurations
  - Disable or Parallel
- Switching Frequency Options
  - 675kHz or 1.3MHz
  - Synchronize to System Clock
- PWM Direct Dimming Input
  - Up to 100kHz Prevents Audio Interference
  - Fast Turn-On/Off
  - Wide 1,000:1 Dimming Range (100Hz)
- Fault Tolerant: Open/Short LED(s)
- Current Limit Protection
- Over-Voltage Protection
- Over-Temperature Protection
- Soft-Start Minimizes Inrush Current
- TQFN34-24 Low Profile Package
- -40°C to +85°C Temperature Range

Applications

- Monitors
- Notebook Computers
- Portable TV
- Portable DVD Players
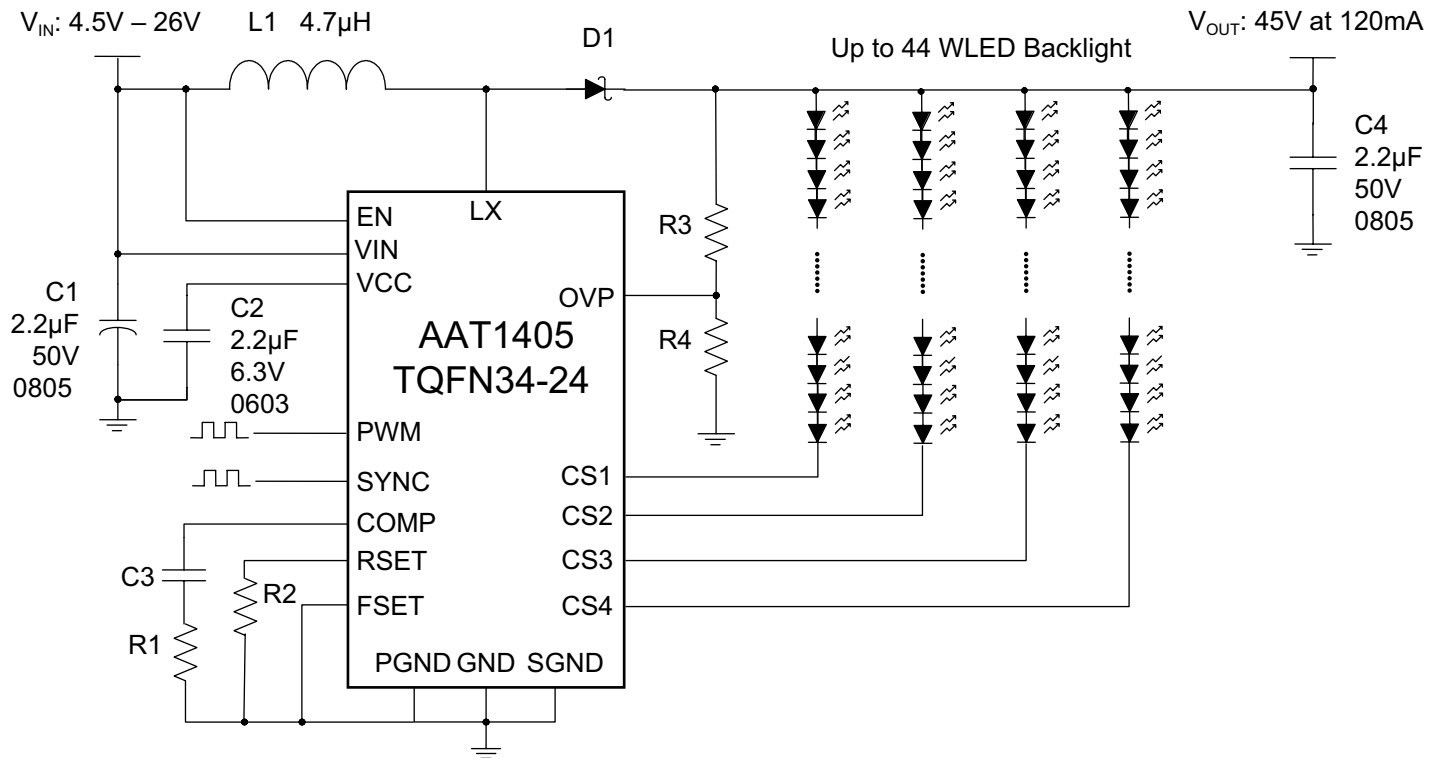- White LED Backlight

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1. The maximum number of LEDs in each string is dependent upon the maximum V_F of the diodes in that string. Under no event should the voltage at LX be exceeded.

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AAT1405

Four-Channel LED Backlight Driver
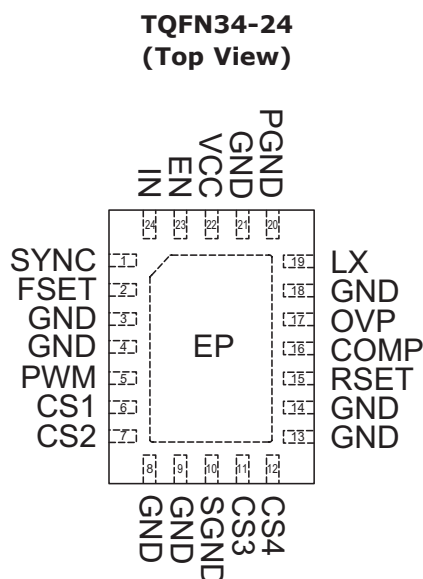*with Integrated Boost and High Frequency Direct PWM Dimming*

Typical Application

V$_{IN}$: 4.5V – 26V   L1   4.7µH   D1   Up to 44 WLED Backlight   V$_{OUT}$: 45V at 120mA

EN   LX

VIN

VCC   OVP

AAT1405
TQFN34-24

PWM

SYNC   CS1

COMP   CS2

RSET   CS3

FSET   CS4

PGND GND SGND

C1
2.2µF
50V
0805

C2
2.2µF
6.3V
0603

C3

R1   R2

R3

R4

C4
2.2µF
50V
0805

AAT1405

Four-Channel LED Backlight Driver
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Pin Descriptions

Pin #	Symbol	Function	Description
1	SYNC	I	Synchronizes switching frequency to external system clock. Tie to GND to disable this feature.
2	FSET	I	Connect logic high to set internal oscillator to 1300kHz. Connect logic low to set internal oscillator to 675kHz.
3, 4, 8, 9, 13, 14, 18, 21	GND	GND	Connect to GND.
5	PWM	I	Direct PWM input pin. Connect logic level PWM input signal in the frequency range 100Hz-100kHz to this pin to enable PWM dimming.
6	CS1	O	Output current sink 1. Connect to SGND to disable channel 1.
7	CS2	O	Output current sink 2. Connect to SGND to disable channel 2.
10	SGND	GND	Current sink ground tied to return of internal current sinks CS1-CS4.
11	CS3	O	Output current sink 3. Connect to SGND to disable channel 3.
12	CS4	O	Output current sink 4. Connect to SGND to disable channel 4.
15	RSET	I	Connect resistor to ground to set maximum current through the LED strings.
16	COMP	I	Connect an external resistor and capacitor to ground to compensate the boost converter.
17	OVP	I	Over-voltage protection pin. Connect resistive divider between VOUT and GND. Care should be taken to ensure that the voltage at LX does not exceed its maximum rating under extreme operating conditions.
19	LX	O	Switching node of boost converter. Connect an inductor between this pin and input voltage source. Connect the anode of Schottky diode between this pin and the boost output capacitor.
20	PGND	GND	Power ground; tied to source of integrated NMOS switching device.
22	VCC	I/O	Internal regulated voltage when operating from input voltage range 5.0V to 26.0V. De-couple with a 2.2µF capacitor to ground. Do not source current from this node. Input voltage pin when operating from input voltage range 4.5V to 5.5V.
23	EN	I	Logic high enable pin. Pull logic high or tie to IN to enable the device. Pull low to disable the device and minimize quiescent current; pulling low also disables the internal linear regulator.
24	IN	I	Input voltage to IC. Tied to input voltage source and input boost inductor.
EP		GND	Exposed paddle. Connect to PCB GND plane. PCB paddle should maintain acceptable junction temperature.

Pin Configuration

TQFN34-24
(Top View)

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Four-Channel LED Backlight Driver
with Integrated Boost and High Frequency Direct PWM Dimming

Absolute Maximum Ratings[1]

Symbol	Description	Value	Units
V_{LX}	LX Voltage to GND	50	V
$V_{IN,EN}$	Input Voltage, EN to GND	-0.3 to 30	
V_{CSx}	Output Current Sinks CS1 – CS4 to GND	-0.3 to 32	
V_{CC}	V_{CC} Voltage to GND	-0.3 to 7.0	
OVP, COMP, PWM, SYNC, RSET,	OVP, COMP, PWM, SYNC, CLK Voltage to GND	-0.3 to V_{CC} + 0.3	
I_{OUT}	Maximum DC Output Current[2]	134	mA
T_J	Maximum Junction Operating Temperature	-40 to +150	°C
T_{LEAD}	Maximum Soldering Temperature (at leads, 10 sec.)	300	
P_D	Maximum Power Dissipation[3]	2	W
Θ_{JA}	Thermal Resistance[3, 4]	50	°C/W

Recommended Operating Conditions

Symbol	Description	Value	Units
V_{IN}	Input Voltage Range	5 to 26	V
V_{OUT}	Output Voltage Range	V_{IN} + 3 to 45	
F_{PWM}	PWM Dimming Frequency Range	0.1 to 100	kHz
I_{OUT}	DC Output Current	80 to 120	mA
T_A	Operating Ambient Temperature	-40 to 85	°C
T_J	Operating Junction Temperature	-40 to 125	

1. Stresses above those listed in Absolute Maximum Ratings may cause permanent damage to the device. Functional operation at conditions other than the operating conditions specified is not implied. Only one Absolute Maximum Rating should be applied at any one time.
2. Based on long-term current density limitation.
3. Mounted on an FR4 board.
4. Derate 20mW/°C above 25°C.

Four-Channel LED Backlight Driver
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Electrical Characteristics[1]

V_{IN} = 12V; C_{IN} = 4.7µF, C_{OUT} = 2.2µF; C_{VCC} = 2.2µF; L_1 = 10µH; R_{SET} =7.5kΩ (I_{CSx} = 21mA); T_A = -40ºC to 85ºC unless otherwise noted. Typical values are at T_A = 25ºC.

Symbol	Description	Conditions	Min	Typ	Max	Units
Power Supply, Current Sinks						
V_{IN}	Input Voltage Range	V_{IN} = V_{CC}	4.5		5.5	V
		V_{CC} = Open	5.0		26.0	
V_{UVLO}	Under-Voltage Threshold	V_{IN} Rising			4.3	V
		Hysteresis		500		mV
		V_{IN} Falling	3.2			V
V_{CC}	V_{CC} Output Voltage	EN = Logic High, $I_{CC(OUT)}$ = 0mA	4.0	4.5	6	V
V_{CX}	Current Sink Voltage	EN = Logic High, I_{CSx} = 21mA (R_{SET} = 7.5kΩ)		0.5		V
I_Q	IN Quiescent Current (no switching)	I_{CSx} = 0%, V_{CSx} = 0.5V, EN = Logic High		1.5		mA
I_{SD}	IN Pin Shutdown Current	CS1-CS4 = Open, EN = Logic Low, does not include LX leakage current			40.0	µA
I_{CSx}	Current Sink Accuracy	I_{CSx} = 21mA	-5	±2	+5	%
$I_{CSx-Matching}$	Current Matching Between Any Sink Channel	I_{CSx} = 21mA	-3	±2	+3	%
V_{OVP}	Over-Voltage Threshold	V_{OUT} Rising	1.1	1.2	1.3	V
	Over-Voltage Hysteresis	V_{OUT} Falling		100		mV
$R_{DS(ON)LO}$	Low Side Switch On Resistance	V_{CC} = 4.5V		200		mΩ
D_{MAX}	Maximum Duty Cycle		90			%
T_{MIN}	Minimum On-Time			100		ns
I_{CSx}/I_{RSET}	Current Set Ratio	I_{CSx}/I_{RSET}, V_{RSET} = 0.6V		262		A/A
I_{LIMIT}	Low Side Switch Current Limit		3.0		6.5	A
I_{LEAK}	LX Pin Leakage	EN = Logic Low; V_{LX} = 40V			1	µA
	CSx Pin Leakage	EN = Logic Low; CSx = 30V			10	µA
F_{OSC}	Oscillator Frequency	FSET = Logic Low; V_{IN} = 5.0 to 26.0V; V_{IN} = V_{CC} = 4.5 to 5.5V	550	675	800	kHz
		FSET = Logic High; V_{IN} = 5.0 to 26.0V; V_{IN} = V_{CC} = 4.5 to 5.5V	1100	1300	1500	kHz
F_{SYNC}	Sync Frequency			F_{OSC} ±20%		kHz
F_{SYNC}	Sync Duty Cycle Range	F_{OSC} ± 20%	10		90	%
$F_{PWM(MAX)}$	Maximum Direct PWM Frequency			50	100	kHz
T_{SS}	Soft-Start Time	V_{OUT} = 35V, C_{COMP} = 18nF, R_{COMP} = 10kΩ		1		ms
Logic Level Inputs: EN Pin						
$V_{EN(L)}$	Threshold Low				0.4	V
$V_{EN(H)}$	Threshold High		2.5			V
I_{LEN}	Input Leakage Enable Pin	V_{EN} = 5V, V_{IN} = 5V			3	µA
Logic Level Inputs: SYNC, PWM, FSET Pins						
$V_{(H)}$	Threshold Low				0.4	V
$V_{(H)}$	Threshold High		1.4			V
I_{LK}	Input Leakage	V_{SYNC} = V_{PWM} = V_{FSET} = 5V	-1		1	µA
$T_{PWM(ON/OFF)}$	PWM Turn On/Off Delay	PWM transition to 95%/5% I_{RSET}		1		µs
Thermal Protection						
$T_{J(SD)}$	T_J Thermal Shutdown Threshold			140		°C
$T_{J(HYS)}$	T_J Thermal Shutdown Hysteresis			15		°C

1. The AAT1405 is guaranteed to meet performance specifications over the -40°C to +85°C operating temperature range and is assured by design, characterization, and correlation with statistical process controls.
2. Output voltage must result in a voltage lower than the LX maximum rating under all operating conditions.
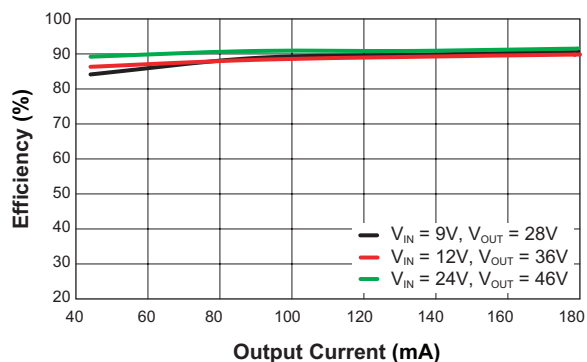
Typical Characteristics

Boost Efficiency vs. Input Voltage
(L = 10µH; V_{CC} = 4.7V; PWM = V_{CC}; F_{SET} = GND; I_{OUT} = 120mA)

Efficiency (%) vs. Input Voltage (V)
- V_{OUT} = 29V
- V_{OUT} = 36V
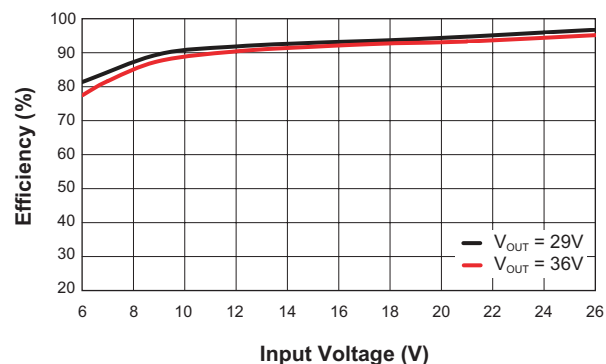
Boost Efficiency vs. Load Current
(L = 10µH; V_{CC} = 4.7V; PWM = V_{CC}; F_{SET} = GND)

Efficiency (%) vs. Output Current (mA)
- V_{IN} = 9V, V_{OUT} = 28V
- V_{IN} = 12V, V_{OUT} = 36V
- V_{IN} = 24V, V_{OUT} = 46V

Boost Efficiency vs. PWM Duty Cycle
(L = 10µH; V_{CC} = 4.7V; F_{SET} = GND)

Efficiency (%) vs. PWM Duty Cycle (%)
- V_{IN} = 9V, V_{OUT} = 29V
- V_{IN} = 12V, V_{OUT} = 36V
- V_{IN} = 24V, V_{OUT} = 45V
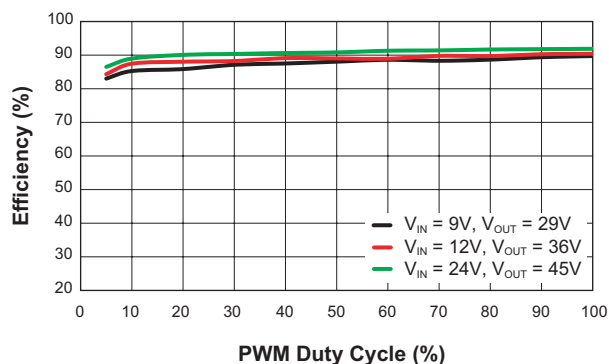
Output Current vs. PWM Duty Cycle
(V_{IN} = 12V; I_{CSX} = 20mA/ch)

Total Output Current (mA) vs. PWM Duty Cycle (%)
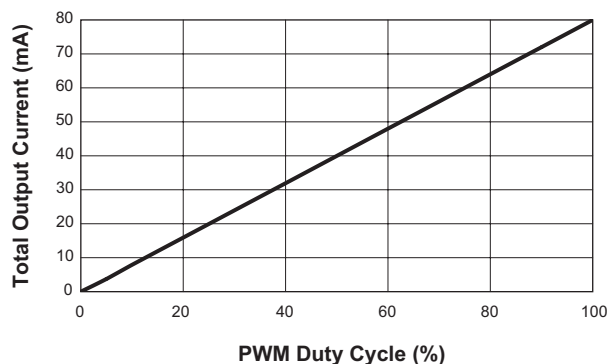
Low Side Switch Current Limit vs. Temperature
(V_{IN} = 5V to 26V)

Current Limit (A) vs. Temperature (°C)
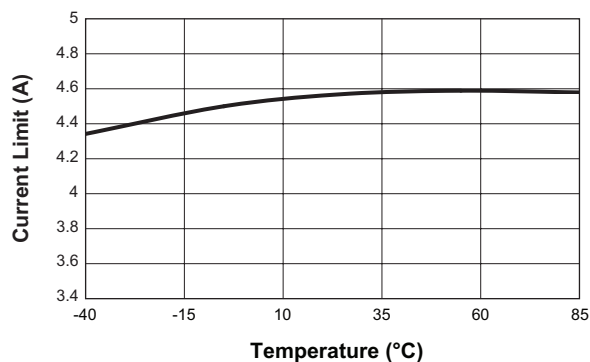
V_{CC} Line Regulation vs. Input Voltage
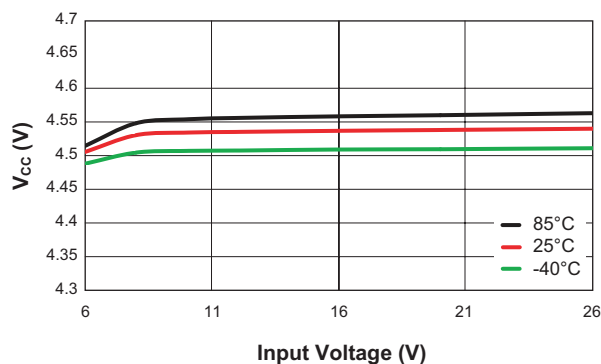
V_{CC} (V) vs. Input Voltage (V)
- 85°C
- 25°C
- -40°C

Four-Channel LED Backlight Driver
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Typical Characteristics

Quiescent Current vs. Input Voltage
(Non-switching; V_{CSX} = 0.5V; I_{CSX} = 0mA)
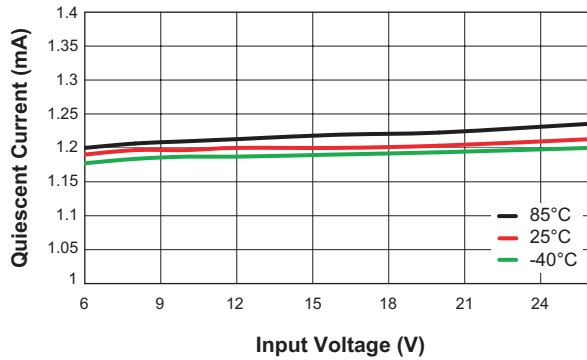
Shutdown Current vs. Input Voltage
(V_{LX} = V_{CSX} = Open)
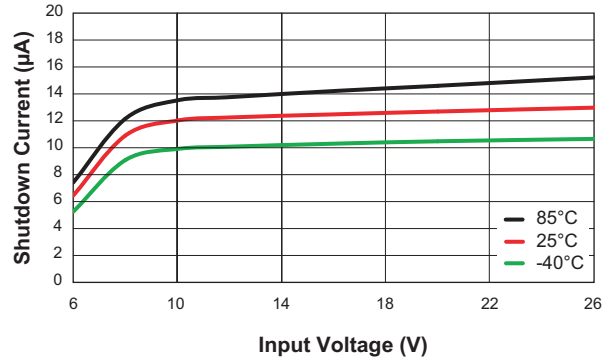
Switching Frequency vs. Temperature
(V_{IN} = 12V)
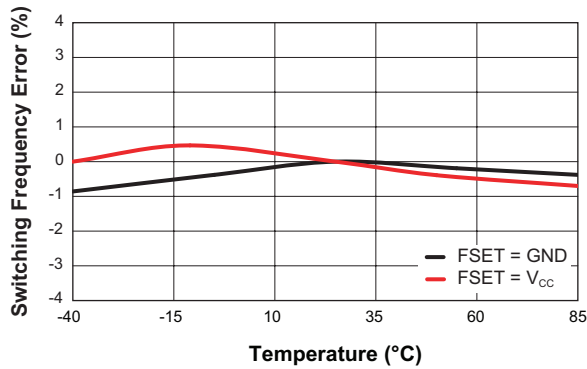
Over-Voltage Threshold vs Temperature
(V_{IN} = 12V)
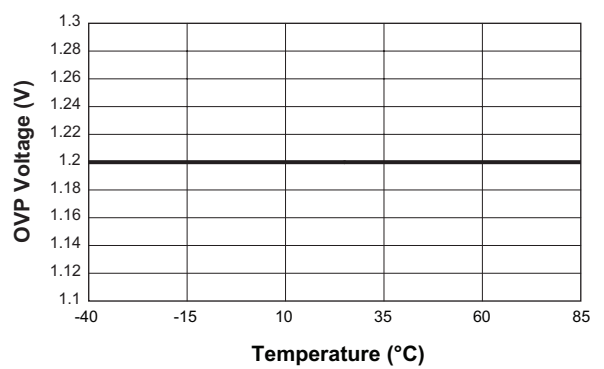
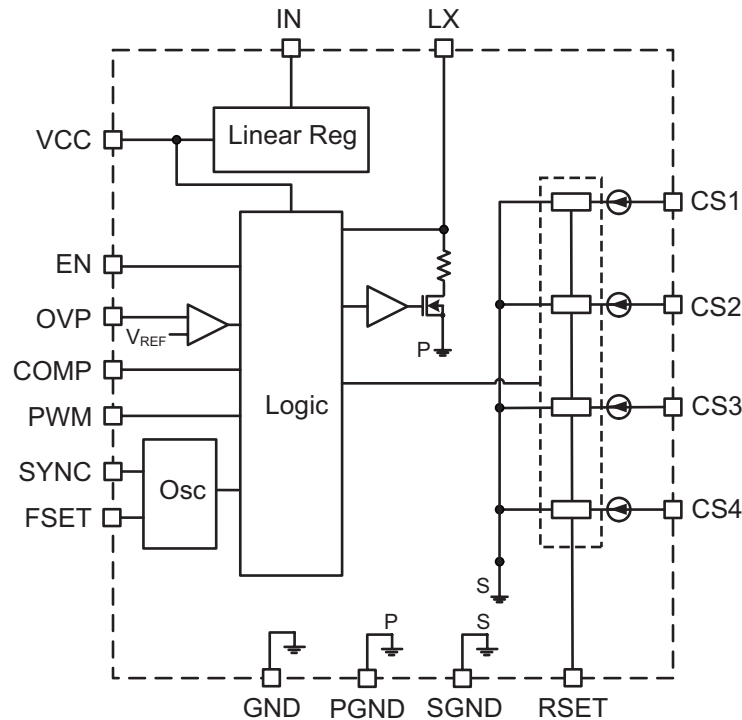**Current Sink Accuracy vs. Temperature**
(V_{IN} = 12V; V_{CSX} = 0.8V; I_{CSX} = 21mA/Channel)
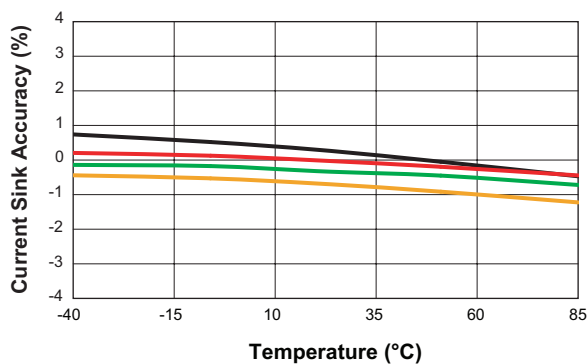
Current Sink Matching vs. Temperature
(V_{IN} = 12V; V_{CSX} = 0.8V; I_{CSX} = 21mA/Channel)
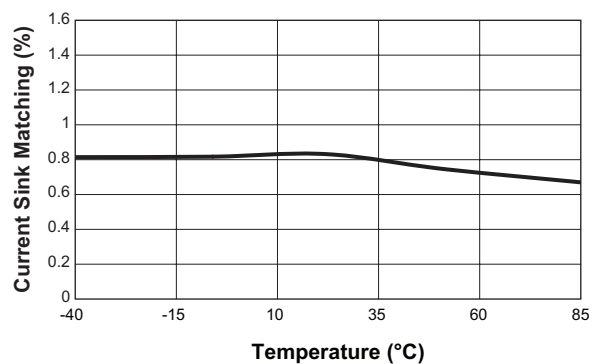
Four-Channel LED Backlight Driver
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Typical Characteristics

Startup
(V$_{IN}$ = 12V; V$_{OUT}$ = 37V; C$_{OUT}$ = 2.2µF;
L = 4.7µH; I$_{OUT}$ = 80mA)

EN/SET
(10V/div)

V$_{OUT}$
(20V/div)

12V

I$_{OUT}$
(50mA/div)

I$_{LX}$
(1A/div)

Time (100µs/div)

Switching Waveforms
(V$_{IN}$ = 12V; V$_{OUT}$ = 37V; C$_{OUT}$ = 2.2µF;
L = 4.7µH; I$_{OUT}$ = 80mA)

V$_{IN}$
(AC coupled)
(50mV/div)

V$_{OUT}$
(AC coupled)
(200mV/div)

I$_{LX}$
(1A/div)

Time (500ns/div)

PWM Switching Waveforms
(V$_{IN}$ = 12V; V$_{OUT}$ = 37V; C$_{OUT}$ = 2.2µF;
L = 4.7µH; I$_{OUT}$ = 80mA)

PWM
(5V/div)

V$_{OUT}$
(1V/div)

37V

I$_{OUT}$
(50mA/div)

I$_{LX}$
(1A/div)

Time (100µs/div)
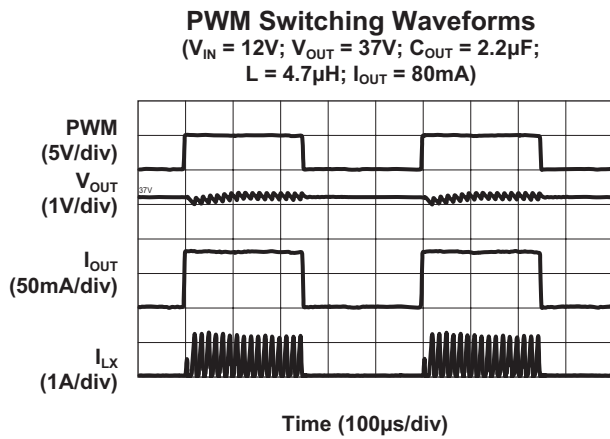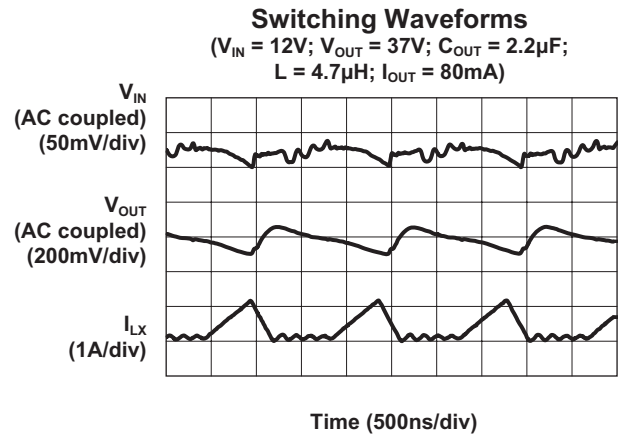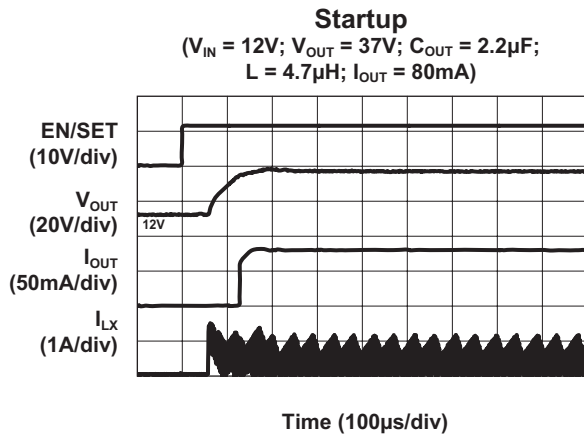
Functional Block Diagram

Functional Description

The AAT1405 is a highly integrated, high efficiency white LED backlight solution for notebook computers, monitors and portable TVs. The device operates from regulated DC inputs, cigarette lighter adapters, and multi-cell Li-ion batteries over a voltage range from 4.5V to 26V.

The integrated boost (step-up) converter provides up to 45V output to drive multiple strings of series LEDs. The maximum number of LEDs is dependent upon the forward voltage of each LED. Four precision current sinks can provide constant current drive for up to 44 white LEDs depending upon LED V_F. The LED current is set by a single external resistor up to 30mA per string for a total output current capability of 120mA. The controller derives output feedback from the channel with the lowest current sink voltage while maintaining the programmed current in each LED string. This ensures the lowest possible output voltage, highest efficiency and continuous operation with mismatched LED strings.

The AAT1405 is designed for maximum flexibility. The LED strings may be disabled or used in parallel for increased current capability. Thus, the AAT1405 allows operation with fewer than 4 LED strings with the maximum number of LEDs per channel set by the LED V_F and the output voltage of the boost converter. A lower number of LEDs can also be used. Unused current sinks are disabled by tying them to ground. The unique high voltage current sinks support non-matching LED strings (LED quantity, type, etc.). For high current applications, such as high-brightness LEDs, multiple current sinks may be connected in parallel providing up to 120mA per LED string.

The AAT1405 supports a high input PWM frequency to help eliminate potential audio emissions caused by harmonic/sub-harmonic resonance of the power stage. The PWM dimming range is up to 1,000:1 (see Figure 1).

The output voltage is regulated by the string with the highest voltage requirement, allowing a wide range of LED characteristics. The boost switching frequency is adjustable up to 1300kHz for optimum efficiency and the smallest external filtering components. Alternatively, the device may be synchronized to an external clock.

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Current mode control provides fast response to line and load transients. Integrated light load mode ensures highest efficiency across the entire input voltage and load range.

Fault tolerant circuitry extends system life by disabling open LED strings. The high voltage current sinks maintain normal operation with non-matched strings while also preventing damage due to shorted LEDs.

The AAT1405 is available in a Pb-free, thermally enhanced 24-pin 3x4 TQFN package.

PWM Dimming

The AAT1405 provides direct PWM dimming. After initial power-up or when EN is cycled, the device is enabled with brightness controlled by PWM duty cycle and the R_{SET} resistor value.

The ultra-fast 1µs turn-on and turn-off time of the boost regulator and current sinks ensures high performance and excellent dimming range in applications requiring high frequency PWM dimming. The high PWM dimming frequency eliminates audio interference. The integrated current sinks ensure good timing between strings (PWM matching) while the fast response yields a linear PWM duty-cycle versus LED current characteristic. PWM inputs from 100Hz to 100kHz are recommended.

AAT1405 Dimming Range
vs. PWM Pin Frequency
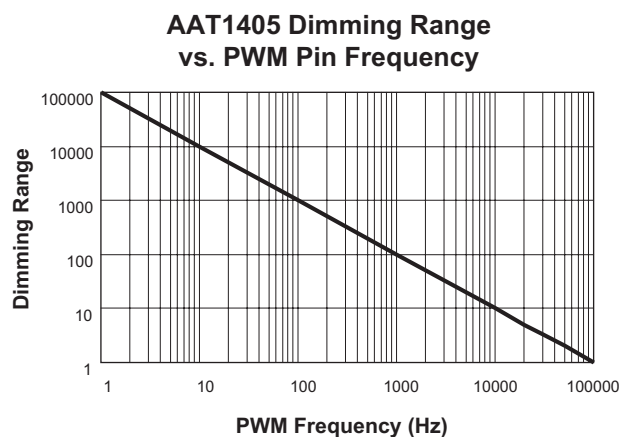
Figure 1: PWM Input Frequency
vs. Dimming Range.

Fault Tolerant Operation

The AAT1405 device is protected from faults arising from LED opens and/or shorts.

An LED open condition will be detected by the controller at startup and during normal operation. The low voltage on the current sink is detected by the controller, which disables the feedback to the boost converter from that current sink. The remaining LED strings to continue to operate normally. The controller re-enables the current sink feedback if the LED open condition is removed during a power or EN cycle. This feature extends backlight life and reliability, which is otherwise limited by intermittent conditions in the LED string(s) and/or circuit board interconnections.

Under all conditions, the over-voltage protection circuitry prevents the switching node (LX) from exceeding the maximum operating voltage prior to disabling the current sink. Over-voltage protection (OVP) disables boost switching while maintaining the programmed LED current. Boost switching is re-enabled when OVP hysteresis is satisfied.

A LED short condition results in a higher voltage appearing on the affected channels' current-sink pin. The affected current sink automatically compensates for the additional voltage. The current sink can withstand a high voltage indefinitely. However, the increased voltage across the current sink causes an increase in power dissipation. The channel will continue to operate until the over-temperature protection activates.

Integrated over-current protection is provided. Over-current protection prevents inductor saturation and any resulting damage to the switching device occurring during an overload fault condition.

Application Information

OVP Protection with Open LED Failure

The AAT1405's OVP protection consists of a resistive divider network (R3 and R4) as shown in Figure 2. The resistor divider must be selected so that the voltage at the OVP pin exceeds the OVP rising threshold when the output is at $V_{OUT(MAX)}$.

$$R3 = R4 \cdot \left(\frac{V_{OUT(MAX)}}{V_{OVP(MAX)}} - 1 \right)$$

When the OVP rising threshold is exceeded, the converter stops switching. The open LED channel is removed from the boost converter feedback loop. When the voltage at the OVP pin falls below the OVP hysteresis voltage, the boost converter can resume switching.

It is important that during normal operation the current sinks are given enough headroom so that the OVP threshold is not tripped.

The output voltage at the minimum OVP threshold is

$$V_{OUT(MIN)} = V_{OVP(MIN)} \cdot \left(\frac{R_3}{R_4} + 1 \right)$$

The maximum voltage of each LED string including the current sink headroom should not exceed $V_{OUT(MIN)}$.

$$V_{OUT(MIN)} > V_{CSX} + N \cdot V_{FLED(MAX)}$$

$$V_{OVP(MIN)} \cdot \left(\frac{R_3}{R_4} + 1 \right) > V_{CSX} + N \cdot V_{FLED(MAX)}$$

$$R_3 > R_4 \cdot \left(\frac{V_{CSX} + N \cdot V_{FLED(MAX)} - V_{OVP(MIN)}}{V_{OVP(MIN)}} \right)$$

Where:

N is the number of LED in each string.
$V_{OVP(MIN)} = 1.1V$ is the minimum over-voltage threshold.
$V_{CSX} = 0.5V$ is the current sink voltage.
$V_{FLED(MAX)}$ is the maximum forward LED voltage at 20mA.

Another factor in setting the OVP voltage using the resistive divider is that the maximum voltage at the LX pin should not exceed $V_{LX(MAX)} = 50V$.

$$V_{LX(MAX)} = V_{OUT(MAX)} + V_{D1} + V_{RING}$$

V_{D1} is the forward voltage of the Schottky diode D1. V_{RING} is the voltage spike at LX node caused by the delay of D1 at turn on.

Measurements should confirm that the maximum switching node voltage $V_{LX(MAX)}$ is less than 50V under worst case conditions.
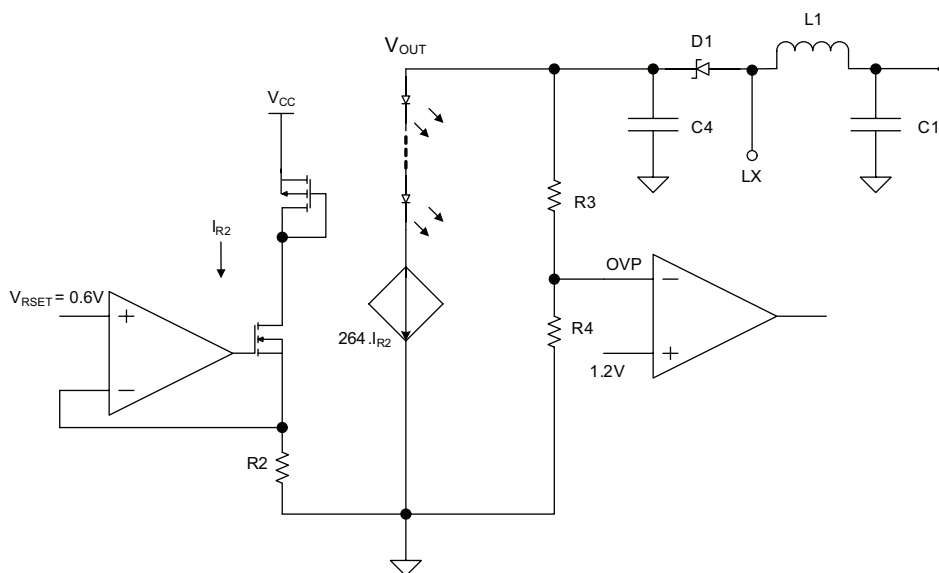
Figure 2: Over-Voltage Protection and Current Sink Setting Circuit.

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For example, if the number of white LEDs in each string is N =11, the resistor divider R3 can be calculated by selecting R4 = 12.1kΩ:

$$R_3 > 12.1k\Omega \cdot \left(\frac{0.5V + 11 \cdot 3.7V - 1.1}{1.1}\right) = 441.1k\Omega$$

choose R3 = 442kΩ.

The maximum output voltage with the selected values of R4, R3 is

$$V_{OUT(MAX)} = V_{OVP(MAX)} \cdot \left(\frac{R_3}{R_4} + 1\right) = 1.3V \cdot \left(\frac{442k\Omega}{12.1k\Omega} + 1\right) = 48.8V$$

LED Current Sink Setting

The current sink is controlled by the RSET voltage (0.6V) and the R_{SET} resistor (R2). For maximum accuracy, a 1% tolerance resistor is recommended.

The R_{SET} resistor (R2) value can be calculated as follows:

$$R_2 = \frac{262 \cdot 0.6V}{I_{CSX(MAX)}}$$

Where $V_{RSET} = 0.6V$.

For example, if the maximum current for each string of LEDs is 30mA, this corresponds to a minimum resistor value of 5.23kΩ.

$$R_2 = \frac{262 \cdot 0.6V}{30mA} = 5.23k\Omega$$

Maximum I_{LED} Current (mA)	R2 (kΩ)
30	5.23
25	6.19
20	7.87
15	10.5
10	15.8
5	31.6

**Table 2: Maximum LED Current Sink
vs. R_{SET} Resistor (R2) Values.**

Schottky Diode Selection

To ensure minimum forward voltage drop, high voltage Schottky diodes are considered the best choice for the white LED boost converter. The output diode is sized to maintain acceptable efficiency and reasonable operating junction temperature under full load operating conditions. Forward voltage (V_F) and package thermal resistance (θ_{JA}) are the dominant factors to consider in selecting a diode. The diode non-repetitive peak forward surge current rating (I_{FSM}) should be considered for high pulsed load applications. I_{FSM} rating drops with increasing conduction period. Manufacturers' datasheets should be consulted to verify reliability under peak loading conditions. The diode's published current rating may not reflect actual operating conditions and should be used only as a comparative measure between similarly rated devices.

During the on-time, the output voltage on the output cap is applied to the cathode of the external Schottky diode. The rectifier's reverse breakdown voltage rating should be greater than the maximum output voltage rating of the Boost. 40V rated Schottky diodes are recommended for outputs less than 30V, while 60V rated Schottky diodes are recommended for outputs greater than 35V.

The approximate power loss on the Schottky diode can be calculated:

$$I_{AVG} = I_{OUT}$$

The average output current multiplied by the forward diode voltage determines the loss of the output diode.

$$P_{LOSS-DIODE} = I_{AVG} \cdot V_F = I_{OUT} \cdot V_F$$

Diode junction temperature can be estimated.

$$T_J = T_{AMB} + \theta_{JA} \cdot P_{LOSS-DIODE}$$

Output diode junction temperature should be maintained below 110°C, but may vary depending on application and/or system guidelines. The diode θ_{JA} can be minimized with additional PCB area on the cathode. PCB heat-sinking the anode may degrade EMI performance. The reverse leakage current of the rectifier must be considered to maintain low quiescent (input) current and high efficiency under light load. The rectifier reversed current increases dramatically at elevated temperatures.

Inductor Selection

The white LED boost (step-up) converter is designed to operate with an inductor with a minimum value of 4.7μH for all input and output voltage combinations. The inductor saturation current rating should be greater than the NMOS current at maximum duty cycle.

$$D_{MAX} = \frac{(V_{OUT} + V_F - V_{IN(MIN)})}{V_{OUT} + V_F}$$

The inductor (L) is selected to avoid saturation at minimum input voltage, maximum output load conditions. Peak current may be calculated from the following equation, again assuming continuous conduction mode. Worst-case peak current occurs at minimum input voltage (maximum duty cycle) and maximum load.

$$I_{PEAK} = \frac{I_{OUT}}{1 - D_{MAX}} + \frac{D_{MAX} \cdot V_{IN(MIN)}}{2 \cdot F_S \cdot L}$$

Output Capacitor

The high output ripple inherent in the boost converter necessitates low impedance output filtering.

Multi-layer ceramic (MLC) capacitors provide small size and adequate capacitance, low parasitic equivalent series resistance (ESR) and equivalent series inductance (ESL), and are well suited for use with the white LED boost regulator. MLC capacitors of type X7R or X5R are recommended to ensure good capacitance stability over the full operating temperature range.

The output capacitor is sized to maintain the output load without significant voltage droop (ΔV_{OUT}) during the power switch ON interval, when the output diode is not conducting. A ceramic output capacitor from 2.2μF to 4.7μF is recommended. Typically, 50V rated capacitors are required for the 42V maximum boost output. Ceramic capacitors sized as small as 0805 or 1206 are available which meet these requirements.

MLC capacitors exhibit significant capacitance reduction with applied voltage. Output ripple measurements should confirm that output voltage droop and operating stability are acceptable. Voltage derating can minimize this factor, but results may vary with package size and among specific manufacturers.

The output capacitor size can be estimated using the equation:

$$C_{OUT} = \frac{I_{OUT} \cdot D_{MAX}}{F_S \cdot \Delta V_{OUT}}$$

To maintain stable operation at full load, the output capacitor should be sized to maintain ΔV_{OUT} between 100mV and 200mV.

The WLED boost converter input current flows during both ON and OFF switching intervals. The input ripple current is less than the output ripple and, as a result, less input capacitance is required.

Compensation Component Selection

The AAT1405 Boost architecture uses peak current mode control to eliminate the double pole effect of the output L&C filter and simplifies the compensation loop design. The current mode control architecture simplifies the transfer function of the control loop to be a one-pole, one left plane zero and one right half plane (RHP) system in frequency domain. The dominant pole can be calculated by:

$$f_P = \frac{1}{2\pi \cdot R_0 \cdot C_4}$$

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The ESR zero of the output capacitor (see Figure 3) can be calculated by:

$$f_{Z\_ESR} = \frac{1}{2\pi \cdot R_{ESR} \cdot C_4}$$

Where:

C_4 is the output filter capacitor
R_O is the equivalent load resistor value
R_{ESR} is the equivalent series resistance of the output capacitor.

The right half plane (RHP) zero can be calculated by:

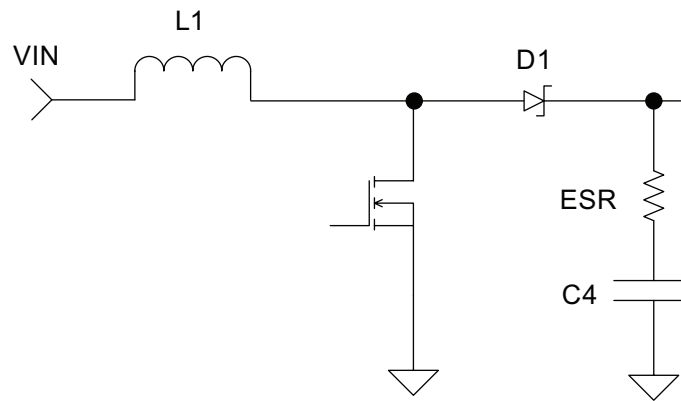$$f_{Z\_ESR} = \frac{V_{IN}^2}{2\pi \cdot L_1 \cdot I_{OUT} \cdot V_{OUT}}$$

Figure 3: AAT1405 Equivalent Output Stage.

It is recommended to design the bandwidth to one decade lower than the frequency of RHP zero to guarantee the loop stability. A series capacitor and resistor network (R1 and C3) connected to the COMP pin sets the pole and zero which are given by:

$$f_{P\_COM} = \frac{1}{2\pi \cdot R_{EA} \cdot C_3}$$

$$f_{Z\_COM} = \frac{1}{2\pi \cdot R_1 \cdot C_3}$$

Where:

C_3 is the compensation capacitor
R_1 is the compensation resistor
R_{EA} is the output resistance of the error amplifier (2.97MΩ).

A 15nF (C3) capacitor and a 5kΩ (R1) resistor in series are chosen for optimum phase margin and fast transient response.

AAT1405

Four-Channel LED Backlight Driver
with Integrated Boost and High Frequency Direct PWM Dimming
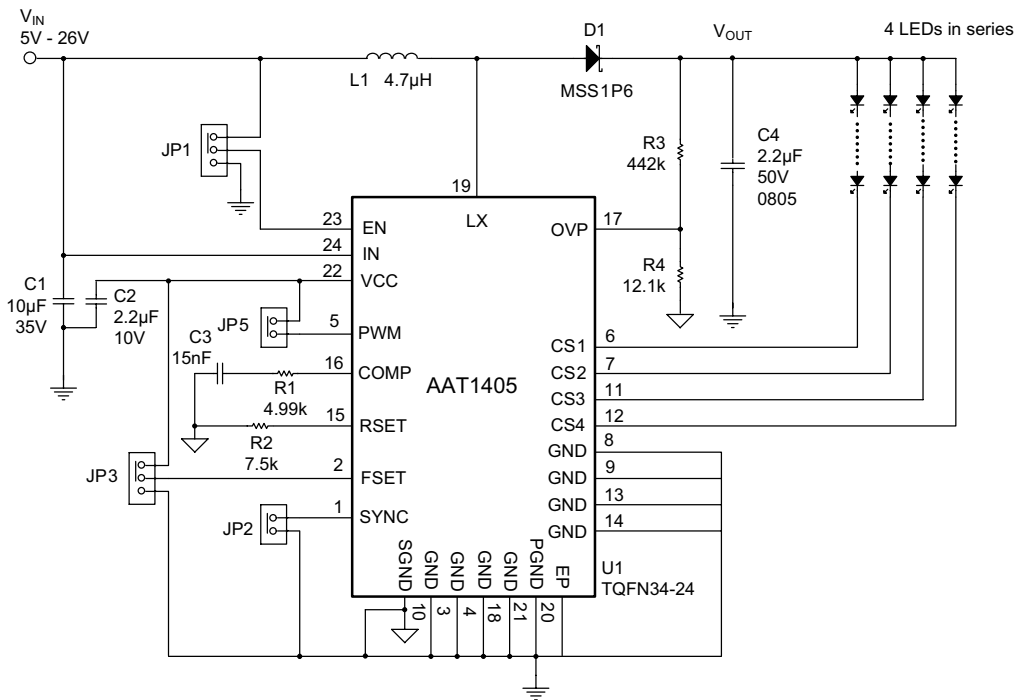
L1   Sumida, CDRH5D28RHPNP-4R7N, 4.7µH, 3.7A, DCR = 43.1mΩ
D1   Vishay, Schottky Barrier Diode, MSS1P6, 1A, 60V
C1   Taiyo Yuden, GMK325BJ106KN-T, 10µF, 35V, X5R,1210; OR Murata GRM32ER71H106K, 10µF, 50V, X7R, 1210
C2   2.2µF, 10V, 0603
C3   15nF, 10V, 0603
C4   Murata, GRM31CR71H225KA88L, 2.2µF, 50V, X7R, 1206
R1, R2, R3, R4 Carbon Film resistors, 1%, 0603

Figure 4: AAT1405IMK Evaluation Board Schematic.
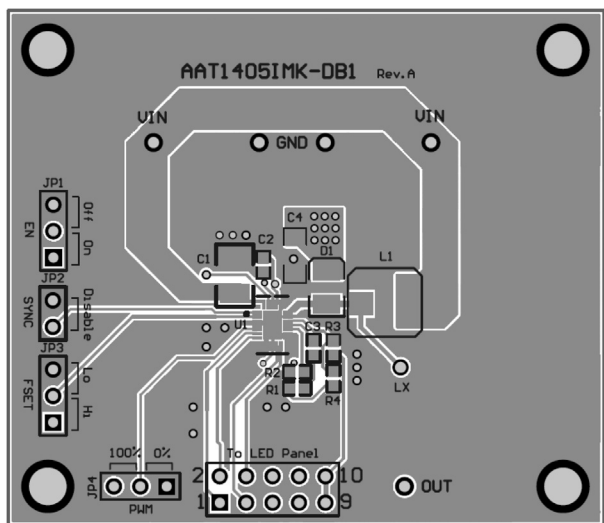
**Figure 5: AAT1405IMK Evaluation Board
Top Side Layout.**
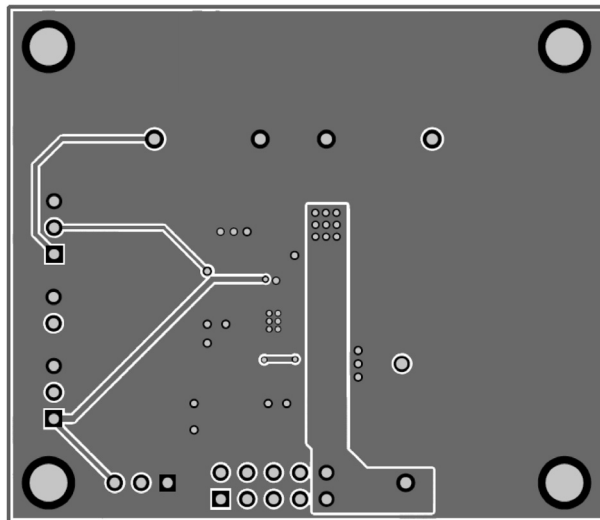
**Figure 6: AAT1405IMK Evaluation Board
Bottom Side Layout.**

Four-Channel LED Backlight Driver
with Integrated Boost and High Frequency Direct PWM Dimming

Ordering Information

Package	Marking[1]	Part Number (Tape and Reel)[2]
TQFN34-24	K3XYY	**AAT1405IMK-T1**

Skyworks Green™ products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green™*, document number SQ04-0074.

Package Information

TQFN34-24[3]

Top View

Bottom View

Detail "A"

Side View
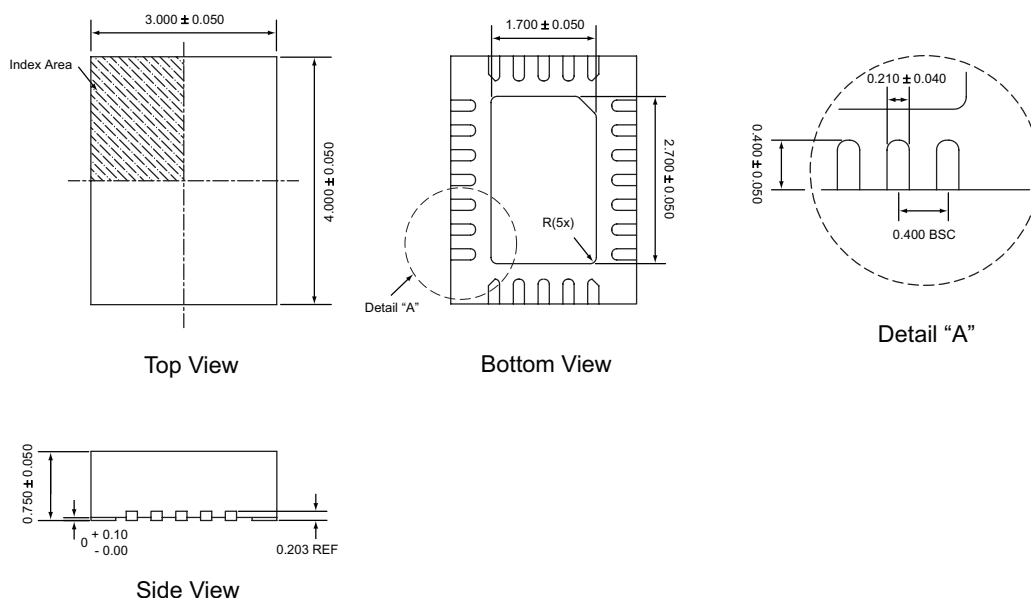
All dimensions in millimeters.

1. XYY = assembly and date code.
2. Sample stock is generally held on part numbers listed in **BOLD**.
3. The leadless package family, which includes QFN, TQFN, DFN, TDFN and STDFN, has exposed copper (unplated) at the end of the lead terminals due to the manufacturing process. A solder fillet at the exposed copper edge cannot be guaranteed and is not required to ensure a proper bottom solder connection.

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