



10 Ω , Quad, SPST, CMOS Analog Switches

General Description

Maxim's MAX312/MAX313/MAX314 analog switches feature low on-resistance (10 Ω max) and 1.5 Ω on-resistance matching between channels. These switches conduct equally well in either direction. They offer low leakage over temperature (2.5nA at +85°C). Low power consumption and ESD tolerance greater than 2000V per Method 3015.7 are guaranteed.

The MAX312/MAX313/MAX314 are quad, single-pole/single-throw (SPST) analog switches. The MAX312 is normally closed (NC), and the MAX313 is normally open (NO). The MAX314 has two NC switches and two NO switches. All three devices operate from a single supply of +4.5V to +30V or from dual supplies of $\pm$ 4.5V to $\pm$ 20V.

Applications

Test Equipment
Communication Systems
PBX, PABX Systems
Audio Signal Routing
Avionics
Sample-and-Hold Circuits
Data Acquisition Systems

Rail-to-Rail is a registered trademark of Nippon Motorola Ltd.

Features

- ◆ Pin Compatible with DG411/DG412/DG413
- ◆ Low On-Resistance (6.5 Ω typical)
- ◆ Guaranteed R_{ON} Match Between Channels (1.5 Ω max)
- ◆ Guaranteed R_{ON} Flatness over Specified Signal Range (2 Ω max)
- ◆ Guaranteed ESD Protection > 2000V per Method 3015.7
- ◆ Crosstalk > 96dB at 20kHz
- ◆ Single-Supply Operation: +4.5V to +30V
Dual-Supply Operation: $\pm$ 4.5V to $\pm$ 20V
- ◆ Rail-to-Rail® Signal Handling

Ordering Information

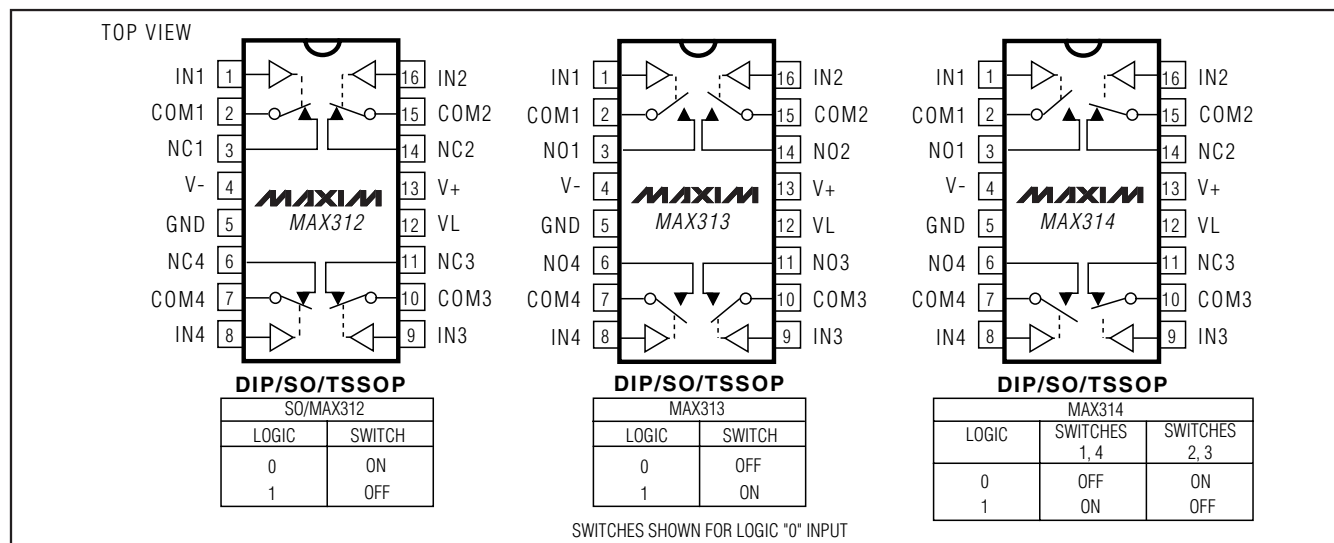
PART	TEMP RANGE	PIN-PACKAGE
MAX312CPE	0°C to +70°C	16 Plastic DIP
MAX312CSE	0°C to +70°C	16 Narrow SO
MAX312CUE	0°C to +70°C	16 TSSOP
MAX312C/D	0°C to +70°C	Dice*
MAX312EPE	-40°C to +85°C	16 Plastic DIP
MAX312ESE	-40°C to +85°C	16 Narrow SO
MAX312EUE	-40°C to +85°C	16 TSSOP
MAX312MJE	-55°C to +125°C	16 CERDIP**

Ordering Information continued at end of data sheet.

* Contact factory for dice specifications.

**Contact factory for availability.

Pin Configurations/Functional Diagrams/Truth Tables



10 Ω , Quad, SPST, CMOS Analog Switches

ABSOLUTE MAXIMUM RATINGS

Voltages Referenced to GND

V+-0.3V to +44V

V-+0.3V to -44V

V+ to V-.....-0.3V to +44V

VL(GND - 0.3V) to (V+ + 0.3V)

All Other Pins (Note 1)(V- - 2V) to (V+ + 2V)

or 30mA (whichever occurs first)

Continuous Current (COM₋, NO₋, NC₋)..... $\pm$ 100mA

Peak Current (COM₋, NO₋, NC₋)..... $\pm$ 300mA

Continuous Power Dissipation (T_A = +70°C)

Plastic DIP (derate 10.53mW/°C above +70°C)842mW

Narrow SO (derate 8.70mW/°C above +70°C)696mW

CERDIP (derate 10.00mW/°C above +70°C)800mW

TSSOP (derate 6.7mW/°C above +70°C)457mW

Operating Temperature Ranges

MAX31_C_0°C to +70°C

MAX31_E_-40°C to +85°C

MAX31_M_.....-55°C to +125°C

Storage Temperature Range-65°C to +150°C

Lead Temperature (soldering, 10s)+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Note 1: Signals on NC₋, NO₋, COM₋, or IN₋ exceeding V+ or V- will be clamped by internal diodes. Limit forward diode current to maximum current rating.

ELECTRICAL CHARACTERISTICS—Dual Supplies

(V+ = 15V, V- = -15V, VL = 5V, GND = 0V, VINH = 2.4V, VINL = 0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS			MIN	TYP (Note 2)	MAX	UNITS
ANALOG SWITCH								
Analog Signal Range	V _{COM-} , V _{NO-} , V _{NC-}	(Note 3)			V-		V+	V
On-Resistance	R _{ON}	I _{COM} = 10mA, V _{NO-} or V _{NC-} = ±10V	T _A = +25°C	C, E	6.5		10	Ω
				M			9	
			T _A = T _{MIN} to T _{MAX}				15	
On-Resistance Match Between Channels (Note 4)	ΔR _{ON}	I _{COM} = 10mA, V _{NO-} or V _{NC-} = ±10V	T _A = +25°C		0.3		1.5	Ω
			T _A = T _{MIN} to T _{MAX}			3		
On-Resistance Flatness (Note 5)	R _{FLAT(ON)}	I _{COM} = 10mA, V _{NO-} or V _{NC-} = -5V, 0V, 5V	T _A = +25°C		0.2		2	Ω
			T _A = T _{MIN} to T _{MAX}			4		
Off Leakage Current (NO ₋ or NC ₋) (Note 6)	I _{NO} I _{NC}	V _{COM} = ±10V, V _{NO-} or V _{NC-} = ±10V	T _A = +25°C		-0.5	-0.02	0.5	nA
			T _A = T _{MIN} to T _{MAX}	C, E	-2.5		2.5	
				M	-40		40	
COM Off Leakage Current (Note 6)	I _{NC(OFF)}	V _{COM} = ±10V, V _{NO-} or V _{NC-} = ±10V	T _A = +25°C		-0.5	-0.02	0.5	nA
			T _A = T _{MIN} to T _{MAX}	C, E	-2.5		2.5	
				M	-40		40	
COM On Leakage Current (Note 6)	I _{COM(ON)}	V _{COM} = ±10V, V _{NO-} or V _{NC-} = ±10V	T _A = +25°C		-1	-0.04	1	nA
			T _A = T _{MIN} to T _{MAX}	C, E	-5		5	
				M	-100		100	

10 Ω , Quad, SPST, CMOS Analog Switches

ELECTRICAL CHARACTERISTICS—Dual Supplies (continued)

(V₊ = 15V, V₋ = -15V, V_L = 5V, GND = 0V, V_{INH} = 2.4V, V_{INL} = 0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP (Note 2)	MAX	UNITS
LOGIC INPUT							
Input Current with Input Voltage High	I _{INH}	IN ₋ = 2.4V, all others = 0.8V		-0.500	0.005	0.500	μA
Input Current with Input Voltage Low	I _{INL}	IN ₋ = 0.8V, all others = 2.4V		-0.500	0.005	0.500	μA
POWER SUPPLY							
Power-Supply Range				±4.5		±20.0	V
Positive Supply Current	I ₊	All channels on or off, V _{IN} = 0V or 5V, V ₊ = 16.5V V ₋ = -16.5V	T _A = +25°C	-1	0.0001	1	μA
			T _A = T _{MIN} to T _{MAX}	-5		5	
Negative Supply Current	I ₋	All channels on or off, V _{IN} = 0V or 5V, V ₊ = 16.5V V ₋ = -16.5V	T _A = +25°C	-1	0.0001	1	μA
			T _A = T _{MIN} to T _{MAX}	-5		5	
Logic Supply Current	I _L	All channels on or off, V _{IN} = 0V or 5V, V ₊ = 16.5V V ₋ = -16.5V	T _A = +25°C	-1	0.0001	1	μA
			T _A = T _{MIN} to T _{MAX}	-5		5	
Ground Current	I _{GND}	All channels on or off, V _{IN} = 0V or 5V, V ₊ = 16.5V V ₋ = -16.5V	T _A = +25°C	-1	-0.0001	1	μA
			T _A = T _{MIN} to T _{MAX}	-5		5	
DYNAMIC							
Turn-On Time	t _{ON}	Figure 2, V _{COM} = ±10V	T _A = +25°C	70	225	ns	
			T _A = T _{MIN} to T _{MAX}		275		
Turn-Off Time	t _{OFF}	Figure 2, V _{COM} = ±10V	T _A = +25°C	65	185	ns	
			T _A = T _{MIN} to T _{MAX}		235		
Break-Before-Make Time Delay	t _D	MAX314 only, Figure 3, R _L = 300Ω, C _L = 35pF	T _A = +25°C	1	5	ns	
Charge Injection (Note 3)	V _{CTE}	C _L = 1.0nF V _{GEN} = 0V, R _{GEN} = 0Ω, Figure 4	T _A = +25°C	-30	20	30	pC
Off Isolation (Note 7)	V _{ISO}	R _L = 50Ω, C _L = 5pF, f = 1MHz, Figure 5	T _A = +25°C		-65		dB
Crosstalk (Note 8)	V _{CT}	R _L = 50Ω, C _L = 5pF, f = 1MHz, Figure 6	T _A = +25°C		-85		dB
NC or NO Capacitance	C _(OFF)	f = 1MHz, Figure 7	T _A = +25°C		15		pF
COM Off Capacitance	C _(COM)	f = 1MHz, Figure 7	T _A = +25°C		15		pF
On Capacitance	C _(COM)	f = 1MHz, Figure 7	T _A = +25°C		47		pF

10 Ω , Quad, SPST, CMOS Analog Switches

ELECTRICAL CHARACTERISTICS—Single Supply

(V+ = 12V, V- = 0V, VL = 5V, GND = 0V, VINH = 2.4V, VINL = 0.8V, TA = TMIN to TMAX, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP (Note 2)	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V _{COM-} , V _{NO-} , V _{NC-}	(Note 3)		0		V+	V
Channel On-Resistance	R _{ON}	I _{COM} = 10mA, V _{NC-} or V _{NO-} +10V	T _A = +25°C	12.5	25		Ω
			T _A = T _{MIN} to T _{MAX}		35		
POWER SUPPLY							
Positive Supply Current	I ₊	V ₊ = 13.2V all channels on or off, V _{IN} = 0V or 5V	T _A = +25°C	-1	0.0001	1	μA
			T _A = T _{MAX}	-5		5	
Logic Supply Current	I _L	V _L = 5.5V all channels on or off, V _{IN} = 0V or 5V	T _A = +25°C	-1	0.0001	1	μA
			T _A = T _{MAX}	-5		5	
Ground Current	I _{GND}	V _L = 5.5V all channels on or off, V _{IN} = 0V or 5V	T _A = +25°C	-1	-0.0001	1	μA
			T _A = T _{MAX}	-5		5	
DYNAMIC							
Turn-On Time (Note 3)	t _{ON}	Figure 2, V _{NO-} or V _{NC-} = 8V	T _A = +25°C	100	325		ns
			T _A = T _{MIN} to T _{MAX}		425		
Turn-Off Time (Note 3)	t _{OFF}	Figure 2, V _{NO-} or V _{NC-} = 8V	T _A = +25°C	95	175		ns
			T _A = T _{MIN} to T _{MAX}		225		
Break-Before-Make Time Delay (Note 3)	t _D	MAX314 only, Figure 3 R _L = 300Ω, C _L = 35pF	T _A = +25°C	5			ns
Charge Injection (Note 3)	V _{CTE}	Figure 4, C _L = 1.0nF, V _{GEN} = 0V, R _{GEN} = 0V	T _A = +25°C	-5			pC

Note 2: The algebraic convention, where the most negative value is a minimum and the most positive value a maximum, is used in this data sheet.

Note 3: Guaranteed by design.

Note 4: $\Delta R_{ON} = \Delta R_{ON\ max} - \Delta R_{ON\ min}$.

Note 5: Flatness is defined as the difference between the maximum and minimum value of on-resistance as measured over the specified analog signal range.

Note 6: Leakage parameters are 100% tested at maximum-rated hot temperature and guaranteed by correlation at +25°C.

Note 7: Off isolation = $20\log_{10} [V_{COM} / (V_{NC} \text{ or } V_{NO})]$, VCOM = output, VNC or VNO = input to off switch.

Note 8: Between any two switches.

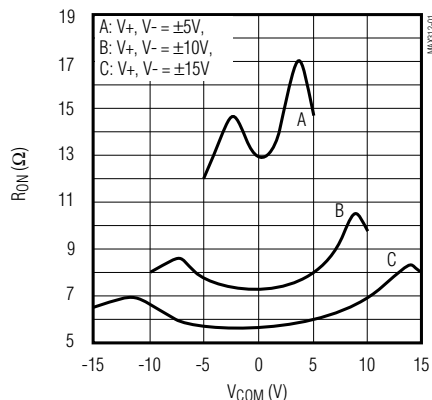
Note 9: Leakage testing at single supply is guaranteed by testing with dual supplies.

10 Ω , Quad, SPST, CMOS Analog Switches

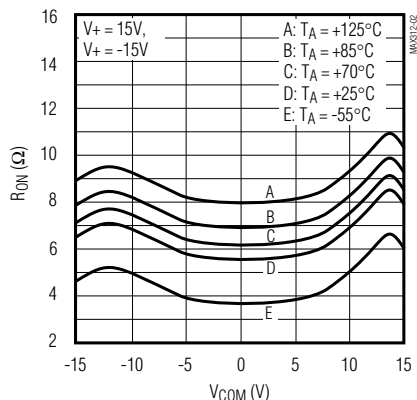
Typical Operating Characteristics

($T_A = +25^\circ\text{C}$, unless otherwise noted.)

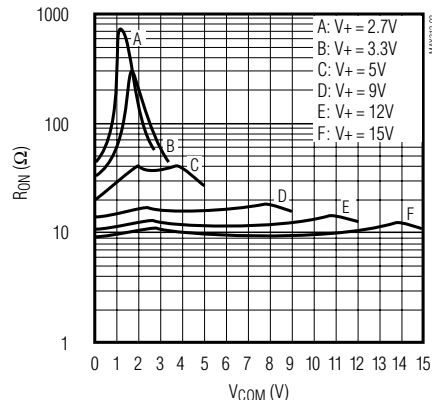
**ON-RESISTANCE vs. V_{COM}
(BIPOLAR SUPPLIES)**



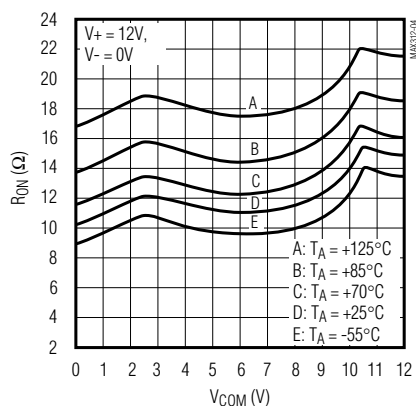
**ON-RESISTANCE vs. V_{COM} AND
TEMPERATURE**



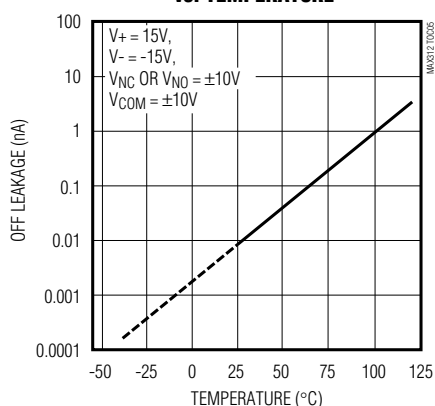
**ON-RESISTANCE vs. V_{COM}
(SINGLE SUPPLY, $V_- = 0\text{V}$)**



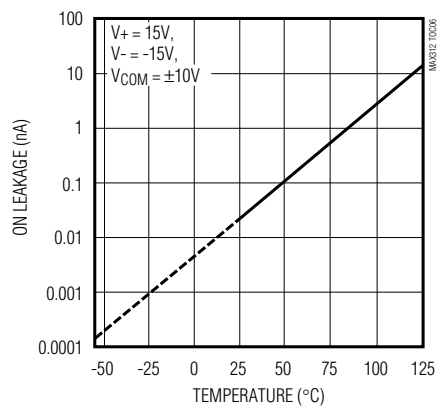
**ON-RESISTANCE vs. V_{COM} AND
TEMPERATURE**



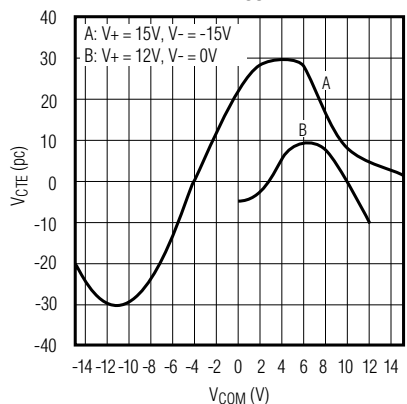
**OFF LEAKAGE CURRENT
vs. TEMPERATURE**



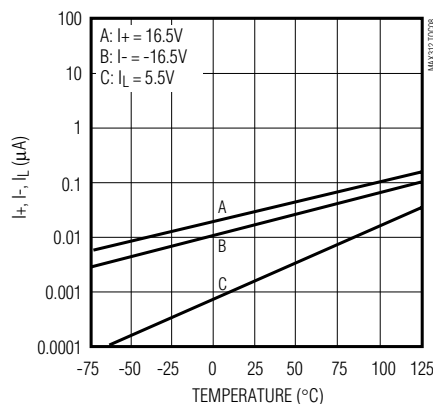
**ON LEAKAGE CURRENT
vs. TEMPERATURE**



**CHARGE INJECTION
vs. V_{COM}**



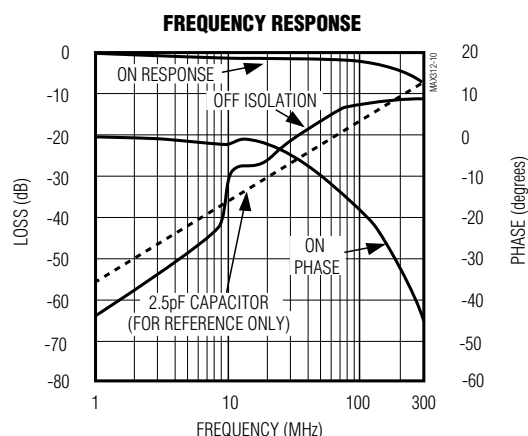
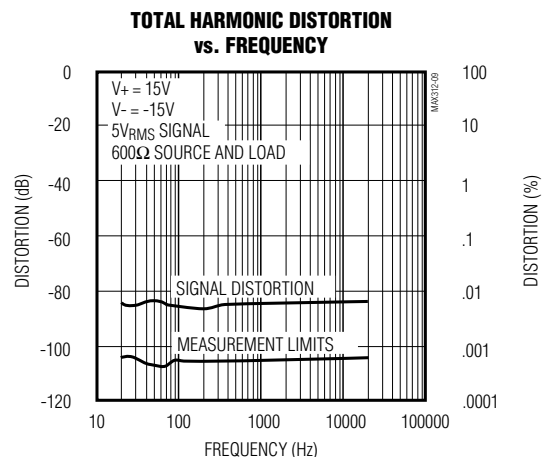
**SUPPLY CURRENT
vs. TEMPERATURE**



10 Ω , Quad, SPST, CMOS Analog Switches

Typical Operating Characteristics (continued)

($T_A = +25^\circ\text{C}$, unless otherwise noted.)



Pin Description

PIN			NAME	FUNCTION
MAX312	MAX313	MAX314		
1, 8, 9, 16	1, 8, 9, 16	1, 8, 9, 16	IN2, IN4, IN3, IN2	Logic Level Inputs
2, 7, 10, 15	2, 7, 10, 15	2, 7, 10, 15	COM1, COM4, COM3, COM2	Analog Signal Common Terminals
3, 6, 11, 14	—	—	NC1, NC4, NC3, NC2	Analog Signal Normally Closed Terminals
—	3, 6, 11, 14	—	NO1, NO4, NO3, NO2	Analog Signal Normally Open Terminals
—	—	3, 6	NO1, NO4	Analog Signal Normally Open Terminals
—	—	11, 14	NC3, NC2	Analog Signal Normally Closed Terminals
4	4	4	V-	Negative Analog Supply Input (connect to GND for single-supply operation)
5	5	5	GND	Logic Level Ground
12	12	12	VL	Logic Supply Voltage
13	13	13	V+	Positive Analog Supply Input

10 Ω , Quad, SPST, CMOS Analog Switches

Applications Information

Low-Distortion Audio

The MAX312/MAX313/MAX314, having very low R_{ON} and very low R_{ON} variation with signal amplitude, are well suited for low-distortion audio applications. The *Typical Operating Characteristics* show Total Harmonic Distortion (THD) vs. Frequency graphs for several signal amplitudes and impedances. Higher source and load impedances improve THD, but reduce off isolation.

Off Isolation at High Frequencies

In 50 Ω systems, the high-frequency on-response of these parts extends from DC to above 100MHz with a typical loss of -2dB. When the switch is turned off, however, it behaves like a capacitor, and off isolation decreases with increasing frequency. (Above 300MHz, the switch actually passes more signal turned off than turned on.) This effect is more pronounced with higher source and load impedances.

Above 5MHz, circuit board layout becomes critical, and it becomes difficult to characterize the response of the switch independent of the circuit. The graphs shown in the *Typical Operating Characteristics* were taken using a 50 Ω source and load connected with BNC connec-

tors to a circuit board deemed "average"; that is, designed with isolation in mind, but not using strip-line or other special RF circuit techniques. For critical applications above 5MHz, use the MAX440, MAX441, and MAX442, which are fully characterized up to 160MHz.

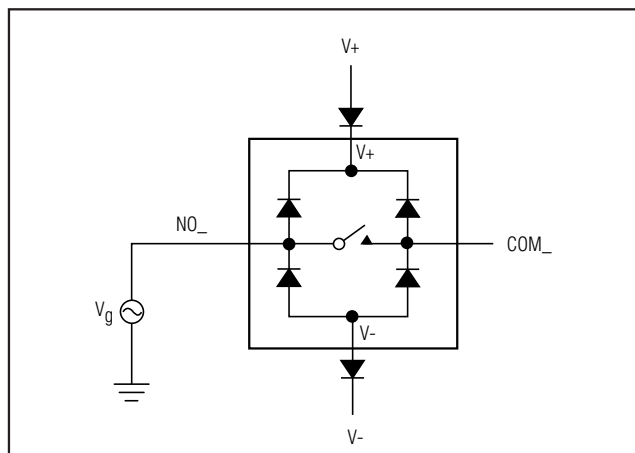


Figure 1. Overvoltage Protection Using External Blocking Diodes

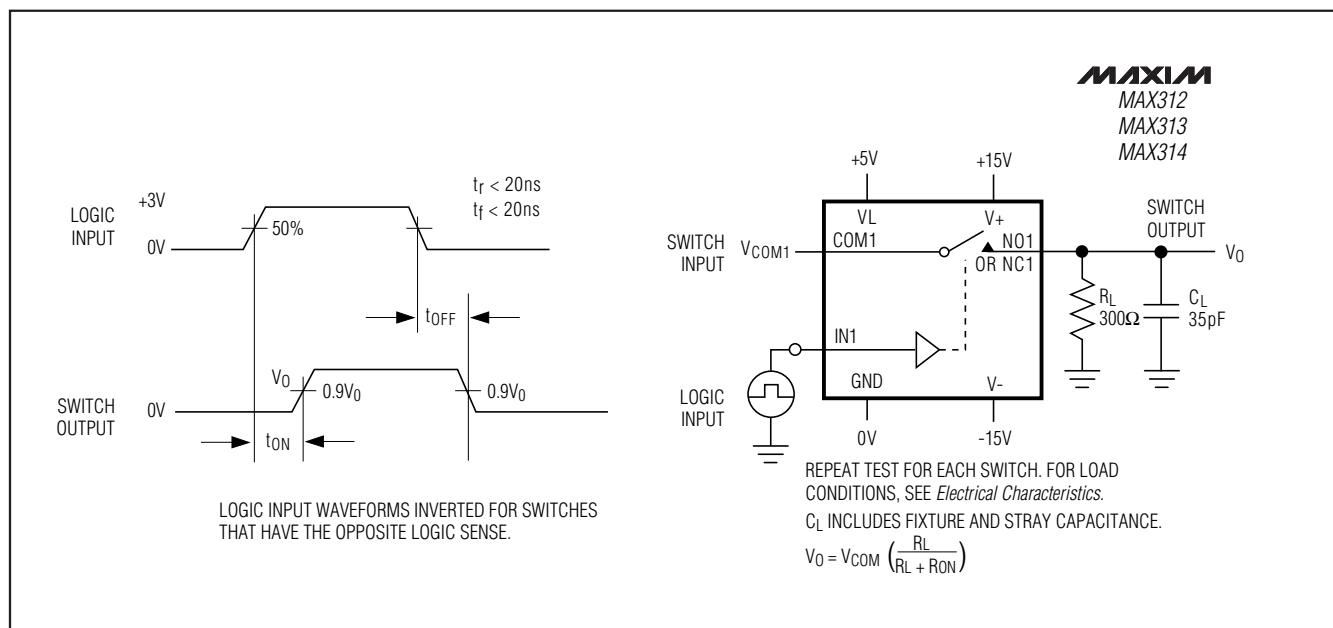


Figure 2. Switching-Time Test Circuit

10 Ω , Quad, SPST, CMOS Analog Switches

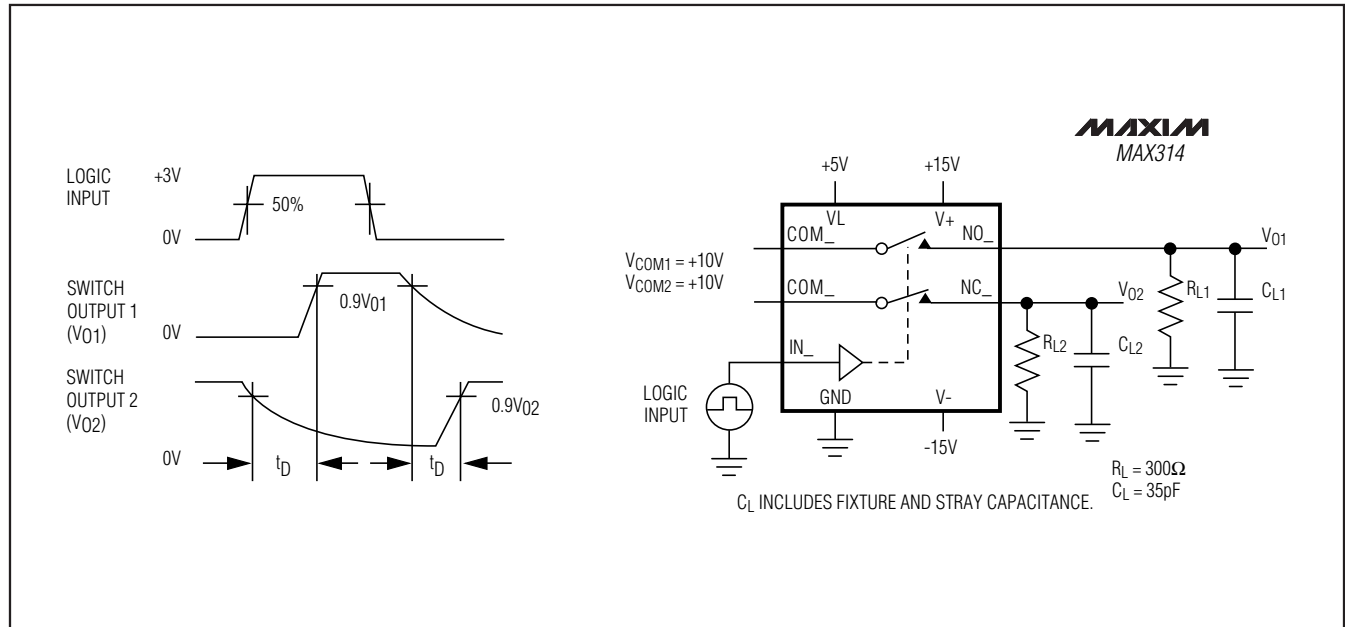


Figure 3. Break-Before-Make Test Circuit (MAX314 only)

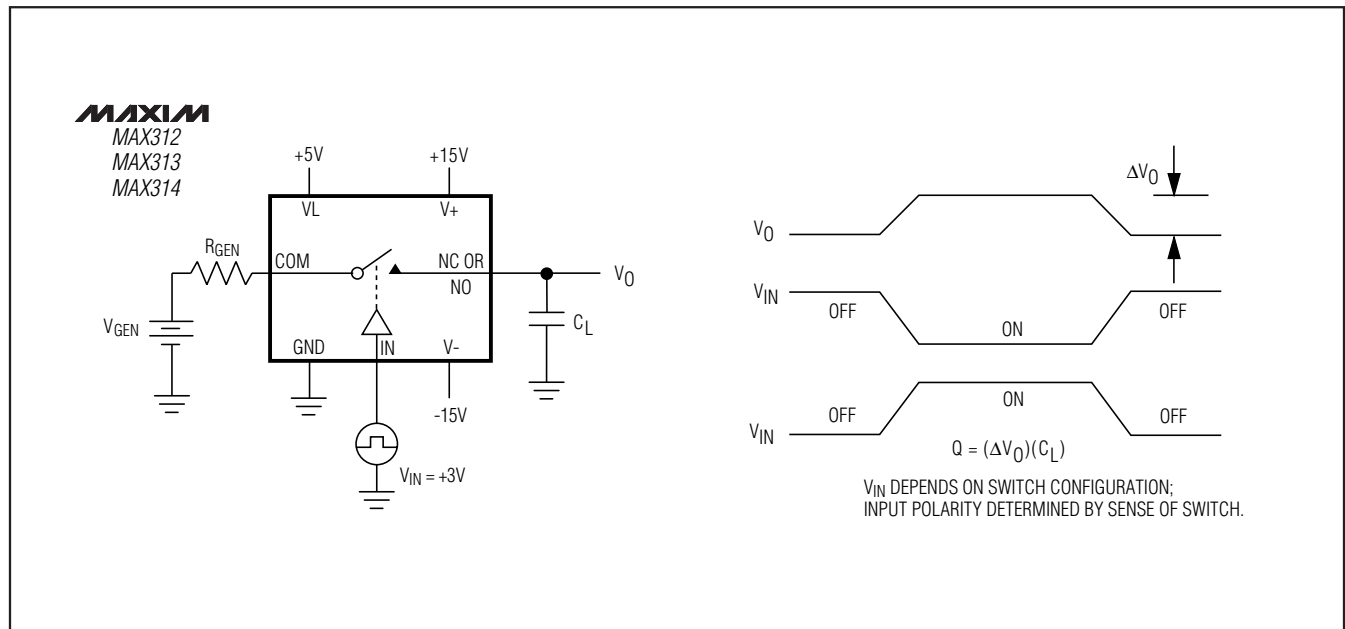


Figure 4. Charge Injection Test Circuit

10 Ω , Quad, SPST, CMOS Analog Switches

MAX312/MAX313/MAX314

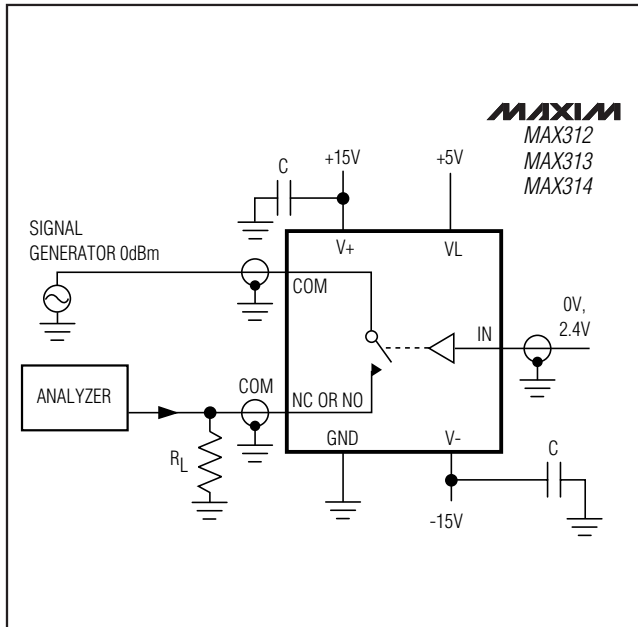


Figure 5. Off-Isolation Test Circuit

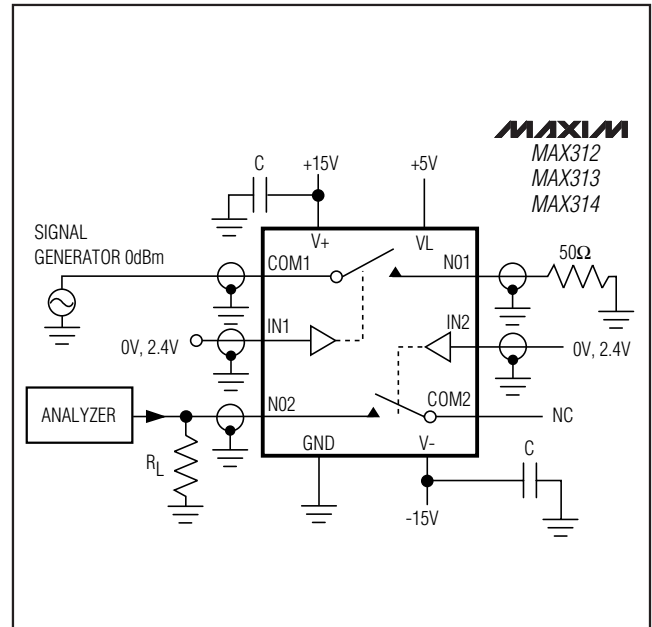


Figure 6. Crosstalk Test Circuit

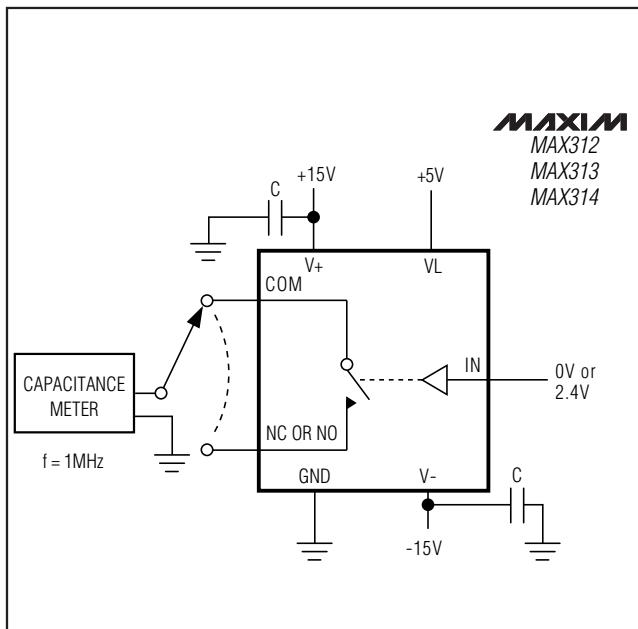


Figure 7. Channel-Off Capacitance Test Circuit

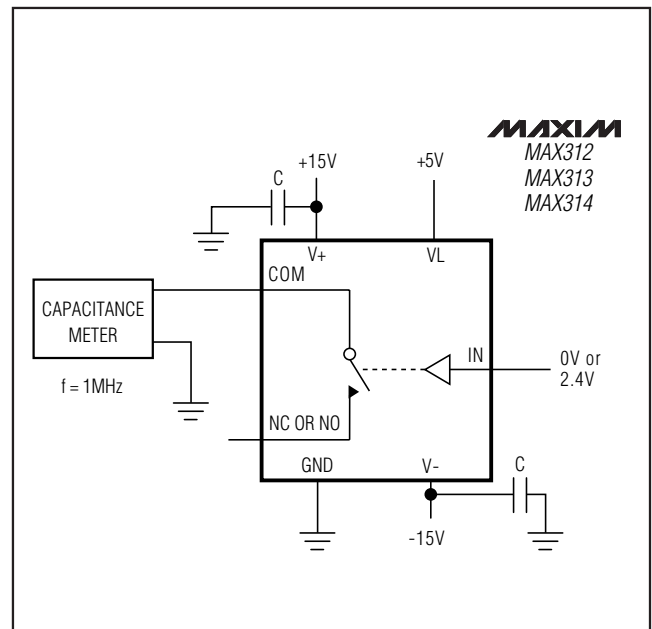


Figure 8. Channel-On Capacitance Test Circuit

10Ω, Quad, SPST, CMOS Analog Switches

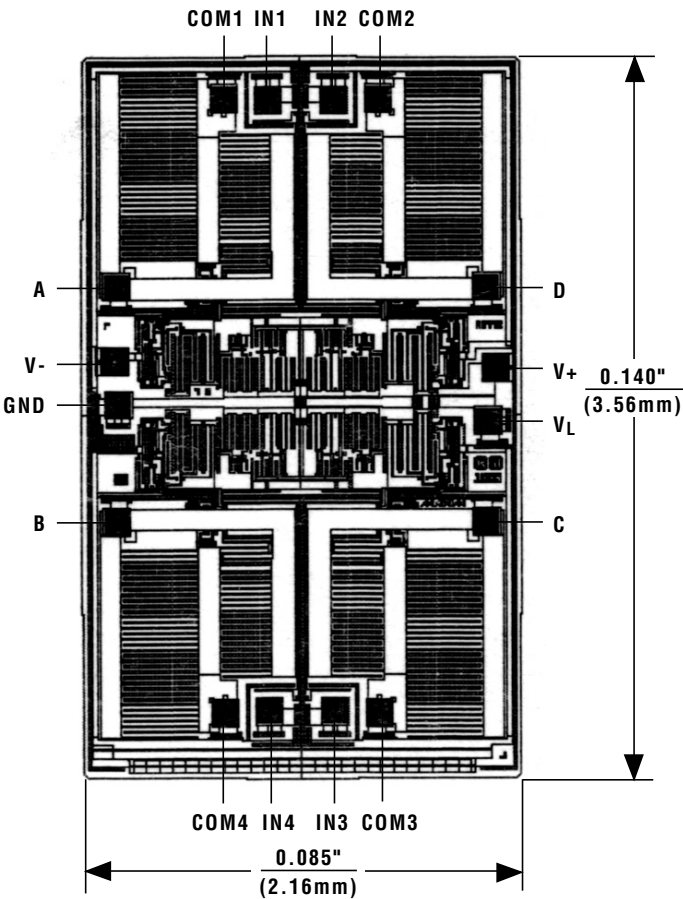
Ordering Information (continued)

PART	TEMP. RANGE	PIN-PACKAGE
MAX313CPE	0°C to +70°C	16 Plastic DIP
MAX313CSE	0°C to +70°C	16 Narrow SO
MAX313CUE	0°C to +70°C	16 TSSOP
MAX313C/D	0°C to +70°C	Dice*
MAX313EPE	-40°C to +85°C	16 Plastic DIP
MAX313ESE	-40°C to +85°C	16 Narrow SO
MAX313EUE	-40°C to +85°C	16 TSSOP
MAX313MJE	-55°C to +125°C	16 CERDIP**
MAX314CPE	0°C to +70°C	16 Plastic DIP
MAX314CSE	0°C to +70°C	16 Narrow SO
MAX314CUE	0°C to +70°C	16 TSSOP
MAX314C/D	0°C to +70°C	Dice*
MAX314EPE	-40°C to +85°C	16 Plastic DIP
MAX314ESE	-40°C to +85°C	16 Narrow SO
MAX314EUE	-40°C to +85°C	16 TSSOP
MAX314MJE	-55°C to +125°C	16 CERDIP**

* Contact factory for dice specifications.

**Contact factory for availability.

Chip Topography



MAX312		MAX313		MAX314	
PIN	NAME	PIN	NAME	PIN	NAME
A	NC1	A	NO1	A	NO1
B	NC4	B	NO4	B	NO4
C	NC3	C	NO3	C	NC3
D	NC2	D	NO2	D	NC2

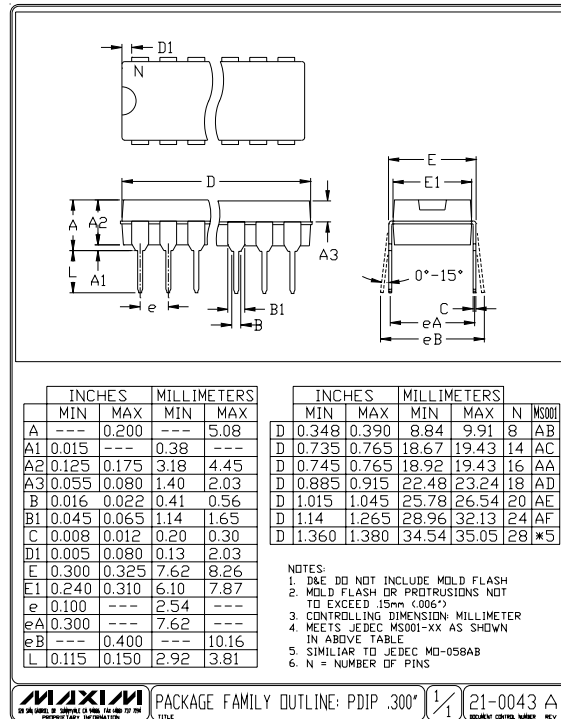
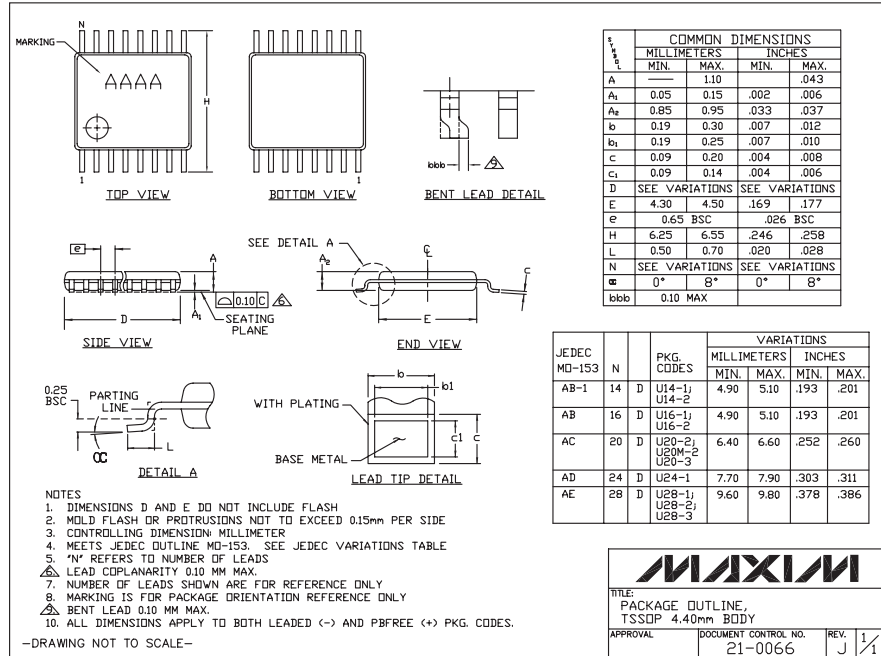
TRANSISTOR COUNT: 100

SUBSTRATE CONNECTED TO V+

10Ω, Quad, SPST, CMOS Analog Switches

Package Information

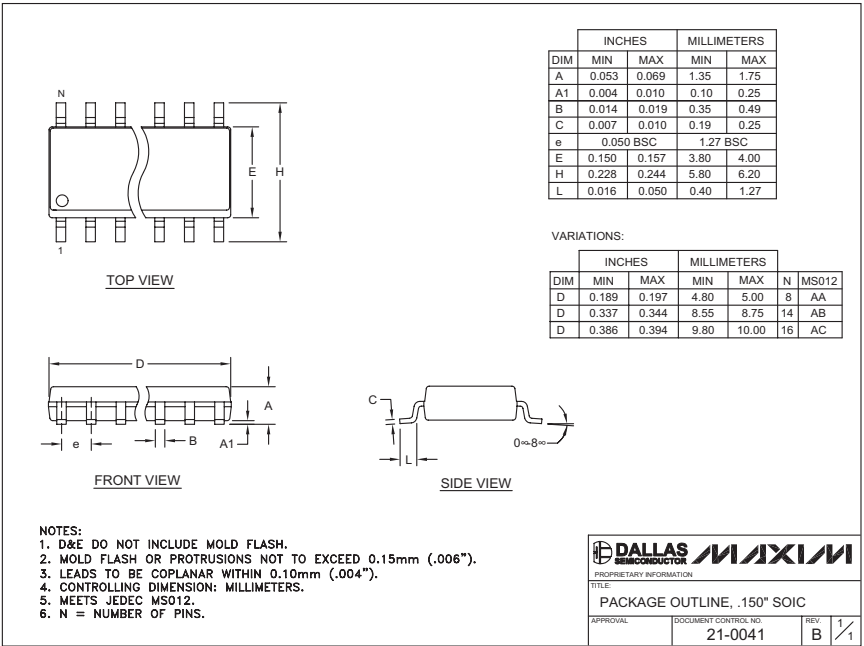
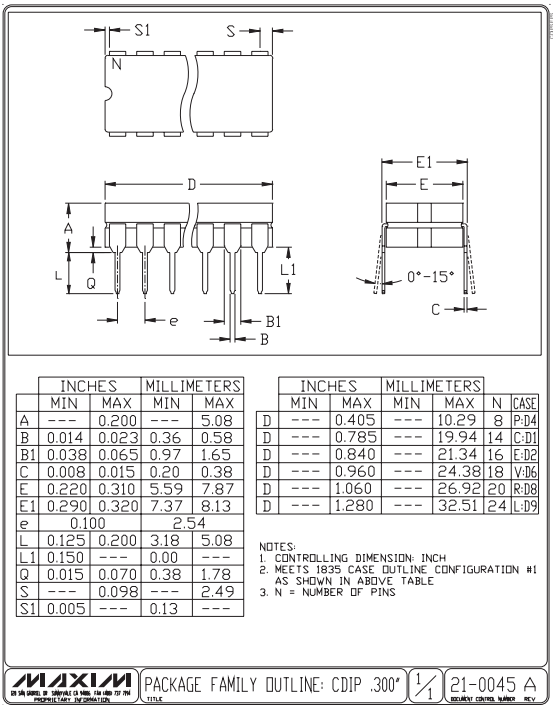
(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)



10Ω, Quad, SPST, CMOS Analog Switches

Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)



Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.