

# HEF4938B

## Dual precision monostable multivibrator

Rev. 6 — 15 November 2011

Product data sheet

### 1. General description

The HEF4938B is a dual retriggerable-resettable monostable multivibrator. Each multivibrator has an active LOW trigger/retrigger input ( $\overline{nA}$ ), an active HIGH trigger/retrigger input ( $nB$ ), an overriding active LOW direct reset input ( $\overline{nCD}$ ), an output ( $nQ$ ) and its complement ( $\overline{nQ}$ ), and two pins (CEXT, always connected to ground, and  $\overline{nREXT/CEXT}$ ) for connecting the external timing components  $C_{EXT}$  and  $R_{EXT}$ . The typical pulse width variation over the specified temperature range is  $\pm 0.2\%$ .

The multivibrator may be triggered by either the positive or the negative edges of the input pulse and will produce an accurate output pulse with a pulse width range of 10  $\mu\text{s}$  to infinity. The duration and accuracy of the output pulse are determined by the external timing components  $C_{EXT}$  and  $R_{EXT}$ . The output pulse width ( $t_W$ ) is equal to  $R_{EXT} \times C_{EXT}$ . The linear design techniques in LOC MOS (Local Oxide CMOS) guarantee precise control of the output pulse width. A LOW level at  $\overline{nCD}$  terminates the output pulse immediately. The trigger inputs' Schmitt trigger action makes the circuit highly tolerant of slower rise and fall times.

It operates over a recommended  $V_{DD}$  power supply range of 3 V to 15 V referenced to  $V_{SS}$  (usually ground). Unused inputs must be connected to  $V_{DD}$ ,  $V_{SS}$ , or another input.

### 2. Features and benefits

- Separate reset inputs
- Triggering from leading or trailing edge
- Tolerant of slow trigger rise and fall times
- Fully static operation
- 5 V, 10 V, and 15 V parametric ratings
- Standardized symmetrical output characteristics
- Specified from  $-40\text{ }^{\circ}\text{C}$  to  $+85\text{ }^{\circ}\text{C}$
- Complies with JEDEC standard JESD 13-B

### 3. Ordering information

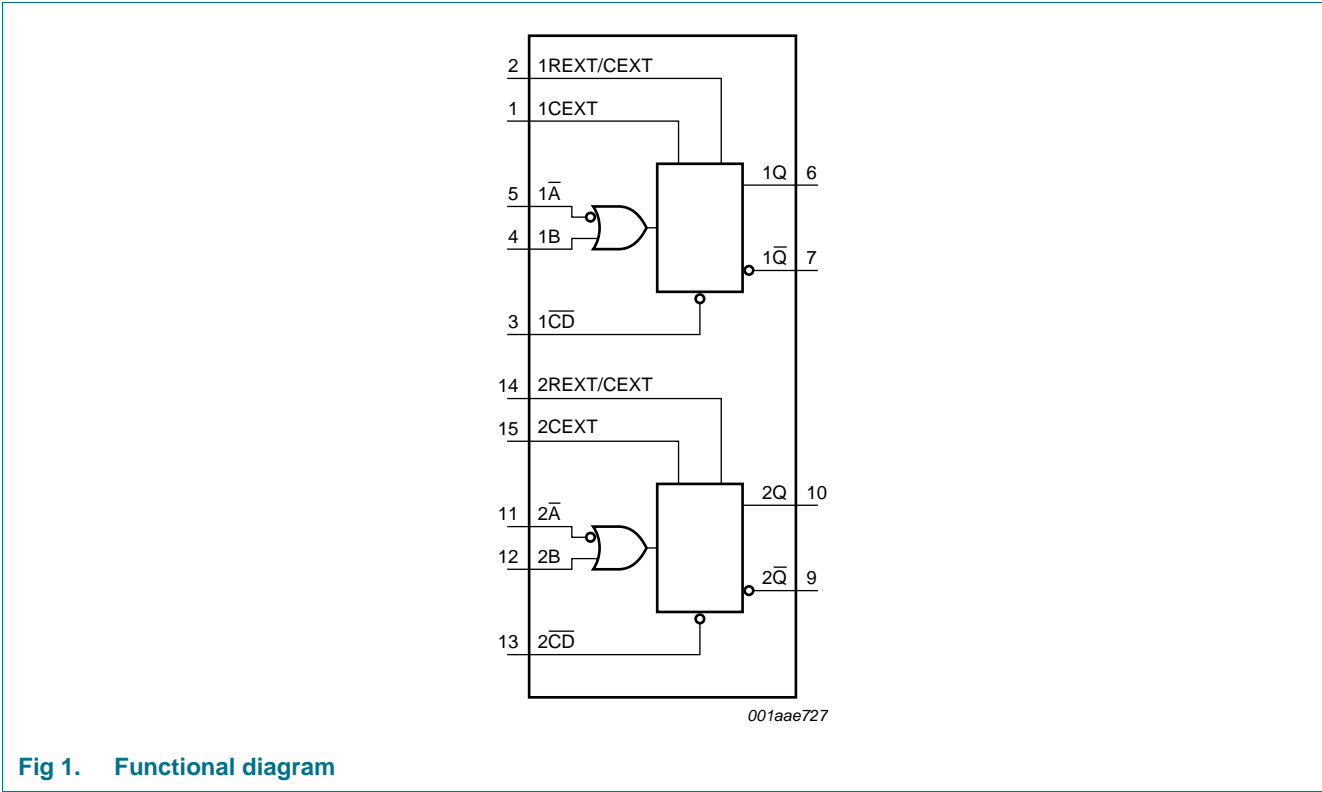
Table 1. Ordering information

All types operate from  $-40\text{ }^{\circ}\text{C}$  to  $+85\text{ }^{\circ}\text{C}$ .

Type number	Package		
	Name	Description	Version
HEF4938BP	DIP16	plastic dual in-line package; 16 leads (300 mil)	SOT38-4
HEF4938BT	SO16	plastic small outline package; 16 leads; body width 3.9 mm	SOT109-1



4. Functional diagram



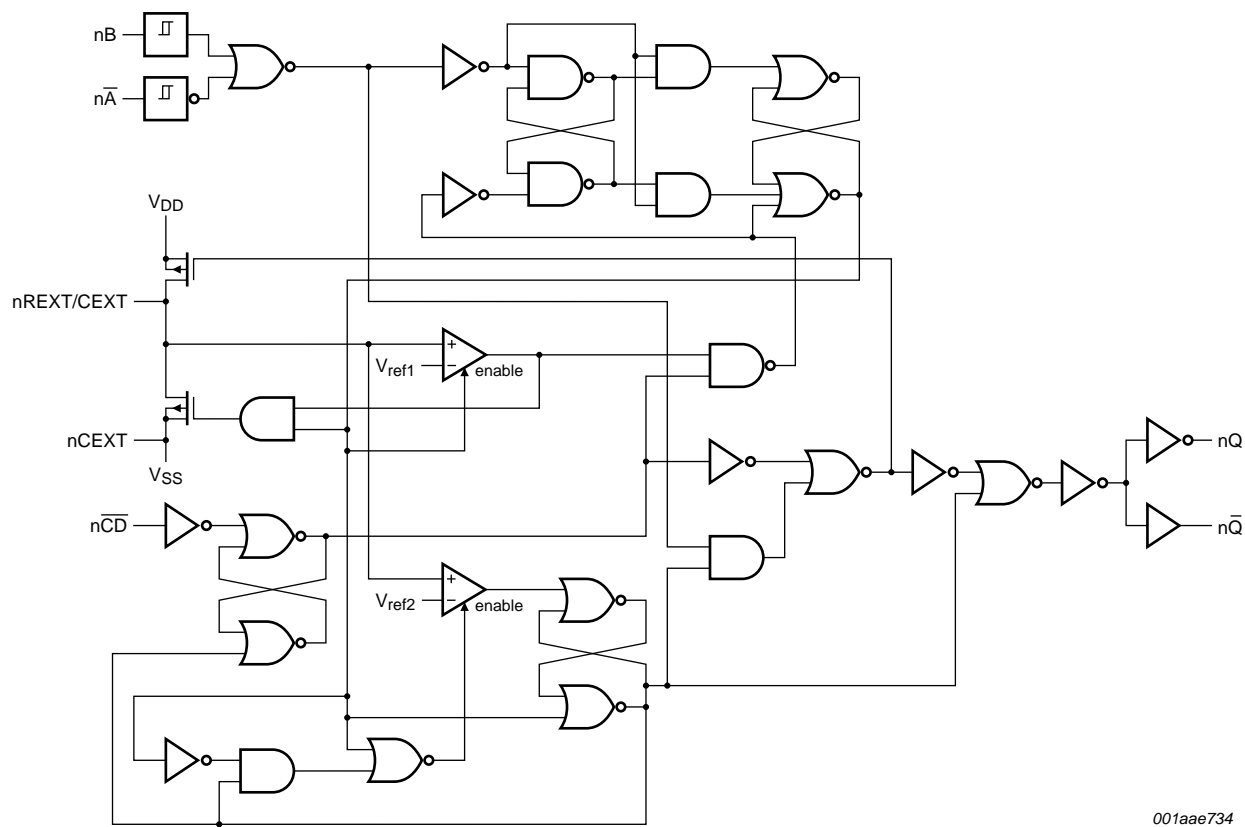


Fig 2. Logic diagram

5. Pinning information

5.1 Pinning

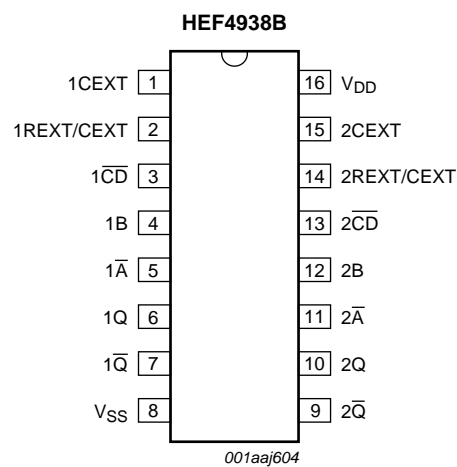


Fig 3. Pin configuration





5.2 Pin description

Table 2. Pin description


Symbol	Pin	Description
1CEXT, 2CEXT	1, 15	external capacitor connection (always connected to ground)
1REXT/CEXT, 2REXT/CEXT	2, 14	external capacitor/resistor connection
1CD, 2CD	3, 13	direct reset input (active LOW)
1B, 2B	4, 12	input (LOW-to-HIGH triggered)
1A, 2A	5, 11	input (HIGH-to-LOW triggered)
1Q, 2Q	6, 10	output
1Q, 2Q	7, 9	complementary output (active LOW)
VSS	8	ground supply voltage
VDD	16	supply voltage


6. Functional description

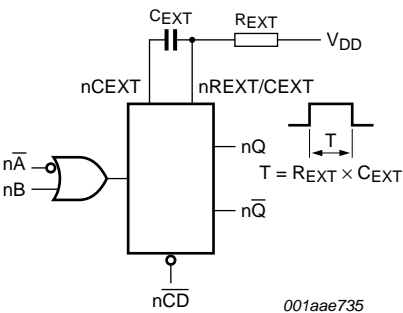
Table 3. Function table

Inputs			Outputs	
nA	nB	nCD	nQ	nQ
↓	L	H		
H	↑	H		
X	X	L	L	H

[1] H = HIGH voltage level; L = LOW voltage level; X = don't care; ↑ = positive-going transition; ↓ = negative-going transition;

 = one HIGH level output pulse, with the pulse width determined by CEXT and REXT;

 = one LOW level output pulse, with the pulse width determined by CEXT and REXT.



The external timing resistor REXT minimum value is 5 kΩ. Its maximum permissible resistance, which holds the specified accuracy of tw (nQ, nQ output), depends on the leakage current of the capacitor CEXT and the leakage of the HEF4938B. The external timing capacitor CEXT minimum value is 2000 pF with no upper limit.

Fig 4. Connection of the external timing components REXT and CEXT

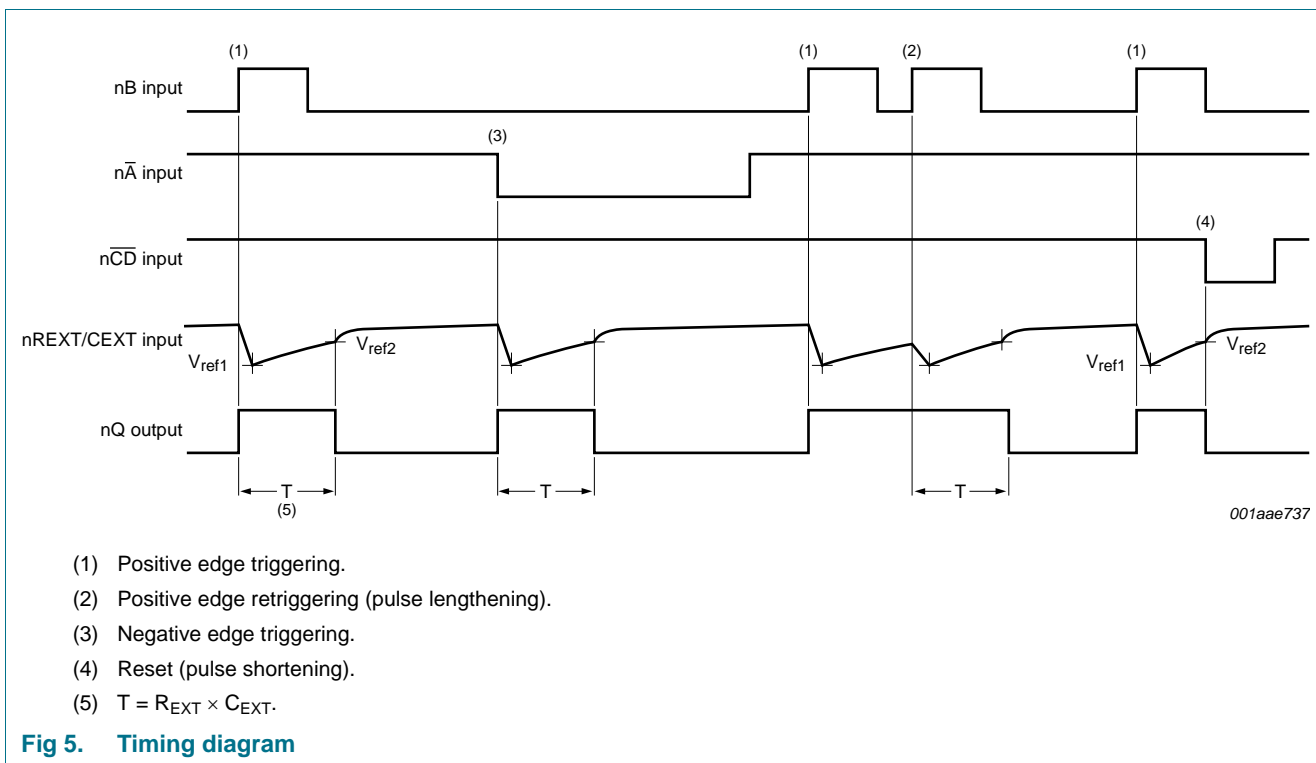


Fig 5. Timing diagram

## 7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to  $V_{SS} = 0$  V (ground)

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DD}$	supply voltage		-0.5	+18	V
$I_{IK}$	input clamping current	$V_I < -0.5$ V or $V_I > V_{DD} + 0.5$ V	-	$\pm 10$	mA
$V_I$	input voltage		-0.5	$V_{DD} + 0.5$	V
$I_{OK}$	output clamping current	$V_I < -0.5$ V or $V_I > V_{DD} + 0.5$ V		$\pm 10$	mA
$I_{I/O}$	input/output current		-	$\pm 10$	mA
$I_{DD}$	supply current			50	mA
$T_{stg}$	storage temperature		-65	+150	°C
$T_{amb}$	ambient temperature		-40	+125	°C
$P_{tot}$	total power dissipation	$T_{amb} = -40$ °C to +85 °C			
		DIP16 package	[1] -	750	mW
		SO16 package	[2] -	500	mW
P	power dissipation	per output	-	100	mW

[1] For DIP16 package:  $P_{tot}$  derates linearly with 12 mW/K above 70 °C.

[2] For SO16 package:  $P_{tot}$  derates linearly with 8 mW/K above 70 °C.

## 8. Recommended operating conditions

**Table 5. Recommended operating conditions**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{DD}$	supply voltage		3	-	15	V
$V_I$	input voltage		0	-	$V_{DD}$	V
$T_{amb}$	ambient temperature	in free air	-40	-	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{DD} = 5\text{ V}$	-	-	3.75	$\mu\text{s/V}$
		$V_{DD} = 10\text{ V}$	-	-	0.5	$\mu\text{s/V}$
		$V_{DD} = 15\text{ V}$	-	-	0.08	$\mu\text{s/V}$

## 9. Static characteristics

**Table 6. Static characteristics**

$V_{SS} = 0\text{ V}$ ;  $V_I = V_{SS}$  or  $V_{DD}$  unless otherwise specified.

Symbol	Parameter	Conditions	$V_{DD}$	$T_{amb} = -40\text{ °C}$		$T_{amb} = 25\text{ °C}$		$T_{amb} = 85\text{ °C}$		Unit
				Min	Max	Min	Max	Min	Max	
$V_{IH}$	HIGH-level input voltage	$ I_O  < 1\text{ }\mu\text{A}$	5 V	3.5	-	3.5	-	3.5	-	V
			10 V	7.0	-	7.0	-	7.0	-	V
			15 V	11.0	-	11.0	-	11.0	-	V
$V_{IL}$	LOW-level input voltage	$ I_O  < 1\text{ }\mu\text{A}$	5 V	-	1.5	-	1.5	-	1.5	V
			10 V	-	3.0	-	3.0	-	3.0	V
			15 V	-	4.0	-	4.0	-	4.0	V
$V_{OH}$	HIGH-level output voltage	$ I_O  < 1\text{ }\mu\text{A}$	5 V	4.95	-	4.95	-	4.95	-	V
			10 V	9.95	-	9.95	-	9.95	-	V
			15 V	14.95	-	14.95	-	14.95	-	V
$V_{OL}$	LOW-level output voltage	$ I_O  < 1\text{ }\mu\text{A}$	5 V	-	0.05	-	0.05	-	0.05	V
			10 V	-	0.05	-	0.05	-	0.05	V
			15 V	-	0.05	-	0.05	-	0.05	V
$I_{OH}$	HIGH-level output current	$V_O = 2.5\text{ V}$	5 V	-	-1.7	-	-1.4	-	-1.1	mA
		$V_O = 4.6\text{ V}$	5 V	-	-0.64	-	-0.5	-	-0.36	mA
		$V_O = 9.5\text{ V}$	10 V	-	-1.6	-	-1.3	-	-0.9	mA
		$V_O = 13.5\text{ V}$	15 V	-	-4.2	-	-3.4	-	-2.4	mA
$I_{OL}$	LOW-level output current	$V_O = 0.4\text{ V}$	5 V	0.64	-	0.5	-	0.36	-	mA
		$V_O = 0.5\text{ V}$	10 V	1.6	-	1.3	-	0.9	-	mA
		$V_O = 1.5\text{ V}$	15 V	4.2	-	3.4	-	2.4	-	mA
$I_I$	input leakage current	pins 2 and 14	15 V	-	$\pm 0.1$	-	$\pm 0.1$	-	$\pm 1.0$	$\mu\text{A}$
$I_{DD}$	supply current	active state	5 V	-	-	(Typical = 55)		-	-	$\mu\text{A}$
			10 V	-	-	(Typical = 150)		-	-	$\mu\text{A}$
			15 V	-	-	(Typical = 220)		-	-	$\mu\text{A}$

**Table 6.** Static characteristics ...continued $V_{SS} = 0\text{ V}$ ;  $V_I = V_{SS}$  or  $V_{DD}$  unless otherwise specified.

Symbol	Parameter	Conditions	$V_{DD}$	$T_{amb} = -40\text{ }^{\circ}\text{C}$		$T_{amb} = 25\text{ }^{\circ}\text{C}$		$T_{amb} = 85\text{ }^{\circ}\text{C}$		Unit
				Min	Max	Min	Max	Min	Max	
$I_{DD}$	supply current	$I_O = 0\text{ A}$	5 V	-	5	-	5	-	150	$\mu\text{A}$
			10 V	-	10	-	10	-	300	$\mu\text{A}$
			15 V	-	20	-	20	-	600	$\mu\text{A}$
$C_I$	input capacitance		-	-	-	-	7.5	-	-	pF

[1] Only one monostable is switching: current present during output pulse (output Q is HIGH).

## 10. Dynamic characteristics

**Table 7.** Dynamic characteristics $V_{SS} = 0\text{ V}$ ;  $T_{amb} = 25\text{ }^{\circ}\text{C}$ ; for test circuit see [Figure 11](#); unless otherwise specified.

Symbol	Parameter	Conditions	$V_{DD}$	Extrapolation formula <sup>[1]</sup>	Min	Typ	Max	Unit
$t_{PHL}$	HIGH to LOW propagation delay	$\overline{nA}$ , $\overline{nB}$ to $\overline{nQ}$ ; see <a href="#">Figure 6</a>	5 V	$193\text{ ns} + (0.55\text{ ns/pF})C_L$	-	220	440	ns
			10 V	$74\text{ ns} + (0.23\text{ ns/pF})C_L$	-	85	190	ns
			15 V	$52\text{ ns} + (0.16\text{ ns/pF})C_L$	-	60	120	ns
		$\overline{nCD}$ to $\overline{nQ}$ ; see <a href="#">Figure 6</a>	5 V	$98\text{ ns} + (0.55\text{ ns/pF})C_L$	-	125	250	ns
			10 V	$44\text{ ns} + (0.23\text{ ns/pF})C_L$	-	55	110	ns
			15 V	$32\text{ ns} + (0.16\text{ ns/pF})C_L$	-	40	80	ns
$t_{PLH}$	LOW to HIGH propagation delay	$\overline{nA}$ , $\overline{nB}$ to $\overline{nQ}$ ; see <a href="#">Figure 6</a>	5 V	$173\text{ ns} + (0.55\text{ ns/pF})C_L$	-	200	460	ns
			10 V	$79\text{ ns} + (0.23\text{ ns/pF})C_L$	-	90	180	ns
			15 V	$52\text{ ns} + (0.16\text{ ns/pF})C_L$	-	60	120	ns
		$\overline{nCD}$ to $\overline{nQ}$ ; see <a href="#">Figure 6</a>	5 V	$98\text{ ns} + (0.55\text{ ns/pF})C_L$	-	125	250	ns
			10 V	$44\text{ ns} + (0.23\text{ ns/pF})C_L$	-	55	110	ns
			15 V	$32\text{ ns} + (0.16\text{ ns/pF})C_L$	-	40	80	ns
$t_{rec}$	recovery time	$\overline{nCD}$ to $\overline{nA}$ , $\overline{nB}$ ; see <a href="#">Figure 7</a>	5 V		-	20	40	ns
			10 V		-	10	20	ns
			15 V		-	5	10	ns
$t_{rtrig}$	retrigger time	$\overline{nQ}$ , $\overline{nQ}$ to $\overline{nA}$ , $\overline{nB}$ ; see <a href="#">Figure 7</a>	5 V		0	-	-	ns
			10 V		0	-	-	ns
			15 V		0	-	-	ns
$t_W$	pulse width	$\overline{A}$ input LOW; minimum width; see <a href="#">Figure 7</a>	5 V		90	45	-	ns
			10 V		30	15	-	ns
			15 V		24	12	-	ns
		$\overline{nB}$ input HIGH; minimum width; see <a href="#">Figure 7</a>	5 V		50	25	-	ns
			10 V		24	12	-	ns
			15 V		20	10	-	ns
		$\overline{nQ}$ or $\overline{nQ}$ output; $R_{EXT} = 100\text{ k}\Omega$ ; $C_{EXT} = 0.1\text{ }\mu\text{F}$ ; see <a href="#">Figure 7</a>	5 V		9.3	10.0	10.6	ms
			10 V		9.2	9.9	10.5	ms
			15 V		9.1	9.8	10.4	ms

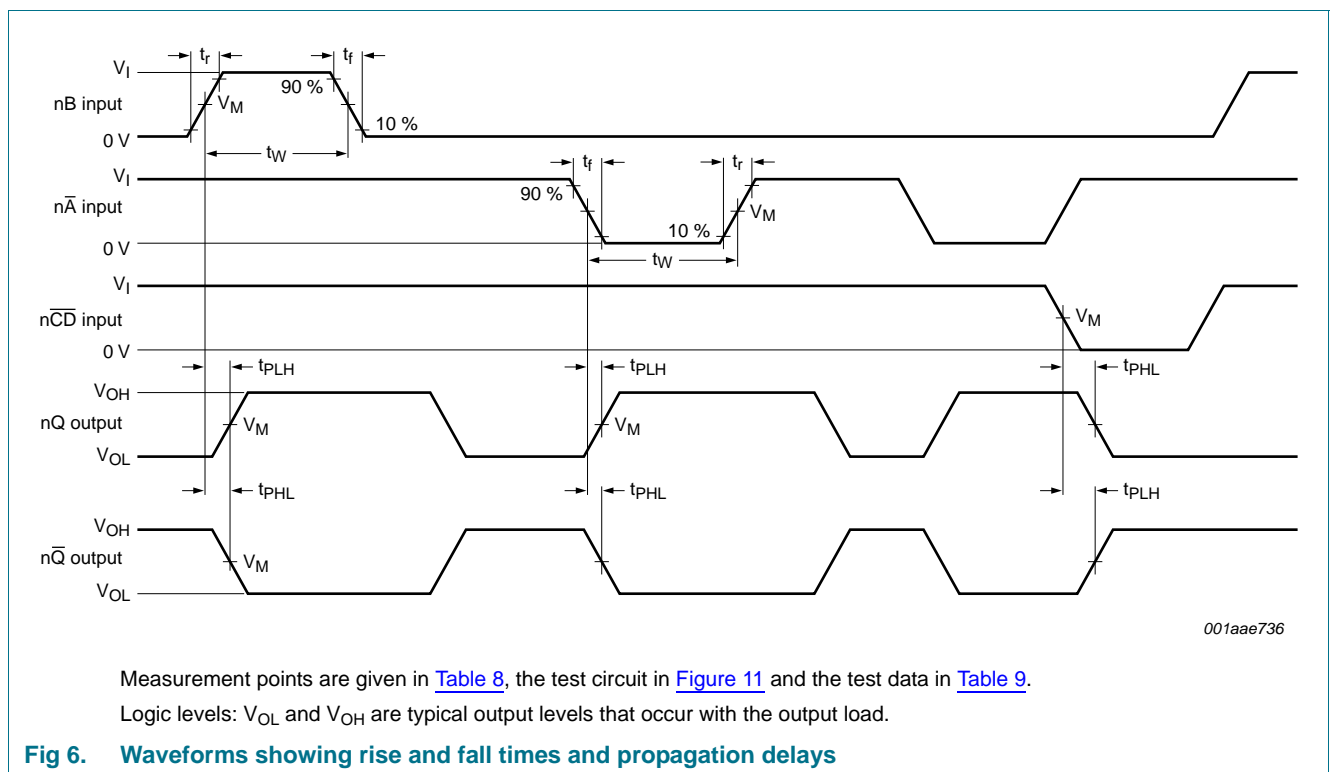
**Table 7. Dynamic characteristics ...continued**

$V_{SS} = 0\text{ V}$ ;  $T_{amb} = 25\text{ }^{\circ}\text{C}$ ; for test circuit see [Figure 11](#); unless otherwise specified.

Symbol	Parameter	Conditions	$V_{DD}$	Extrapolation formula <sup>[1]</sup>	Min	Typ	Max	Unit
$\Delta t_W$	pulse width variation	nQ or $\overline{nQ}$ output variation over temperature ( $T_{amb}$ ) range; see <a href="#">Figure 8</a>	5 V		-	$\pm 0.2$	-	%
			10 V		-	$\pm 0.2$	-	%
			15 V		-	$\pm 0.2$	-	%
		nQ or $\overline{nQ}$ output variation over $V_{DD}$ voltage range 5 V to 15 V; see <a href="#">Figure 9</a>			-	$\pm 1.5$	-	%
		nQ or $\overline{nQ}$ output variation between same package devices; $R_{EXT} = 100\text{ k}\Omega$ ;	5 V		-	$\pm 1$	-	%
		$C_{EXT} = 2\text{ nF}$ to $10\text{ }\mu\text{F}$	10 V		-	$\pm 1$	-	%
$C_I$	input capacitance	nREXT/CEXT	15 V		-	15	-	pF

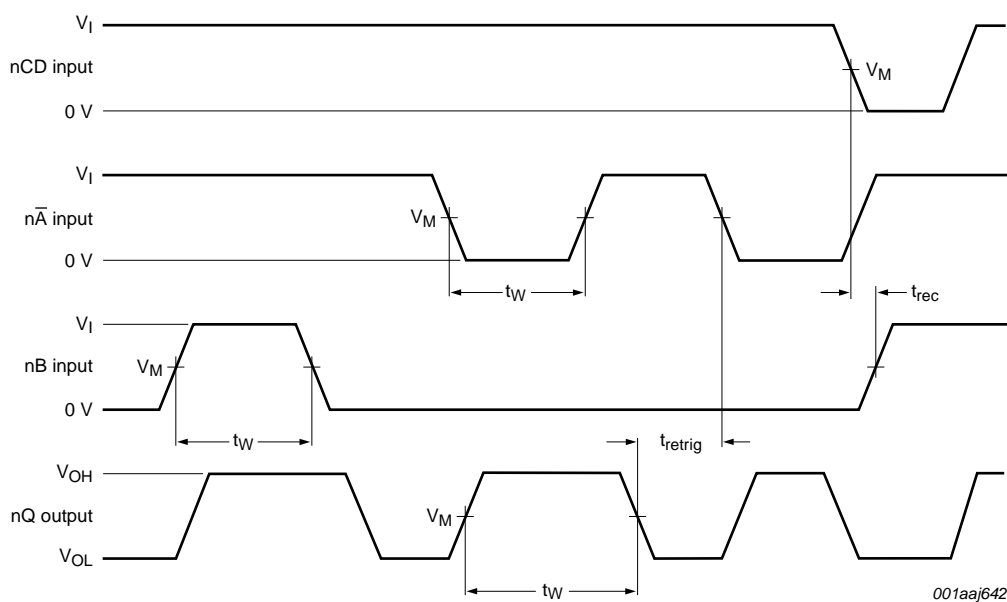
[1] The typical values of the propagation delay and transition times are calculated from the extrapolation formulas shown ( $C_L$  in pF).

## 11. Waveforms

**Fig 6. Waveforms showing rise and fall times and propagation delays****Table 8. Measurement points**

Supply voltage	Input	Output
$V_{DD}$	$V_M$	$V_M$
5 V to 15 V	$0.5V_{DD}$	$0.5V_{DD}$

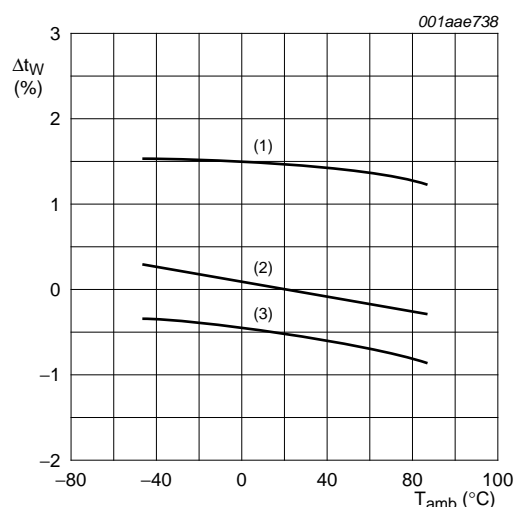




Measurement points are given in [Table 8](#), the test circuit in [Figure 11](#) and the test data in [Table 9](#).

Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output levels that occur with the output load.

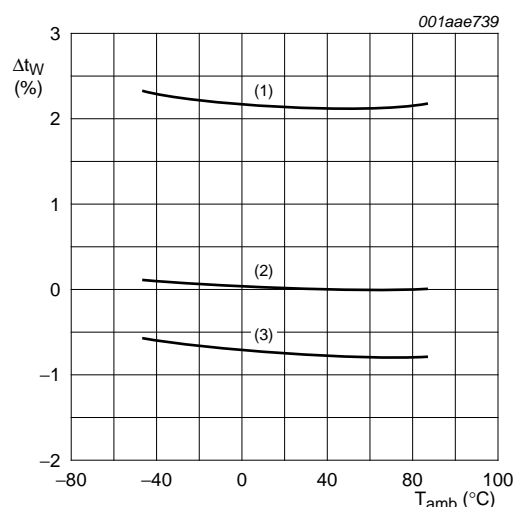
**Fig 7. Waveforms showing minimum  $n\bar{A}$ ,  $nB$ , and  $nQ$  pulse widths and recovery and retrigger times**



a.  $R_{EXT} = 100 \text{ k}\Omega$ ;  $C_{EXT} = 100 \text{ nF}$

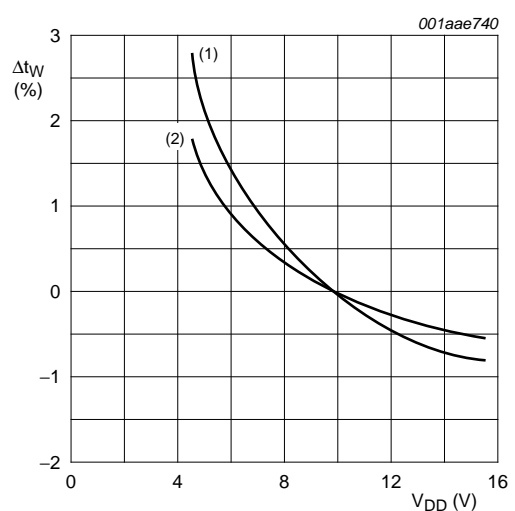
0 % at  $V_{DD} = 10 \text{ V}$  and  $T_{amb} = 25 \text{ }^{\circ}\text{C}$

- (1)  $V_{DD} = 5 \text{ V}$ .
- (2)  $V_{DD} = 10 \text{ V}$ .
- (3)  $V_{DD} = 15 \text{ V}$ .



b.  $R_{EXT} = 100 \text{ k}\Omega$ ;  $C_{EXT} = 2 \text{ nF}$

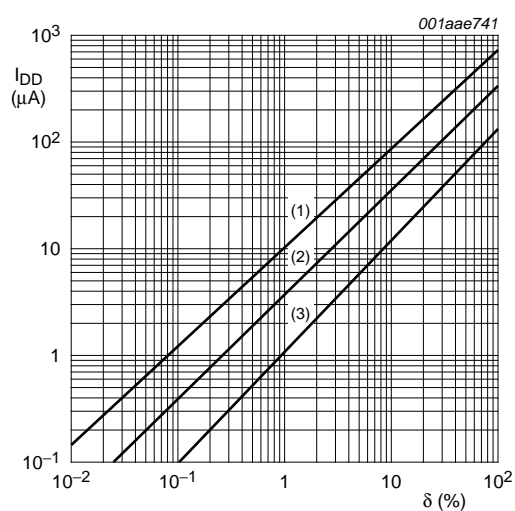
**Fig 8.** Typical normalized change in output pulse width as a function of ambient temperature



$T_{amb} = 25 \text{ }^{\circ}\text{C}$ ; 0 % at  $V_{DD} = 10 \text{ V}$ ;  $R_{EXT} = 100 \text{ k}\Omega$

- (1)  $C_{EXT} = 2 \text{ nF}$ .
- (2)  $C_{EXT} = 100 \text{ nF}$ .

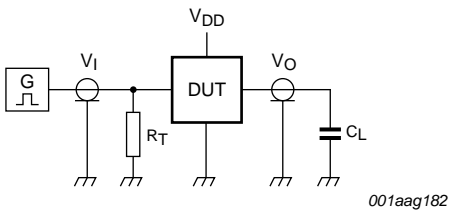
**Fig 9.** Typical normalized change in output pulse width as a function of the supply voltage



$R_{EXT} = 100 \text{ k}\Omega$ ;  $C_{EXT} = 100 \text{ nF}$ ;  $C_L = 50 \text{ pF}$ ;  
one monostable multivibrator switching only

- (1)  $V_{DD} = 15 \text{ V}$ .
- (2)  $V_{DD} = 10 \text{ V}$ .
- (3)  $V_{DD} = 5 \text{ V}$ .

**Fig 10.** Total supply current as a function of the output duty factor



Test data is given in [Table 9](#).  
Definitions for test circuit:  
DUT = Device Under Test.  
 $C_L$  = load capacitance including jig and probe capacitance.  
 $R_T$  = termination resistance should be equal to the output impedance  $Z_o$  of the pulse generator.

Fig 11. Test circuit

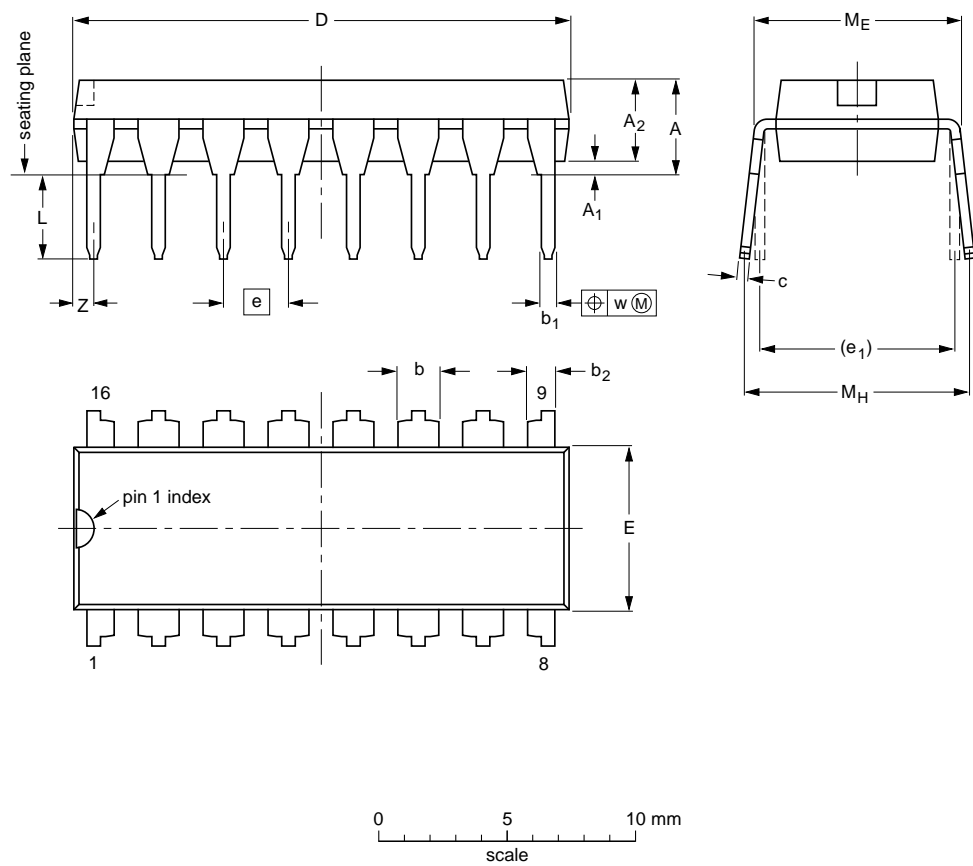
Table 9. Test data

Supply voltage	Input		Load
$V_{DD}$	$V_I$	$t_r, t_f$	$C_L$
5 V to 15 V	$V_{SS}$ or $V_{DD}$	$\leq 20$ ns	50 pF

12. Package outline

DIP16: plastic dual in-line package; 16 leads (300 mil)

SOT38-4



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A <sub>1</sub> min.	A <sub>2</sub> max.	b	b <sub>1</sub>	b <sub>2</sub>	c	D <sup>(1)</sup>	E <sup>(1)</sup>	e	e <sub>1</sub>	L	M <sub>E</sub>	M <sub>H</sub>	w	Z <sup>(1)</sup> max.
mm	4.2	0.51	3.2	1.73 1.30	0.53 0.38	1.25 0.85	0.36 0.23	19.50 18.55	6.48 6.20	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	0.76
inches	0.17	0.02	0.13	0.068 0.051	0.021 0.015	0.049 0.033	0.014 0.009	0.77 0.73	0.26 0.24	0.1	0.3	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.03

**Note**  
1. Plastic or metal protrusions of 0.25 mm (0.01 inch) maximum per side are not included.

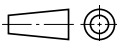
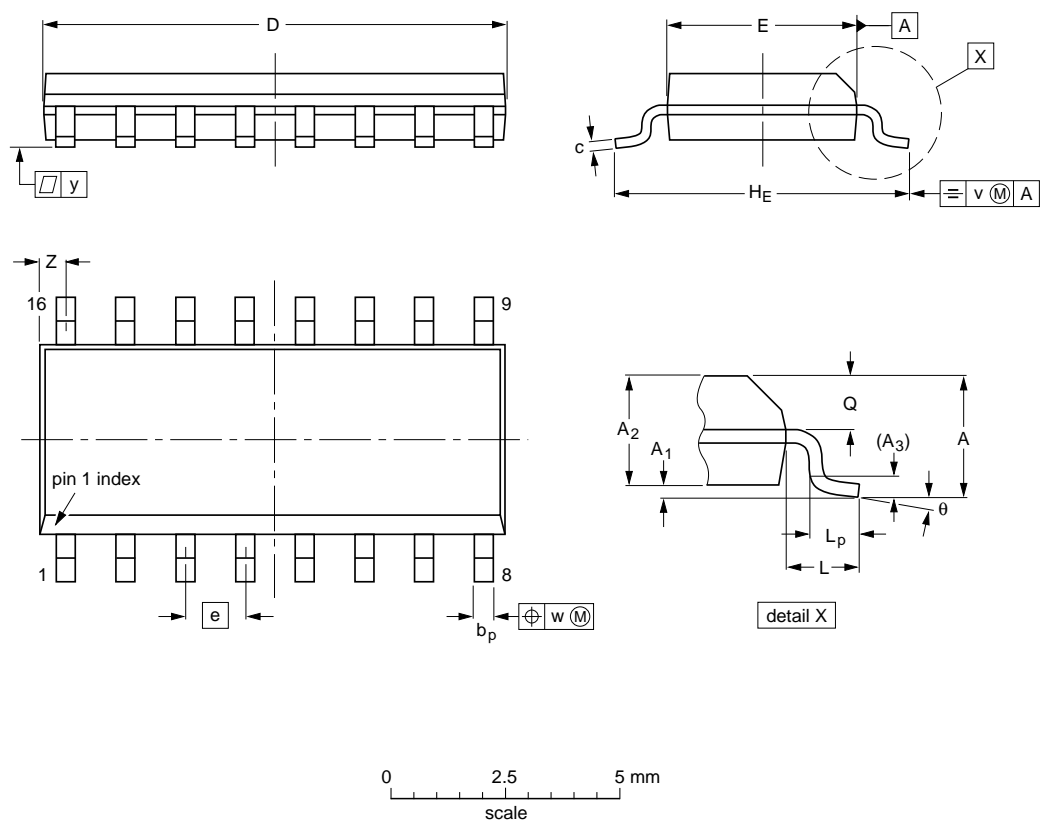
OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT38-4						95-01-14- 03-02-13

Fig 12. Package outline SOT38-4 (DIP16)

SO16: plastic small outline package; 16 leads; body width 3.9 mm

SOT109-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	b <sub>p</sub>	c	D <sup>(1)</sup>	E <sup>(1)</sup>	e	H <sub>E</sub>	L	L <sub>p</sub>	Q	v	w	y	z <sup>(1)</sup>	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	10.0 9.8	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8° 0°
inches	0.069	0.010 0.004	0.057 0.049	0.01	0.019 0.014	0.0100 0.0075	0.39 0.38	0.16 0.15	0.05	0.244 0.228	0.041	0.039 0.016	0.028 0.020	0.01	0.01	0.004	0.028 0.012	

Note

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT109-1	076E07	MS-012				99-12-27 03-02-19

Fig 13. Package outline SOT109-1 (SO16)

## 13. Revision history

**Table 10. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
HEF4938B v.6	20111115	Product data sheet	-	HEF4938B v.5
Modifications:	<ul style="list-style-type: none"><li>• Section Applications removed</li><li>• <a href="#">Table 6</a>: <math>I_{OH}</math> minimum values changed to maximum</li><li>• <a href="#">Figure 11</a>: added "DUT = Device Under Test"</li></ul>			
HEF4938B v.5	20100106	Product data sheet	-	HEF4938B v.4
HEF4938B v.4	20090309	Product data sheet	-	HEF4938B_CNV v.3
HEF4938B_CNV v.3	19950101	Product specification	-	HEF4938B_CNV v.2
HEF4938B_CNV v.2	19950101	Product specification	-	-

## 14. Legal information

### 14.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

### 14.2 Definitions

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