

EV32C6A3A1-4.434M

[Click part number to visit Part Number Details page](#)

REGULATORY COMPLIANCE (Data Sheet downloaded on Dec 3, 2017)


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ITEM DESCRIPTION

Voltage Controlled Quartz Crystal Clock Oscillators VCXO LVC MOS/TTL (CMOS) 3.3Vdc 6 Pad 5.0mm x 7.0mm Ceramic Surface Mount (SMD) 4.434MHz ± 50 ppm Maximum 0°C to +70°C ± 50 ppm Minimum 10% Typical, 20% Maximum

ELECTRICAL SPECIFICATIONS

Nominal Frequency	4.434MHz
Frequency Tolerance/Stability	± 50 ppm Maximum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, Shock, and Vibration.)
Aging at 25°C	± 2 ppm/First Year Typical, ± 10 ppm/10 Years Maximum
Operating Temperature Range	0°C to +70°C
Supply Voltage	3.3Vdc $\pm 10\%$
Input Current	15mA Maximum
Output Voltage Logic High (Voh)	90% of Vdd Minimum (IOH = -4mA)
Output Voltage Logic Low (Vol)	10% of Vdd Maximum (IOL = +4mA)
Rise/Fall Time	5nSec Maximum (Measured at 20% to 80% of Waveform)
Duty Cycle	50 ± 10 (%) (Measured at 50% of Waveform)
Load Drive Capability	10TTL Load or 30pF LVC MOS Load Maximum
Output Logic Type	CMOS
Absolute Pull Range	± 50 ppm Minimum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, Shock, Vibration, and Aging over the Control Voltage (Vc).)
Control Voltage	0.3Vdc to 3.0Vdc (Test Condition for APR)
Control Voltage Range	0.0Vdc to Vdd
Linearity	10% Typical, 20% Maximum
Transfer Function	Positive Transfer Characteristic
Modulation Bandwidth	10kHz Minimum (Measured at -3dB, Vc = 1.65Vdc)
Input Impedance	50kOhms Minimum
Input Leakage Current	10 μ A Maximum
Phase Noise	All Values are Typical -70dBc/Hz at offset of 10Hz -100dBc/Hz at offset of 100Hz -130dBc/Hz at offset of 1kHz -147dBc/Hz at offset of 10kHz -152dBc/Hz at offset of 100kHz -155dBc/Hz at offset of 1MHz
Tri-State Input Voltage (Vih and Vil)	90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)
RMS Phase Jitter	1pSec Maximum (Fj = 12kHz to 20MHz; Random)
Start Up Time	10mSec Maximum
Storage Temperature Range	-55°C to +125°C

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

ESD Susceptibility	MIL-STD-883, Method 3015, Class 1, HBM: 1500V
Fine Leak Test	MIL-STD-883, Method 1014, Condition A
Flammability	UL94-V0
Gross Leak Test	MIL-STD-883, Method 1014, Condition C
Mechanical Shock	MIL-STD-883, Method 2002, Condition B

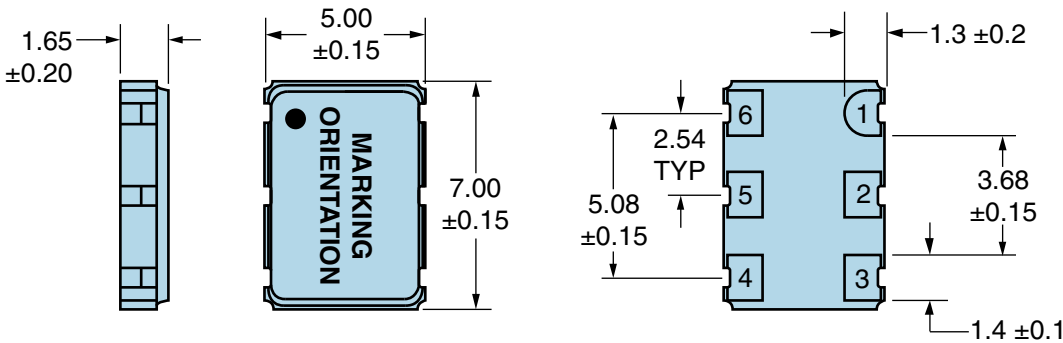
EV32C6A3A1-4.434M [Click part number to visit Part Number Details page](#)**ENVIRONMENTAL & MECHANICAL SPECIFICATIONS CONTINUED**

Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A

EV32C6A3A1-4.434M

[Click part number to visit Part Number Details page](#)

MECHANICAL DIMENSIONS (all dimensions in millimeters)

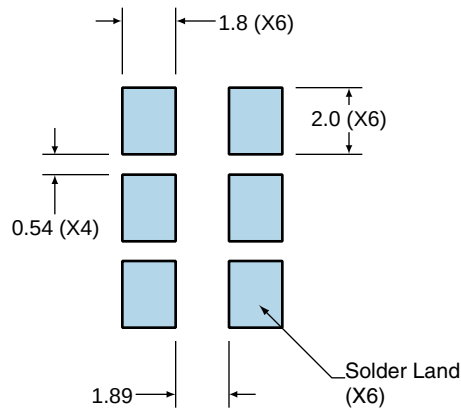


PIN	CONNECTION
1	Control Voltage
2	Tri-State
3	Case Ground
4	Output
5	No Connect
6	Supply Voltage

LINE	MARKING
1	ECLIPTEK
2	4.4340M
3	XXXXXX XXXXXX=Ecliptek Manufacturing Identifier

Suggested Solder Pad Layout

All Dimensions in Millimeters

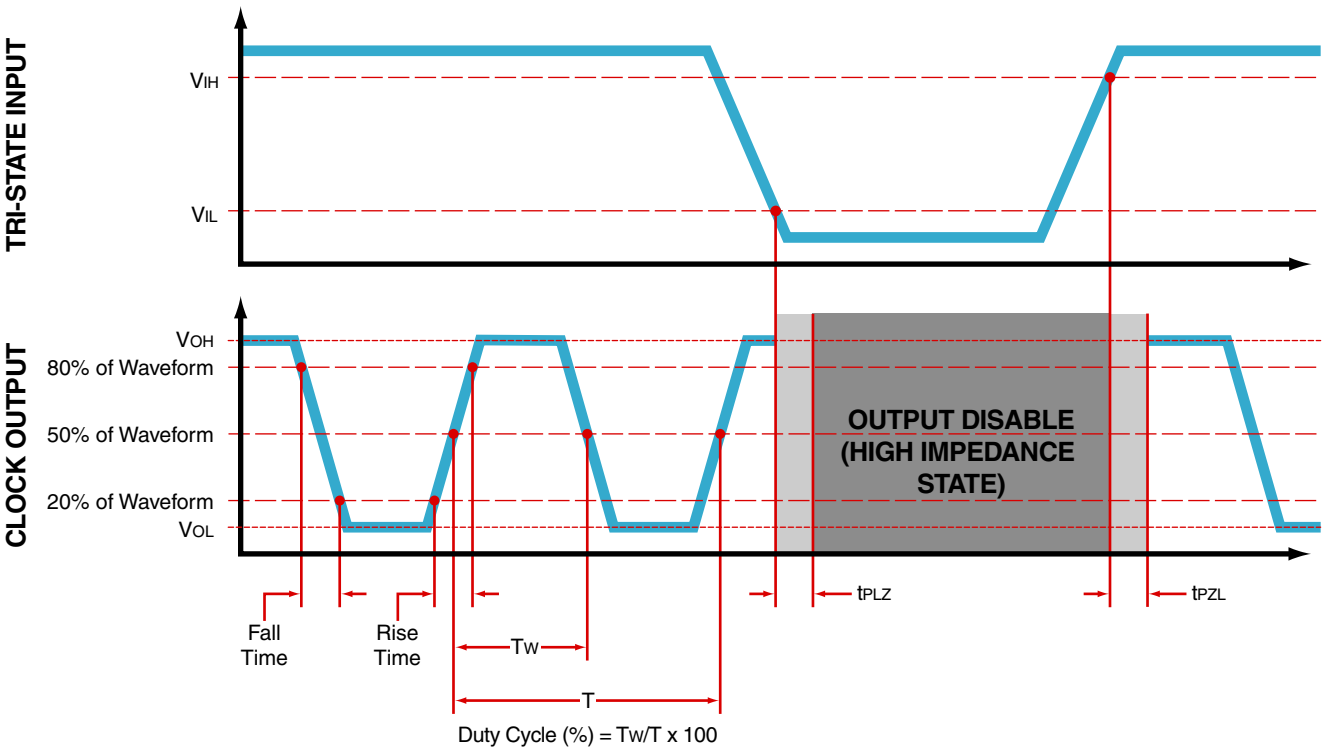


All Tolerances are ±0.1

EV32C6A3A1-4.434M

[Click part number to visit Part Number Details page](#)

OUTPUT WAVEFORM & TIMING DIAGRAM



 Click part number to visit
Part Number Details page

The diagram illustrates a test setup for measuring the output resistance of a CMOS inverter. The circuit includes the following components and connections:

- Current Meter:** Connected in series with the supply voltage (V_{DD}) to measure the current flowing into the inverter's input.
- Voltage Meters:** Two voltage meters are used. One measures the supply voltage (V_{DD}) across the current meter. The other measures the output voltage of the inverter.
- Power Supplies:** Two power supplies are shown. One provides the supply voltage (V_{DD}) to the inverter's input. The other provides a load voltage to the output of the inverter.
- Capacitors:** Two bypass capacitors, $0.01\mu F$ (Note 1) and $0.1\mu F$ (Note 1), are connected in parallel to the supply voltage pin. A load capacitor C_L (Note 3) is connected to the output of the inverter.
- Measurement Equipment:** An oscilloscope and a frequency counter are connected to the output of the inverter via a probe (Note 2).
- Switch:** A switch is used to connect the output of the inverter to the load voltage source.

Note 1: An external $0.01\mu F$ ceramic bypass capacitor in parallel with a $0.1\mu F$ high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance ($<12pF$), 10X attenuation factor, high impedance

Note 3: Capacitance value C_L includes sum of all probe and fixture capacitance.

EV32C6A3A1-4.434M

[Click part number to visit Part Number Details page](#)

Recommended Solder Reflow Methods



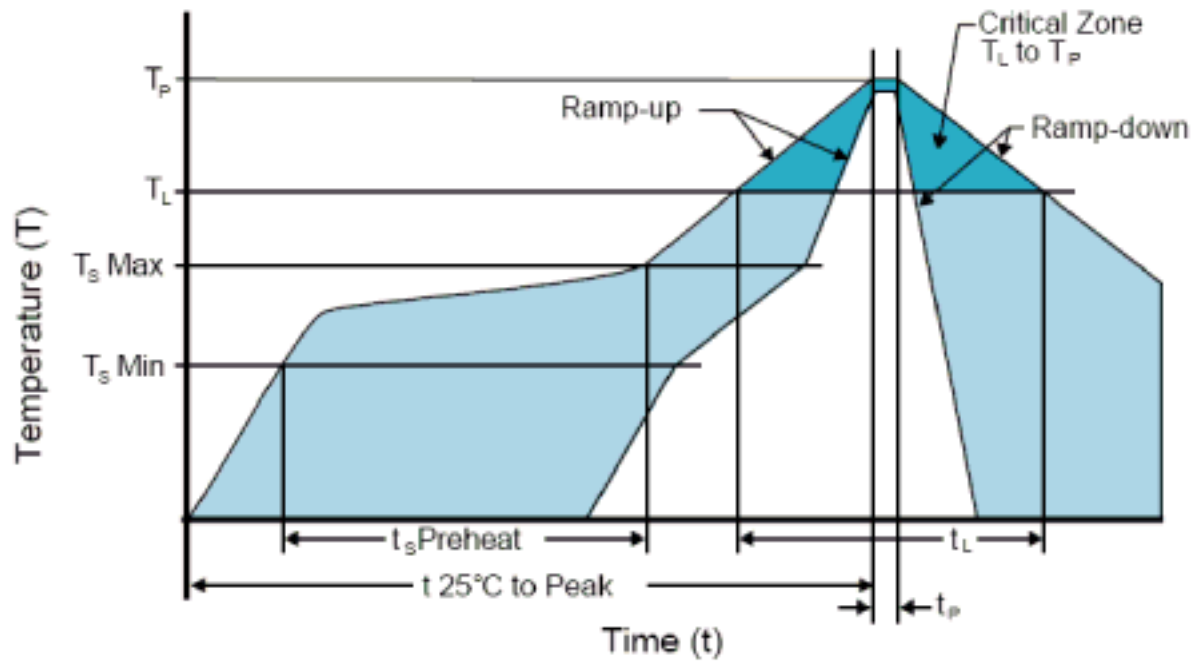
High Temperature Infrared/Convection

Ts MAX to TL (Ramp-up Rate)	3°C/Second Maximum
Preheat	
- Temperature Minimum (Ts MIN)	150°C
- Temperature Typical (Ts TYP)	175°C
- Temperature Maximum (Ts MAX)	200°C
- Time (ts MIN)	60 - 180 Seconds
Ramp-up Rate (TL to TP)	3°C/Second Maximum
Time Maintained Above:	
- Temperature (TL)	217°C
- Time (tL)	60 - 150 Seconds
Peak Temperature (TP)	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (TP Target)	250°C +0/-5°C
Time within 5°C of actual peak (tP)	20 - 40 Seconds
Ramp-down Rate	6°C/Second Maximum
Time 25°C to Peak Temperature (t)	8 Minutes Maximum
Moisture Sensitivity Level	Level 1

EV32C6A3A1-4.434M

[Click part number to visit Part Number Details page](#)

Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

TS MAX to TL (Ramp-up Rate)	5°C/Second Maximum
Preheat	
- Temperature Minimum (TS MIN)	N/A
- Temperature Typical (TS TYP)	150°C
- Temperature Maximum (TS MAX)	N/A
- Time (ts MIN)	60 - 120 Seconds
Ramp-up Rate (TL to TP)	5°C/Second Maximum
Time Maintained Above:	
- Temperature (TL)	150°C
- Time (tL)	200 Seconds Maximum
Peak Temperature (TP)	240°C Maximum
Target Peak Temperature (TP Target)	240°C Maximum 2 Times / 230°C Maximum 1 Time
Time within 5°C of actual peak (tp)	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time
Ramp-down Rate	5°C/Second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1

Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum.

High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum.