



PTC thermistors

Limit temperature sensors,
probe assemblies

Series/Type: **B59801**
Date: March 2006

Sensors

Limit temperature sensors, probe assemblies

D801

Applications

- Limit temperature sensor

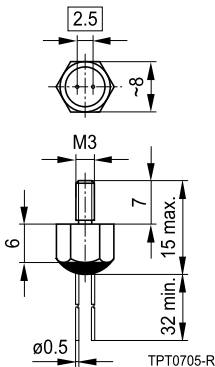
Features

- Insulated screw-type metal case
- Thread M3
- Tinned leads
- Marked with nominal threshold temperature and type designation
- Characteristics for nominal threshold temperatures $T_{NTT} = 90$ to 120 °C conform with DIN 44081
- Fast response due to small dimensions
- RoHS-compatible

Delivery mode

- Bulk

Dimensional drawing



Dimensions in mm

General technical data

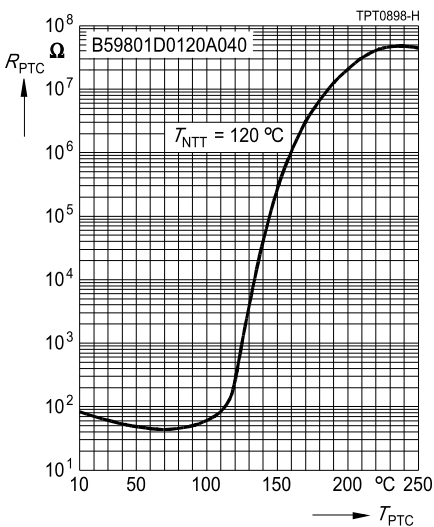
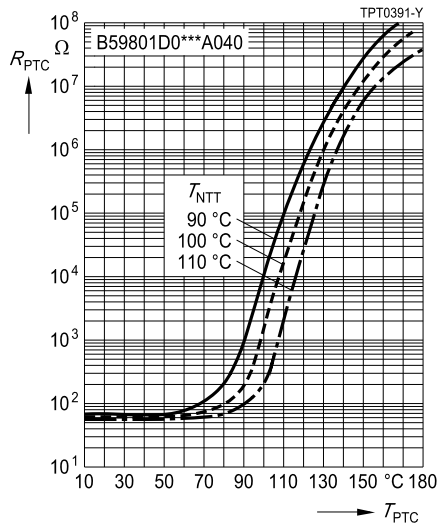
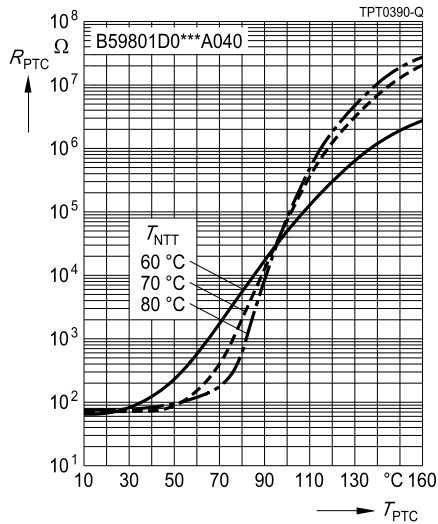
Max. operating voltage	($T_A = 0 \dots 40$ °C)	V_{max}	30	VDC
Max. measuring voltage	($T_A = -25$ °C ... $T_{NTT} + 23$ K)	$V_{meas,max}$	7.5	VDC
Rated resistance	($V_{PTC} \leq 2.5$ V)	R_R	≤ 100	Ω
Insulating test voltage		V_{ins}	1.5	kVAC
Thermal threshold time		t_a	< 20	s
Operating temperature range	($V \leq V_{meas,max}$)	T_{op}	$-40 / T_{NTT} + 23$	°C
Operating temperature range	($V = V_{max}$)	T_{op}	$0 / +40$	°C

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Electrical specifications and ordering codes

T_{NTT} °C	R ($T_{NTT} - 5 \text{ K}$) ($V_{PTC} \leq 2.5 \text{ V}$) Ω	R ($T_{NTT} + 5 \text{ K}$) ($V_{PTC} \leq 2.5 \text{ V}$) Ω	R ($T_{NTT} + 15 \text{ K}$) ($V_{PTC} \leq 7.5 \text{ V}$) Ω	R ($T_{NTT} + 23 \text{ K}$) ($V_{PTC} \leq 2.5 \text{ V}$) Ω	Ordering code
60	≤ 570	≥ 570	-	$\geq 10 \text{ k}$	B59801D0060A040
70	≤ 570	≥ 570	-	$\geq 10 \text{ k}$	B59801D0070A040
80	≤ 570	≥ 570	-	$\geq 10 \text{ k}$	B59801D0080A040
90	≤ 550	≥ 1330	$\geq 4 \text{ k}$	-	B59801D0090A040
100	≤ 550	≥ 1330	$\geq 4 \text{ k}$	-	B59801D0100A040
110	≤ 550	≥ 1330	$\geq 4 \text{ k}$	-	B59801D0110A040
120	≤ 550	≥ 1330	$\geq 4 \text{ k}$	-	B59801D0120A040

Characteristics (typical)

PTC resistance R_{PTC} versus PTC temperature T_{PTC}
(measured at low signal voltage)



Cautions and warnings

General

- EPCOS thermistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- Ensure suitability of thermistor through reliability testing during the design-in phase. The thermistors should be evaluated taking into consideration worst-case conditions.

Storage

- Store thermistors only in original packaging. Do not open the package before storage.
- Storage conditions in original packaging: storage temperature $-25\text{ }^{\circ}\text{C} \dots +45\text{ }^{\circ}\text{C}$, relative humidity $\leq 75\%$ annual mean, maximum 95%, dew precipitation is inadmissible.
- Avoid contamination of thermistors surface during storage, handling and processing.
- Avoid storage of thermistor in harmful environment with effect on function on long-term operation (examples given under operation precautions).
- Use thermistor within 6 months after delivery.

Handling

- PTCs must not be dropped. Chip-offs must not be caused during handling of PTCs.
- Components must not be touched with bare hands. Gloves are recommended.
- Avoid contamination of thermistor surface during handling.

Soldering

- Use rosin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended.
- Complete removal of flux is recommended.

Mounting

- Electrode must not be scratched before/during/after the mounting process.
- Contacts and housing used for assembly with thermistor have to be clean before mounting. Especially grease or oil must be removed.
- When PTC thermistors are encapsulated with sealing material, the precautions given in chapter "Mounting instructions", "Sealing and potting" must be observed.
- When the thermistor is mounted, there must not be any foreign body between the electrode of the thermistor and the clamping contact.
- The minimum force of the clamping contacts pressing against the PTC must be 10 N.
- During operation, the thermistor's surface temperature can be very high. Ensure that adjacent components are placed at a sufficient distance from the thermistor to allow for proper cooling at the thermistors.
- Ensure that adjacent materials are designed for operation at temperatures comparable to the surface temperature of thermistor. Be sure that surrounding parts and materials can withstand this temperature.
- Avoid contamination of thermistor surface during processing.

Operation

- Use thermistors only within the specified temperature operating range.
- Use thermistors only within the specified voltage and current ranges.
- Environmental conditions must not harm the thermistors. Use thermistors only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.
- Be sure to provide an appropriate fail-safe function to prevent secondary product damage caused by abnormal function (e.g. use VDR for limitation of overvoltage condition).

Important notes

The following applies to all products named in this publication:

1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an EPCOS product with the properties described in the product specification is suitable for use in a particular customer application.
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