P1010/P1014 Reference Design Board

Overview

The P1010 RDB is a highly integrated reference design board that can help shorten your time to market. The reference design is aimed at low-cost networking applications (with or without trust architecture), wired and wireless access, as well as industrial and smart energy applications.

This cost-effective board is based on the QorlQ P1010 processor family along with leading-edge external components to help you quickly design and implement your target application. The P1010 RDB leverages the P1010 processor and supports both the full-featured P1010 as well as the more cost-efficient P1014 device.

The P1010 processor family is based on the e500 core, built on Power Architecture® technology, which has a core frequency range of 533-800 MHz, and supports a 32 KB instruction and L1 data cache and a 256 KB L2 cache. The P1010 also supports a complete trust architecture environment, CAN controllers for industrial applications, three Gigabit Ethernet controllers with IEEE® 1588 v2 support, USB 2.0 plus PHY, two PCI Express® 1.0a controllers, up to six SerDes interfaces, 16/32-bit DDR3 and DDR3L (16-bit mode supports ECC), SPI controller supporting booting from SPI, serial flash memory, SD/MMC card controller supporting booting from flash cards and an integrated security engine. The new P1010 family with, built in 45 nm process technology provides high integration for cost-efficient solutions and enables a trust platform (through trust architecture) for customers that want to preserve the hardware or software integrity of their platforms.



The P1010 RDB platform features 1 GB of DDR3 SDRAM, 32 MB NOR flash and 32 MB NAND flash, and can boot securely from either NOR or NAND flash memory. Other boot options (i.e. SD/MMC, SPI) also supported. It supports three Gigabit-capable ports via an SGMII or RGMII PHYs, as well as a single-port USB and associated PHY.

The P1010 RDB also supports a PCI Express® add-in connector and a MiniPCI Express slot, as well as an SD/MMC card slot. These components, integrated with the P1010 family, provide an application-specific platform that can help you get a jump start on your next application design. In addition, the P1010 RDB provides a simplified migration path for existing PowerQUICC or PowerQUICC Pro processor customers wishing to migrate to QorlQ multicore processors. Customers can migrate to the single-core P1014 as a stepping stone on their way to a full-featured P1010 processor.

Since the P1010 product is pin-compatible with the P1014, only one version of the board is available with options on the board support package to target either processor.

Along with the hardware support, the P1010 RDB comes with a board support package (BSP) that includes both U-boot and Linux® 2.6 operating system support. The platform is also available with third-party software applications. For a demonstration or for details of Freescale's third-party applications for this platform, please contact your local Freescale sales office.



P1010 RDB Board Features

Memory

- 1 GB DDR3 (8-bit x 4 chips), supports up to 8 GB on-board DDR3 SDRAM
- 32 MB (16-bit) NOR flash memory
- · 32 MB SLC NAND flash memory
- 256 KB I2C EEPROM
- 128 MB SPI Flash
- SD/MMC connector to interface with the SD memory card

PCI Express

- · One standard PCI Express connector
- · One mini PCI Express connector

SATA

- One internal SATA connected to 2.5" 160G SATA 2 HDD
- · One eSATA connector to rear panel

USB 2.0

- One USB 2.0 port connected via UTMI PHY to mini AB interface
- One USB 2.0 port directly connected to mini AB interface

Three 10/100/1000 Ports

- · eTSEC 1 connected to RGMII PHY
- · eTSEC 2 and 3 connected to SGMII PHY

Flex CAN

• Two connectors for field bus interface

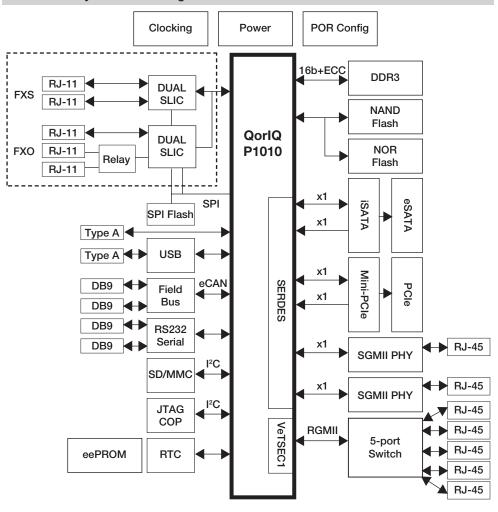
UART

DUART interface, terminated as RJ45 connectors

TDM

- Two FXS ports connected via na external SLIC to the TDM interface
- One FXO port connected via a relay to FXS for switchover to POTS

P1010-RDB System Block Diagram



Others

- LEDs: Power (green), link (green), activities (yellow)
- JTAG for debugging
- IEEE 1588 v2 signals for test and measurement

Form Factor

Mini ITX form factor

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