

To all our customers

Regarding the change of names mentioned in the document, such as Mitsubishi Electric and Mitsubishi XX, to Renesas Technology Corp.

The semiconductor operations of Hitachi and Mitsubishi Electric were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Mitsubishi Electric, Mitsubishi Electric Corporation, Mitsubishi Semiconductors, and other Mitsubishi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

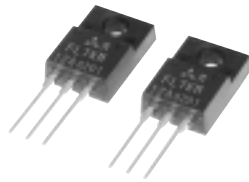
Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

FL7KM-12A

HIGH-SPEED SWITCHING USE

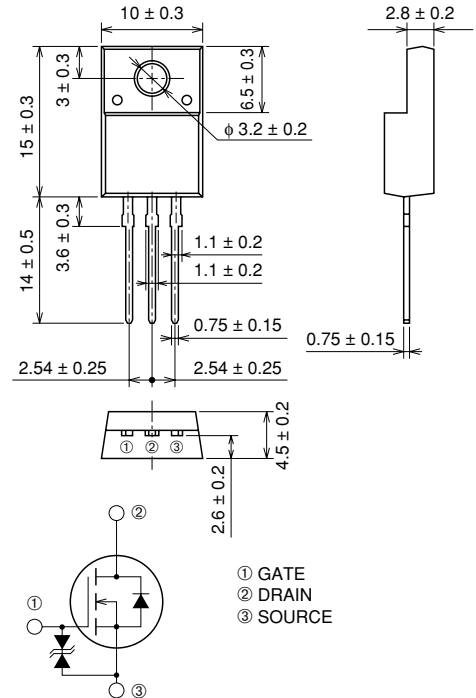
FL7KM-12A



- 10V DRIVE
- V_{DS} 600V
- $r_{DS} (ON) (MAX)$ 1.3Ω
- I_D 7A

OUTLINE DRAWING

Dimensions in mm



TO-220FN

APPLICATION

SMPS, Inverter fluorescent light sets, etc.

MAXIMUM RATINGS (T_c = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit
V_{DS}	Drain-source voltage	$V_{GS} = 0V$	600	V
V_{GS}	Gate-source voltage	$V_{DS} = 0V$	±30	V
I_D	Drain current		7	A
I_{DM}	Drain current (Pulsed)		21	A
I_{DA}	Avalanche drain current (Pulsed)	$L = 200\mu H$	7	A
P_D	Maximum power dissipation		35	W
T_{ch}	Channel temperature		-55 ~ +150	°C
T_{stg}	Storage temperature		-55 ~ +150	°C
V_{iso}	Isolation voltage	AC for 1minute, Terminal to case	2000	V
—	Weight	Typical value	2.0	g

ELECTRICAL CHARACTERISTICS ($T_{ch} = 25^{\circ}\text{C}$)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
$V_{(BR)DSS}$	Drain-source breakdown voltage	$I_D = 1\text{mA}$, $V_{GS} = 0\text{V}$	600	—	—	V
$V_{(BR)GSS}$	Gate-source breakdown voltage	$I_{GS} = \pm 100\mu\text{A}$, $V_{DS} = 0\text{V}$	± 30	—	—	V
I_{GSS}	Gate-source leakage current	$V_{GS} = \pm 25\text{V}$, $V_{DS} = 0\text{V}$	—	—	± 10	μA
I_{DSS}	Drain-source leakage current	$V_{DS} = 600\text{V}$, $V_{GS} = 0\text{V}$	—	—	1	mA
$V_{GS(th)}$	Gate-source threshold voltage	$I_D = 1\text{mA}$, $V_{DS} = 10\text{V}$	2.0	3.0	4.0	V
$r_{DS(on)}$	Drain-source on-state resistance	$I_D = 3\text{A}$, $V_{GS} = 10\text{V}$	—	1.1	1.3	Ω
$V_{DS(on)}$	Drain-source on-state voltage	$I_D = 3\text{A}$, $V_{GS} = 10\text{V}$	—	3.3	3.9	V
$ y_{fs} $	Forward transfer admittance	$I_D = 3\text{A}$, $V_{DS} = 10\text{V}$	—	5.0	—	S
C_{iss}	Input capacitance	$V_{DS} = 25\text{V}$, $V_{GS} = 0\text{V}$, $f = 1\text{MHz}$	—	950	—	pF
C_{oss}	Output capacitance		—	115	—	pF
C_{rss}	Reverse transfer capacitance		—	30	—	pF
$t_d(on)$	Turn-on delay time	$V_{DD} = 200\text{V}$, $I_D = 3\text{A}$, $V_{GS} = 10\text{V}$, $R_{GEN} = R_{GS} = 50\Omega$	—	20	—	ns
t_r	Rise time		—	30	—	ns
$t_d(off)$	Turn-off delay time		—	180	—	ns
t_f	Fall time		—	65	—	ns
V_{SD}	Source-drain voltage	$I_S = 3\text{A}$, $V_{GS} = 0\text{V}$	—	1.5	2.0	V
$R_{th(ch-c)}$	Thermal resistance	Channel to case	—	—	3.57	$^{\circ}\text{C/W}$

PERFORMANCE CURVES
