



POE-D11-00-E-06

**CERAMIC DISC CAPACITOR SAFETY RECOGNIZED,  
AC SERIES**

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# PRODUCT SPECIFICATION

**PRODUCT: CERAMIC DISC CAPACITOR  
SAFETY RECOGNIZED****TYPE: AC SERIES****CUSTOMER:** \_\_\_\_\_**DOC. NO.: POE-D11-00-E-06****Ver.: 6****APPROVED BY CUSTOMER****VENDOR :**☐ **WALSIN TECHNOLOGY CORPORATION**566-1, KAO SHI ROAD, YANG-MEI  
TAO-YUAN, TAIWAN☐ **PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD.**NO.277,HONG MING ROAD,EASTERN SECTION,  
GUANG ZHOU ECONOMIC AND TECHNOLOGY  
DEVELOPMENT ZONE,CHINA**MAKER : PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD.**NO.277,HONG MING ROAD,EASTERN SECTION,  
GUANG ZHOU ECONOMIC AND TECHNOLOGY  
DEVELOPMENT ZONE,CHINA

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### Record of change

Date	Version	Description	page
2008.6.3	1	1. D23-00-E-01(before) → POE-D11-00-E-01(1 <sup>st</sup> edition)	
2008.8.22	2	1 Complete lead code	20
		2. Add last SAP code “ H” for halogen and Pb free , epoxy resin..	3
2008.12.12	3	1.Complete the 13 <sup>th</sup> to 17 <sup>th</sup> codes of SAP P/N.	4
		2. Page layout adjustment.	
2009.7.16	4	1 Change PSA & POE logo to Walsin & POE logo.	9
		2.Complete Marking statement.	11
		3. Revised standard NO. of SEV, SEMKO, FIMKO, NEMKO, DEMKO and KEMA. Revised recognized NO. of FIMKO, NEMKO, DEMKO ,KEMA and CQC.	
		4. Downsize : YP0AC101K080** → YP0AC101K060 ** YP0AC102K100** → YP0AC102K080** YV0AC152M080** → YV0AC152M060**	6
2009.9.14	5	1. “Protrusion length”: “+0.5 to-1.0” revised to “2.0max (Or the end of lead wire may be inside the tape.)”	9
2009.12.24	6	1. Marking	10
		2. Correct recognized No	11
		3. Revised the Figure of impulse voltage test(Item 7.3.14) according to the standard IEC 60384-14 ed.3	14

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## 1. Part number for SAP system

(Ex.)     YV     0AC     472     M     100     L     25     C     7     B  
                  **①**            **②**            **③**            **④**            **⑤**            **⑥**            **⑦**            **⑧**            **⑨**            **⑩**

### ① Temperature characteristic (identified code)

CODE	CH(NP0)	SL	YP (Y5P)	YV(Y5V)	YU (Y5U)
Cap. Change	0±60PPM/°C	-1000~+350PPM/°C	±10%	-80% ~ +30%	-55% to +20%

### ② TYPE (identified by 3-figure code) : 0AC = AC TYPE (X1/Y2)

### ③ Capacitance (identified by 3-figure code) : EX.221=220pF

### ④ Capacitance tolerance (identified by code) : C : ±0.25pF, D : ±0.5pF, J : ±5%, K : ±10%, M : ±20%

### ⑤ Nominal body diameter dimension (identified by 3-figure code)

### ⑥ Lead Style : Refer to “2. Mechanical”.

### ⑦ Packing mode and lead length (identified by 2-figure code)

Taping Code	Description
AF	Box and Pitch : 15.0 mm
AM	Box and Pitch : 25.4 mm
AT	Box and Pitch : 30.0 mm

Bulk Code	Description
3E	Lead length : 3.5mm
04	Lead length : 4.0mm
4E	Lead length : 4.5mm
25	Lead length : 25.0mm

### ⑧ Length tolerance

Code	Description
A	±0.5 mm (only for kink lead type)
B	±1.0 mm
C	MIN.
D	Taping special purpose

### ⑨ Pitch

Code	Description
7	7.5±1 mm
0	10±1 mm

### ⑩ Epoxy Resin Code

Code	Description
B	Pb free, Epoxy Resin
H	Halogen and Pb free , epoxy resin.

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**2. Mechanical**

Encapsulation : Epoxy resin, flammability UL94 V-0

Available lead code(unit: mm)

Lead type	SAP P/N (13-17)digits	Old P/N (Ref. to “3.1 old P/N”)	Pitch (F)	Lead Length (L)	Packing	Lead Configuration
Lead style : L	L25C7	L7	7.5 ±1.0	25 MIN.	Bulk	
Type L Straight long lead	L25C0	L0	10 ± 1.0	25 MIN.		
Lead style : B	BAFD7	L7F	Refer to “4. Taping format”		Tap. Ammo	
Type B Straight long lead	BATD7 / BATD0	L7T / L0T				
	BAMD0	L0M				
Lead style : L	L03B7	S7	7.5 ± 1.0	3.0 ± 1.0	Bulk	
Type L Straight short lead	L4EB7	S7	7.5 ± 1.0	4.5 ± 1.0		
	L05B7	P16	7.5 ± 1.0	5.0 ± 1.0		
	L03B0	S0	10 ± 1.0	3.0 ± 1.0		
	L4EB0	S0	10 ± 1.0	4.5 ± 1.0		
	L05B0	A	10 ± 1.0	5.0± 1.0		
Lead style : D	D3EA7	D2	7.5 ± 1.0	3.5 ± 0.5	Bulk	
Type D Vertical kink lead	D04A7	D7	7.5 ± 1.0	4.0 ± 0.5		
	D3EA0	D3	10 ± 1.0	3.5 ± 0.5		
	D04A0	D0	10 ± 1.0	4.0 ± 0.5		
	DAFD7	D7F	Refer to “4. Taping format”		Tap. Ammo	
	DATD7 / DATD0	D7T / D0T				
	DAMD0	D0M				
Lead style : X	X3EA7	Q2	7.5 ± 1.0	3.5 ± 0.5	Bulk	
Type X Outside kink lead	X04A7	X7	7.5 ± 1.0	4.0 ± 0.5		
	X05B7	X2	7.5 ± 1.0	5.0 ± 1.0		
	X3EA0	Q3	10 ± 1.0	3.5 ± 0.5		
	X04A0	X0	10 ± 1.0	4.0 ± 0.5		
	X05B0	X3	10 ± 1.0	5.0 ± 1.0		
	XAFD7	X7F	Refer to “4. Taping format”		Tap. Ammo	
	XATD7 / XATD0	X7M / X0M				
	XAMD0	X0M				
Lead style : H	H3EA7	H2A	7.5±1.0	3.5±0.5 mm	Bulk	
Type H Inside kink lead	H3EA0	H3A	10.0±1.0	3.5±0.5 mm		
	HATD0	H0T	Refer to “4. Taping format”		Tap. Ammo	
	HAMD0	H0M				

\* Lead diameter Φd: 0.60 +0.1/-0.05

\* e (Coating **extension** on leads): 3.0mmMax for straight lead lead style; Not exceed the kink for kink lead.

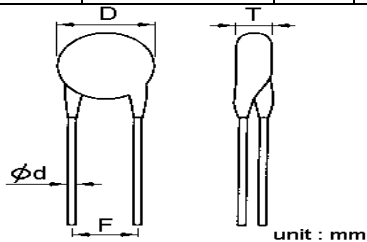
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**3. Part numbering/T.C/Capacitance/ Tolerance/Diameter :**

POE Part. No.		T.C.	Capacitance	Tolerance	Dimensions (unit : mm)			
SAP P/N	Old P/N (Refer to 3.1 Old P/N)				D (max)	T (max)	F	
							Bulk type	Taping type
CH0AC***C060*	AC06CH***C*	CH (NP0)	2, 3,4, 5(pF)	±0.25pF	7.0	5.0	7.5±1, 10±1	7.5±1
CH0AC***D060*	AC06CH***D*		6,7,8,9,10(pF)	±0.5pF	7.0			
CH0AC***J060*	AC06CH***J*		12,15(pF)	±5%	7.0			
CH0AC***J070*	AC07CH***J*		18,20,22, 24(pF)	±5%	8.0			
CH0AC***J080*	AC08CH***J*		27,30,33,(pF)	±5%	9.0			
CH0AC390J090*	AC09CH390J*		36,39(pF)	±5%	10.0			
CH0AC470J100*	AC10CH470J*		47(pF)	±5%	11.0			
SL0AC***J060*	AC06SL***J*	SL	10,12,15,18,20,22, 24,27,30,33, 36,39,47,50,51(pF)	±5%	7.0			
SL0AC***J070*	AC07SL***J*		56,62, 68,75(pF)	±5%	8.0			
SL0AC820J080*	AC08SL820J*		82pF	±5%	9.0			
SL0AC101J090*	AC09SL101J*		100pF	±5%	10.0			
YP0AC101K060*	AC06B101K*	Y5P	100 pF	±10%	7.0			0.60 +0.10 - 0.05
YP0AC151K080*	AC08B151K*		150 pF	±10%	9.0			
YP0AC221K080*	AC08B221K*		220 pF	±10%	9.0			
YP0AC331K080*	AC08B331K*		330 pF	±10%	9.0			
YP0AC471K060*	AC06B471K*		470 pF	±10%	7.0			
YP0AC561K080*	AC08B561K*		560pF	±10%	9.0			
YP0AC681K090*	AC09B681K*		680 pF	±10%	10.0			
YP0AC821K100*	AC10B821K*		820 pF	±10%	11.0			
YP0AC102K080*	AC08B102K*		1000 pF	±10%	9.0			
YU0AC102M060*	AC06E102M*	Y5U	1000 pF	±20%	7.0			7.5±1
YU0AC152M100*	AC100E152M*		1500 pF	±20%	11.0			
YU0AC222M080*	AC08E222M*		2200 pF	±20%	9.0			
YU0AC332M100*	AC10E332M*		3300 pF	±20%	11.0			
YU0AC392M120*	AC12E392M*		3900 pF	±20%	13.0			
YU0AC472M120*	AC12E472M*		4700 pF	±20%	13.0			
YV0AC102M060*	AC06F102M*	Y5V	1000 pF	±20%	7.0			7.5±1
YV0AC152M060*	AC06F152M*		1500 pF	±20%	7.0			
YV0AC222M060*	AC06F222M*		2200 pF	±20%	7.0			
YV0AC332M080*	AC08F332M*		3300 pF	±20%	9.0			
YV0AC392M110*	AC11F392M*		3900 pF	±20%	12.0			
YV0AC472M100*	AC10F472M*		4700 pF	±20%	11.0			
YV0AC682M120*	AC12F682M*		6800 pF	±20%	13.0			
YV0AC103M140*	AC14F103M*		10000 pF	±20%	15.0			



Graph (ex.)

\*\* The minimum thickness of coating  
(reinforced insulation) is 0.4mm.

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**3.1 Old P/N :**

(Ex.) A C 10 F 472 M L 7 F  
TPYE (1) (2) (3) (4) (5) (6) (7)

(1)Nominal body diameter dimension

(2)Temperature characteristic (identified code):

CH(NP0):0±60PPM/°C, SL: +350~-1000PPM/°C, B(Y5P):±10%,  
E(Y5U):-55% ~ +20%, F(Y5V) :-80% ~ +30%

(3)Capacitance (identified by 3-figure code)

(4)Capacitance tolerance (identified by code): C : ±0.25pF, D : ±0.5pF, J : ±5%, K : ±10%, M : ±20%

(5)Lead style (configuration) (identified by code) —

L: straight long lead

S: straight short lead

D: vertical kink lead

X: outside kink lead

B: inside kink lead

(6)Lead Space:

7=7.5 ± 1.0 mm

0=10 ± 1.0 mm

(7)Taping type or other code

Code	Pitch component
F	15.0 mm
M	25.4 mm
T	30.0 mm
No code	BULK

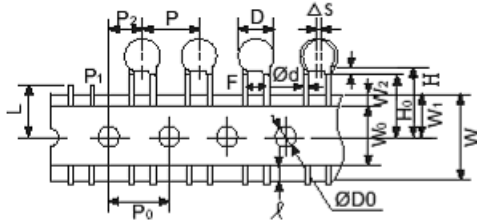
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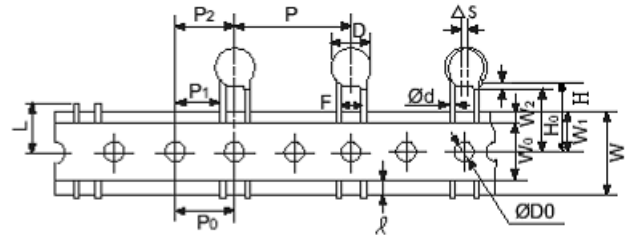
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## 4. Taping Format

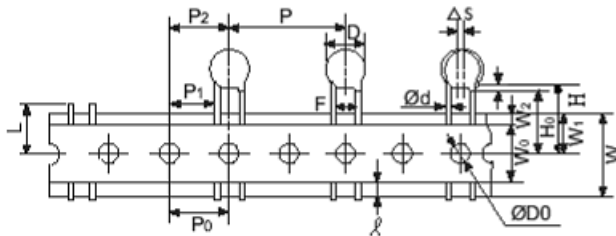
- 15mm pitch/lead spacing 7.5mm taping

Lead Code: **\*BAFD7** & **\*DAFD7** & **\*XAFD7**


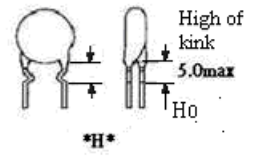
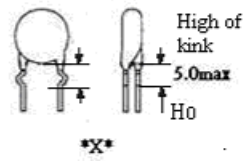
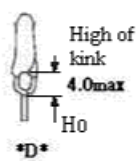
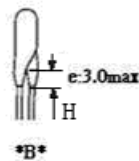
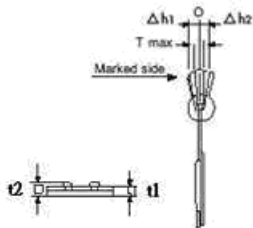
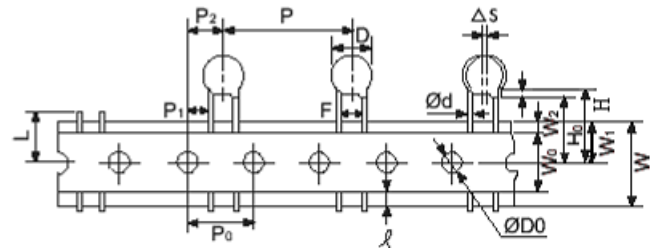
- 25.4mm pitch/lead spacing 10.0mm taping

Lead Code: **\*DAMD0** & **\*XAMD0** & **\*HAMD0** & **\*BAMD0**


- 30mm pitch/lead spacing 10.0mm taping

Lead Code: **\*DATD0** & **\*XATD0** & **\*HATD0** & **\*BATD0**


- 30mm pitch/lead spacing 7.5mm taping

Lead Code: **\*DATD7** & **\*XATD7** & **\*BATD7**





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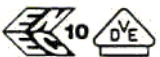








POE Part Number		*BATD0 *DATD0 *XATD0	*BAFD7	*DAFD7 *XAFD7	*BATD7 *DATD7 *HATD7 *XATD7	*BAMD0 *DAMD0 *HAMD0 *XAMD0
Item	Symbol	Dimensions (mm)	Dimensions (mm)	Dimensions (mm)	Dimensions (mm)	Dimensions (mm)
Pitch of component	P	30.0	15.0	15.0	30.0	25.4
Pitch of sprocket	P0	15.0±0.3	15.0±0.3	15.0±0.3	15.0±0.3	12.7±0.3
Lead spacing	F	10.0±1.0	7.5±1.0	7.5±1.0	7.5±1.0	10.0±1.0
Length from hole center to component center	P2	15.0±1.5	7.5±1.5	7.5±1.5	7.5±1.5	12.7 ± 1.5
Length from hole center to lead	P1	10.0±1.0	3.75±1.0	3.75±1.0	3.75±1.0	7.7±1.5
Body diameter	D	See the “3. Part numbering/T.C/Capacitance/ Tolerance/Diameter”				
Deviation along tape, life or right	△S	0±2.0				
Carrier tape width	W	18.0 +1/-0.5				
Position of sprocket hole	W1	9.0±0.5				
Lead distance between the kink and center of sprocket hole	H0	18.0+2.0/-0 For: *DATD0 *XATD0	---	18.0+2.0/-0	18.0+2.0/-0 For: *DATD7 *XATD7	18.0+2.0/-0 For: *DAMD0 *HAMD0 *XAMD0
Lead distance between the bottom of body and the center of sprocket hole	H	20.0+1.5/-1.0 For: *BATD0	20.0+1.5/-1.0	---	20.0+1.5/-1.0 For: *BATD7	20.0+1.5/-1.0 For: *BAMD0
Protrusion length	ℓ	2.0max (Or the end of lead wire may be inside the tape.)				
Diameter of sprocket hole	D0	4.0±0.2				
Lead diameter	φd	0.60 +0.1/-0.05				
Total tape thickness	t1	0.6±0.3				
Total thickness, tape and lead wire	t2	1.5 max.				
Deviation across tape	△h1	2.0 max.				
	△h2	2.0 max.				
Portion to cut in case of defect	L	11.0 max.				
Hole-down tape width	W0	11.5min				
Hole-down tape distortion	W2	1.5±1.5				
Coating extension on leads	e	3.0 max for straight lead style; Not exceed the kink leads for kink lead.				
Body thickness	T	See the “3. Part numbering/T.C/Capacitance/ Tolerance/Diameter”				

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## 5. Marking :

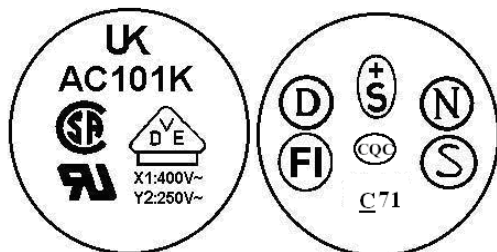
1. Type Designation	: AC
2. Nominal Capacitance	: 3-digit-system
3. Capacitance Tolerance	: C: $\pm 0.25\text{pF}$ , D: $\pm 0.5\text{pF}$ , J: $\pm 5\%$ , K: $\pm 10\%$ , M: $\pm 20\%$
4. Company Name Code	: <b>UK</b>
5. Manufactured Date	Abbreviation ex. <div style="display: flex; justify-content: space-around; align-items: center;"> <div style="text-align: center;"> Supplier of Epoxy ← <b>&lt;</b>  No. of test equipment ← <b>7</b> </div> <div style="text-align: center;"> <b>C</b>  ↓  Manufacture  C: Guangzhou  Halogen and Pb free </div> <div style="text-align: center;"> <b>0 1</b>  ↓  Last digit of year 2010  7: 2007  8: 2008  9: 2009  0: 2010  1: 2011  ... </div> <div style="text-align: center;"> Week of manufacture by month  <b>1</b> : → Month  1: January  ...  9: September  O: October  N: November  D: December </div> </div>

### 6. Approved Monogram

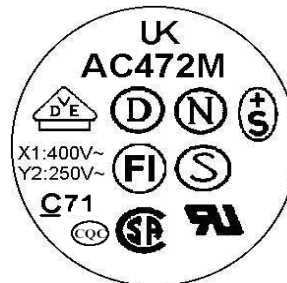
(1) VDE approval mark	 IEC 60384-14 3rd (2005). Class Code : X1 : 400V~ , Y2 : 250V~		
(2) UL approval mark		(7) FIMKO approval mark	
(3) CSA approval mark		(8) SEV approval mark	
(4) SEMKO approval mark		(9) CQC approval mark	
(5) NEMKO approval mark			
(6) DEMKO approval mark			

Ex.:

Two sides:



One side:



- \* Marking by the stamp or laser.
- \* The marking can be printed on either one side or two side of coating body.
- \* **C71**, Marked with code “ \_ ” stand for Halogen and Pb free ; No marked with “ \_ ” stand for Pb free.

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## 6. Scope

THIS SPECIFICATION APPLIES TO CERAMIC INSULATED CAPACITORS DISK TYPE USED IN ELECTRONIC EQUIPMENT.

- VDE/SEV/SEMKO/FIMKO/NEMKO/DEMKO/KEMA/UL/CSA recognized capacitor for Antenna coupling and AC line-by-pass.X1, Y2 Capacitor based on IEC 60384-14 3rd Edition (2005)  
“UL (AC250V), CSA recognized for across-the-line, line-by-pass” and antenna-isolation

## 2.Approval Standard and Recognized No.

Safety Standard	Standard No.	Subclass	w.v.	Recognized No.
UL	UL 1414	X,Y	250VAC	E146544
CSA	C22.2 NO.1-04	X,Y	250VAC	1363528(LR 92203-1)
VDE (ENEC)	IEC60384-14 (ed. 3) 2005	X1	400VAC	40001829
		Y2	250VAC	
SEV	IEC60384-14 (ed. 3) 2005	X1	400VAC	09.1153
		Y2	250VAC	
SEMKO	IEC60384-14 (ed. 3) 2005	X1	400VAC	600117
		Y2	250VAC	
FIMKO	IEC60384-14 (ed. 3) 2005	X1	400VAC	NCS/FI 24754
		Y2	250VAC	
NEMKO	IEC60384-14 (ed. 3) 2005	X1	400VAC	P09210633
		Y2	250VAC	
DEMKO	IEC60384-14 (ed. 3) 2005	X1	400VAC	314839-01
		Y2	250VAC	
KEMA	IEC60384-14 (ed. 3) 2005	X1	400VAC	2123149.01
		Y2	250VAC	
CQC	GB/T 14472-1998	X1	400VAC	CQC080010026519
		Y2	250VAC	
KTL	K60384-14	X1	400VAC	SU03017-4001A
		Y2	250VAC	SU03017-4002A

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## 7. Specification and test method

(1) Operating Temperature Range : -25 to +125°C (-25 to +85°C in case of the standard of UL / CSA)

(2) Performance Tests

Item			Specification		Testing Method													
1	Dielectric Strength	Between lead wires	No failure.		The capacitors shall not be damage when AC2600V are applied between the lead wires for 60 sec.													
		Body Insulation	No failure.		First the terminal of capacitor shall be connected together. Then a metal foil shall be closely wrapped around the body of the capacitor distance of about 3 to 4 mm from each terminal. Then the capacitor shall be inserted into a container filled with metal balls of about 1 mm diameter. Finally. AC2600V is applied for 60 sec. between the capacitor lead wires and metal balls.													
2	Insulation Resistance(I.R.)		10000MΩ min.		The insulation resistance shall be measured with 500±50VDC with 60±5sec. of charging.													
3	Capacitance		Within specified tolerance		B&E&F: The capacitance shall be measured at 20±2℃ with 1kHz±20% and 5V(rms.) or less. CH&SL: The capacitance shall be measured at 25℃ with 1MHz±20% and1.0±0.2Vrms													
4	Dissipation Factor(D.F.) or Q	Char.	Specification															
		B,E	D.F≤2.5%															
		F	D.F≤5.0%															
		CH,SL	Q: 30pF&above : ≥ 1000 Below 30PF : ≥ 400+20×C															
5	Temperature Characteristic	Char.	Capacitance Change		The capacitance measurement shall be made at each step specified in table 1. (Table 1) <table><tr><td>Step</td><td>Temperature</td></tr><tr><td>1</td><td>+20±2℃</td></tr><tr><td>2</td><td>-25±2℃</td></tr><tr><td>3</td><td>+20±2℃</td></tr><tr><td>4</td><td>+85±2℃</td></tr><tr><td>5</td><td>+20±2℃</td></tr></table> Pr-treatment : Capacitor shall be stored at 85±2℃ for 1 hour. Then placed at room condition for 1(※)24±2 hours before measurement		Step	Temperature	1	+20±2℃	2	-25±2℃	3	+20±2℃	4	+85±2℃	5	+20±2℃
		Step	Temperature															
		1	+20±2℃															
		2	-25±2℃															
		3	+20±2℃															
		4	+85±2℃															
5	+20±2℃																	
B	Within ± 10%																	
E	Within ± <sup>20</sup> <sub>5</sub> %																	
F	Within -80 ~ +30%																	
CH	0±60ppm/℃																	
SL	-1000~+350 ppm/℃																	
6	Robustness of Termination	Tensile	Lead wire shall not cut off capacitor shall not be broken.		With the termination in its normal position the specimen is held by its body in such a manner that the axis of the termination is vertical : the tensile force of 10N shall be applied to the termination in the direction of its axis and acting in a direction away from the body of the specimen.													
		Bending	Lead wire shall not cut off capacitor shall not be broken.		With the termination in its normal position the specimen is held by its body in such a manner that the axis of the termination is vertical : a mass applying a force of 5N is then suspended from the end of the termination. The body of the specimen is then inclined within a period of 2 to 3 sec., through an angle of approximately 90° in the vertical plane and then resumed to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.													

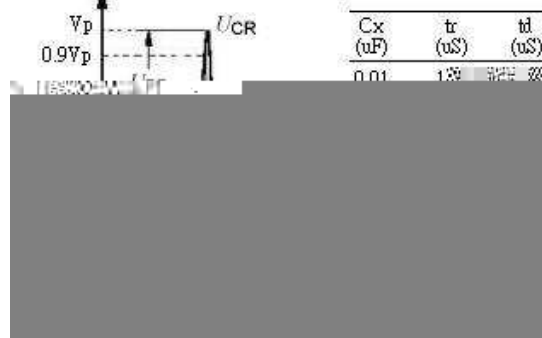
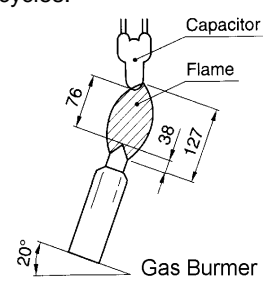
※ "room condition" temperature : 15~35°C, humidity : 45~75%, atmospheric pressure : 86~106kPa

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Item		Specification	Testing Method
7	Solderability of leads	Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	<p>The lead wire of capacitor should be dipped into molten solder for <math>2 \pm 0.5</math> sec.</p> <p>The depth of immersion is up to about 1.5 to 2.0 mm from the root of lead wires.</p> <p>Temp. of solder : Lead free solder ( Sn-3Ag -0.5Cu) <math>245 \pm 5^{\circ}\text{C}</math></p>
8	Soldering Effect (Non-Preheat)	Appearance	No marked defect
		I.R.	1000M $\Omega$ min.
		Dielectric Strength	Per Item 1.
		Capacitance	<p>B,E,F : Within <math>\pm 10\%</math> SL, CH : Within <math>\pm 2.5\%</math> or <math>\pm 0.25\text{pF}</math>, Whichever is large.</p> <div data-bbox="842 817 1109 987" data-label="Image"> </div> <p>Pre-treatment: Capacitor shall be stored at <math>85 \pm 2^{\circ}\text{C}</math> for 1 hour, then placed at <math>\text{**}^1</math> room condition for <math>24 \pm 2</math> hours before initial measurements.</p> <p>Post-treatment: Capacitor shall be stored for 1 to 2 hours at <math>\text{**}^1</math> room condition.</p>
	Soldering Effect (On-Preheat)	Appearance	No marked defect.
		I.R.	1000M $\Omega$ min.
		Dielectric Strength	Per Item 1.
		Capacitance	<p>B,E,F : Within <math>\pm 10\%</math> SL, CH : Within <math>\pm 2.5\%</math> or <math>\pm 0.25\text{pF}</math>, Whichever is large.</p> <div data-bbox="842 1462 1153 1632" data-label="Image"> </div> <p>Pre-treatment: Capacitor shall be stored at <math>85 \pm 2^{\circ}\text{C}</math> for 1 hour, then placed at <math>\text{**}^1</math> room condition for <math>24 \pm 2</math> hours before initial measurements.</p> <p>Post-treatment: Capacitor shall be stored for 1 to 2 hours at <math>\text{**}^1</math> room condition.</p>

Item			Specification	Testing Method			
9	Humidity (Under Steady State)	Appearance	No marked defect.	Set the capacitor for 500±12 hours at 40±2 °C, in 90% humidity. Then capacitor shall be stored for 1 to 2 hours at room condition.			
		Capacitance	B ≤ Within ±10% E ≤ Within ±20% F ≤ Within ±30% SL&CH: Within±2.5 =or ±0.25pF,Whichever is large.				
		D.F.	B,E ≤ 5.0% max. F ≤ 7.5% max.				
10	Humidity Loading	Q	SL&CH: Less than 30pF=> Q 100+10×C/3 More than 30pF=> Q 200	Apply the rated voltage for 500±12 hours at 40±2 °C to 95% humidity and set it for 1 to 2 hours at room condition.			
		I.R.	B,E,F ≤ 3000MΩ min. SL&CH: 1000MΩ min.				
11	Life	Appearance	No marked defect.	Impulse Voltage: Each individual capacitor shall be subjected to 15 impulses for three times. After the capacitors are applied for test.  Fig. The specimen capacitors are placed in a circulating oven for a period of 1000 hrs. The air in the oven is maintained at a temperature of 125±2 °C. Throughout the test. The capacitors are subjected to a 425Vrms alternating voltage of medium frequency. Except that once each hour the voltage is increased to 1000Vrms for 0.1sec.			
		Capacitance	B,E,F ≤ Within ±20% SL&CH: Within±3 =or ±0.3pF,Whichever is large.				
		I.R.	3000MΩ min. SL&CH: 1000MΩ min.				
		Dielectric Strength	Per Item 1.				
12	Flame Test	The capacitor flame discharge as follows		The capacitor shall subject to applied for 15 sec and then removed for 15 sec, until 5 cycles. Fig.  (Unit: mm)			
		<table><tr><th>Cycle</th><th>Time</th></tr><tr><td>1~4</td><td>30 sec, max.</td></tr><tr><td>5</td><td>60 sec, max.</td></tr></table>	Cycle		Time	1~4	30 sec, max.
Cycle	Time						
1~4	30 sec, max.						
5	60 sec, max.						

á "room condition" temperature 15~35: °C, humidity 45~75%, atmospheric pressure 86~106kPa

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Item		Specification	Testing Method
13	Active Flammability	The cheesecloth shall not be on fire.	<p>The specimens shall be individually wrapped in <del>at least</del> one but more than two complete layers of cheesecloth. <del>The specimens shall be subjected to 20 discharges. The interval between successive discharges shall be 5sec. The Uac shall be maintained for 2 min. after the last discharge.</del> Fig.</p> <p>C1,2š 1 f±10% C3š 0.03 f±5% 10KV L1-4 š 1.5Mh±20% 16A Rod core choke R š 100 ±2% Ctš 3 f±5% 10KV Uacš Ur±5% Urš Rated working voltage Cx š Capacitor F š Fuse, Rated 10A Ut š Voltage applied to Ct</p>
14	Passive Flammability	The burning time shall not be exceeded the time 30 sec. The tissue paper shall not ignite.	<p>The capacitor under test shall be held in the flame position which best promotes burning. Each specimen shall be exposed once to the flame. Time of exposure to flameš 30 sec Length of flameš 12±1 mm Gas burnerš Length 35 mm min. Inside Dia.š 0.5±0.1 mm Outside Diaš 0.9 mm max. Gas š Butane gas Purity 95% min. Fig.</p>

á "room condition" temperaturš 15~35: , humidityš 45~75%,atmospheric pressureš 86~106kPa

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## 8. Packing specificationš

Bulk š

When dimension 13” Bulk Packaging is 500pcs/bag.

When dimension 13 ” Bulk Packaging is 200pcs/bag.

Tapingš 1000pcs/box



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## 9. Notices:

### 9.1 Caution(Rating):

#### (1). Operating Voltage

Be sure to maintain the  $V_{p-p}$  value of the applied voltage or the  $V_{o-p}$  which contains DC bias within the rated voltage range.

When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing this irregular voltage.

#### (2). Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or the like, it may have the self-generated heat due to dielectric-loss.

Applied voltage should be the load such as self-generated heat is within  $20^{\circ}\text{C}$  on the condition of atmosphere temperature  $25^{\circ}\text{C}$ . When measuring, use a thermocouple of small thermal capacity-K of  $\phi 0.1\text{mm}$  and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability.

#### (3). Test condition for withstanding Voltage

##### *I. Test Equipment*

Test equipment for AC withstanding voltage shall be used with the performance of the wave similar to 50/60 Hz sine waves.

If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.

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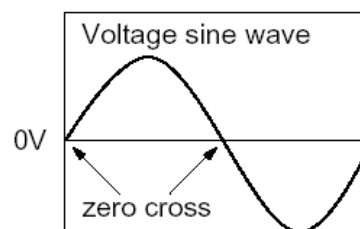
## II. Voltage Applied Method

When the withstanding voltage is applied, capacitor's lead or terminal shall be firmly connected to the output of the withstanding voltage test equipment, and then the voltage shall be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the \*zero cross. At the end of the test time, the test voltage shall be reduced to near zero, and then capacitor's lead or terminal shall be taken off the output of the withstanding voltage test equipment.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.

ZERO CROSS is the point where voltage sine wave pass 0V.- See the right figure.



### (4). Fail-Safe

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

**Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.**

## 9.2 Caution (Storage and operating condition):

### Operating and storage environment

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40 degrees centigrade and 15 to 85 %. Use capacitors within 6 months.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

## 9.3 Caution (Soldering and Mounting):

### 9.3.1 Vibration and impact:

Do not expose a capacitor or its leads to excessive shock or vibration during use.

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### 9.3.2 Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 degrees C. max.

Soldering iron wattage: 50W max.

Soldering time: 3.5 sec. max.

### 9.3.3 Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time: 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

## 9.4 Caution (Handling):

Vibration and impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."