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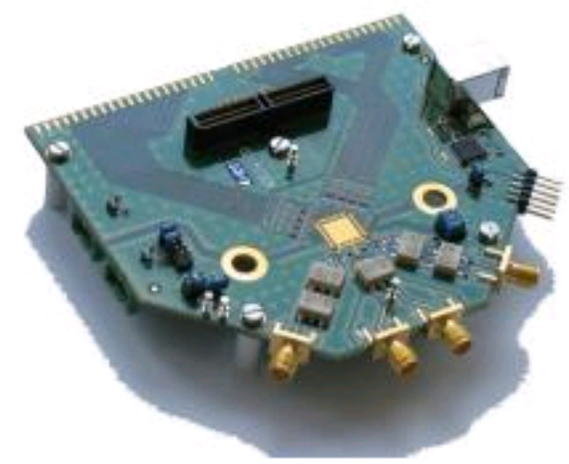
ADC1212D065F2
ADC1212D065F2 demo board; LVDS/DDR version

IDT's dual-channel ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVDS/DDR output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance.



Features

- SMA connector for clock and analog inputs signal
- LVDS/DDR output

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ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
ADC1212D065F2-DB	Active New	BD0	BOARD	0	C	N	Check Inventory

Documents

	TITLE	TYPE	FORMAT	FILE SIZE	DATE
	Demo Board Software ADC_1_r04	Manual - Software	ZIP	2.33 MB	07/19/2012

Related Products

PRODUCT ID	TITLE
ADC1212D065HN	Dual 12-bit ADC; 65 Msps;

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